

承 認 書

SPECIFICATION FOR APPROVAL

CUSTOMER:	鹿鸣
CUSTOMER P/N	
PART NO:	
DESCRIPTION:	SMD POWER INDUCTORS
PRODUCTS NO:	CYSCM9070FTL-SERIES
PRODUCTS REV:	1
DATE:	2018-7-19

PURCHASER CONFIRMED		
REMARK		

PROVIDER ENGINEER DEPT.		
APPROVAL BY	CHECK BY	DRAWN BY
		<i>chenlinli</i>



TAIPEI OFFICE

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CHINA FACTORY

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CHINA FACTORY

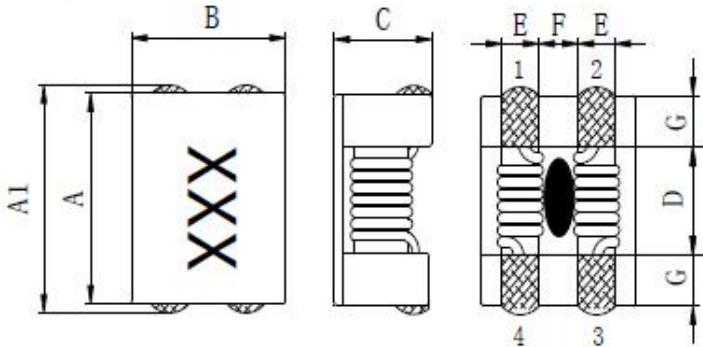
昆山誠陽電子有限公司
KUNSHAN CHENG YANG ELECTRONICSCO.,LTDP
Qiang-An Road., High-Tech. Industrial Park, Kunshan City, Jiangsu Province,
江蘇省昆山市高科技工業園區強安路38號
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TEST DATA

DIMENSION&ELECTRIC CHARACTER

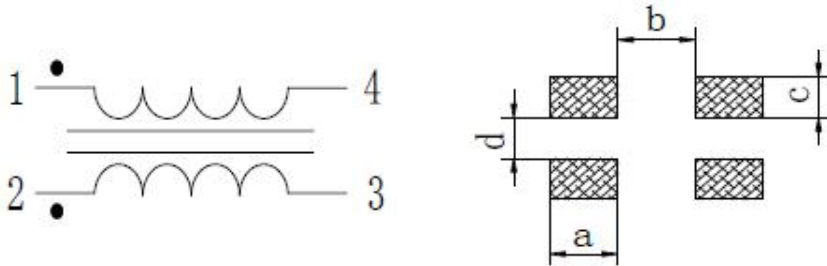
CUSTOMER :	鹿鸣	PART NO.:	
COMER :	SMD INDUCTOR	SERIES NO:	CYSCM9070FTL-SERIES

EXTERNAL DIMENSIONS

UNIT: mm


A	9.0±0.5
A1	9.5±0.6
B	7.0±0.5
C	4.8MAX
D	5.7REF
E	1.5±0.5
F	2.0±0.5
G	1.7±0.5
a	3.00
b	5.00
c	2.20
d	1.80

RECOMMEND LAND PATTERN DIMENSIONS



Part NO.	Impedance (Ω)	DCR (mΩ)	Rated Current (A)	Test Freq	MARKING
CYSCM9070FTL-301	225Min (300TYP)	6 MAX	6 MAX	100MHz	301
CYSCM9070FTL-501	450Min (600TYP)	8 MAX	6 MAX	100MHz	501
CYSCM9070FTL-701	500Min (700TYP)	10 MAX	5 MAX	100MHz	701
CYSCM9070FTL-102	750Min (1000TYP)	13 MAX	4 MAX	100MHz	102
CYSCM9070FTL-222	1700Min (2200TYP)	60 MAX	2.5 MAX	100MHz	222
CYSCM9070FTL-272	2000Min (2700TYP)	65 MAX	2.0 MAX	100MHz	272
CYSCM9070FTL-302	2500Min (3000TYP)	70 MAX	1.9 MAX	100MHz	302

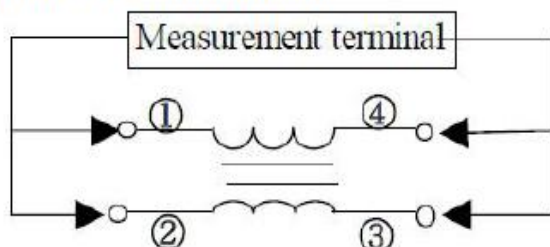
Rated Current: $\Delta T \leq 40^{\circ}\text{C}$ Typ

NOTE:

Operating temperature: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$

storage : 温度: $0^{\circ}\text{C} \sim +40^{\circ}\text{C}$ 湿度: RH10%~70%

Impedance TEST EQUIPMENT



APPROVED BY: Vincent

CHECKED BY: Yasir

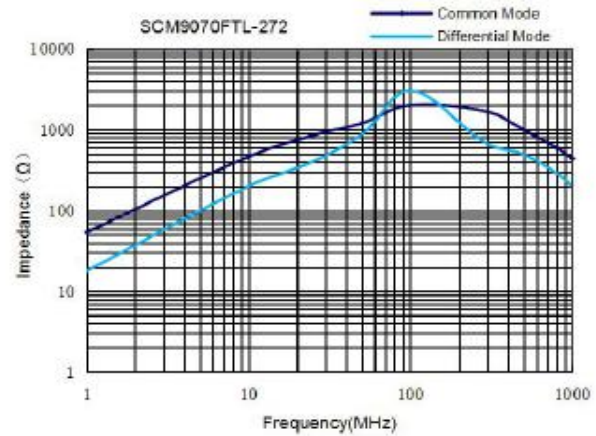
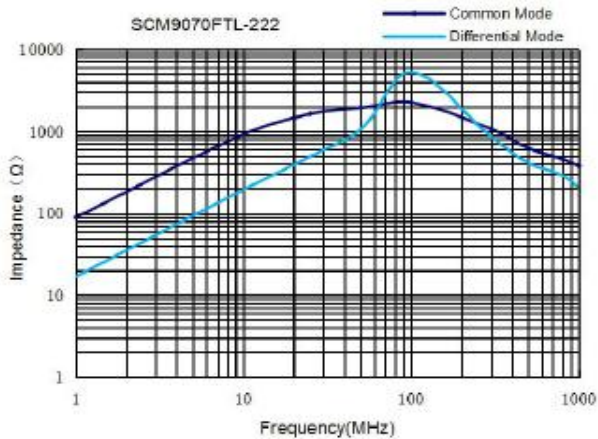
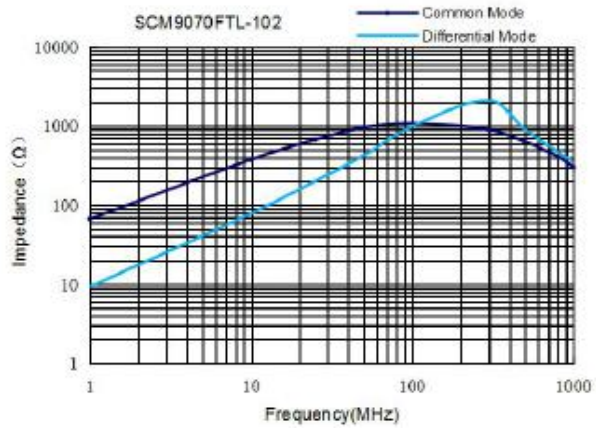
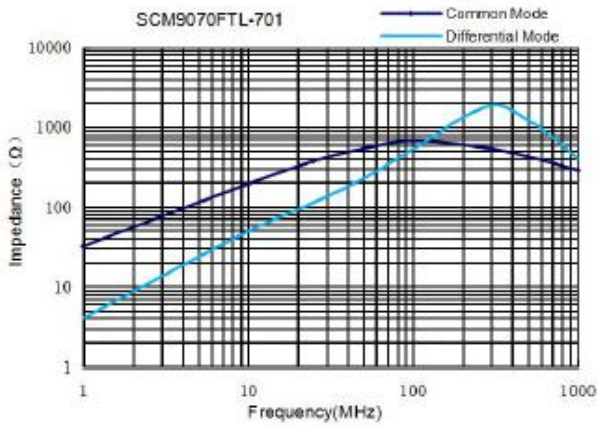
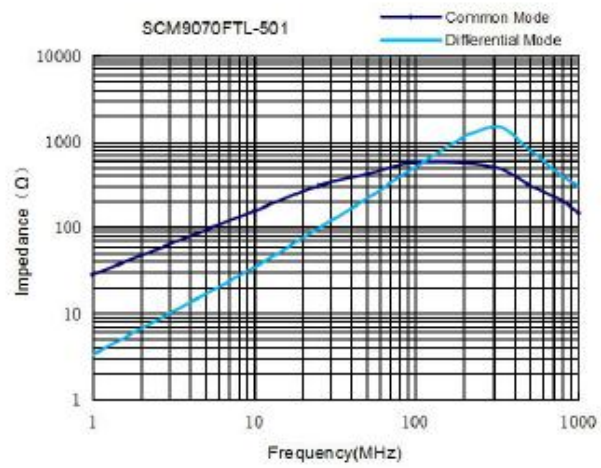
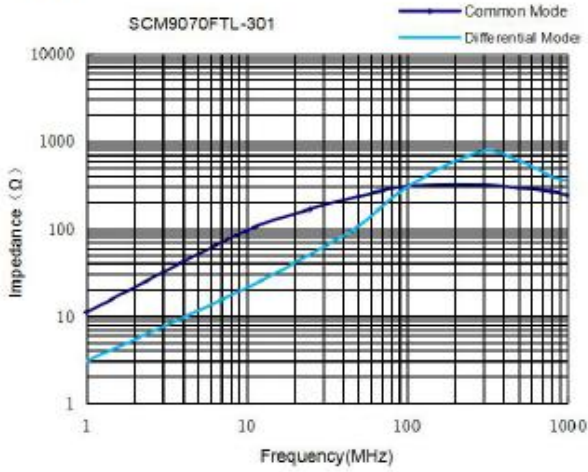
DRAWN BY: chenlinli

TEST DATA

DIMENSION&ELECTRIC CHARACTER

CUSTOMER:	鹿鸣	PART NO.:	
TOMER :	SMD INDUCTOR	SERIES NO:	CYSCM9070FTL-SERIES

Curve



APPROVED BY	CHECKED BY	PREPARED BY
Vincent	Yasir	chenlinli

TEST DATA

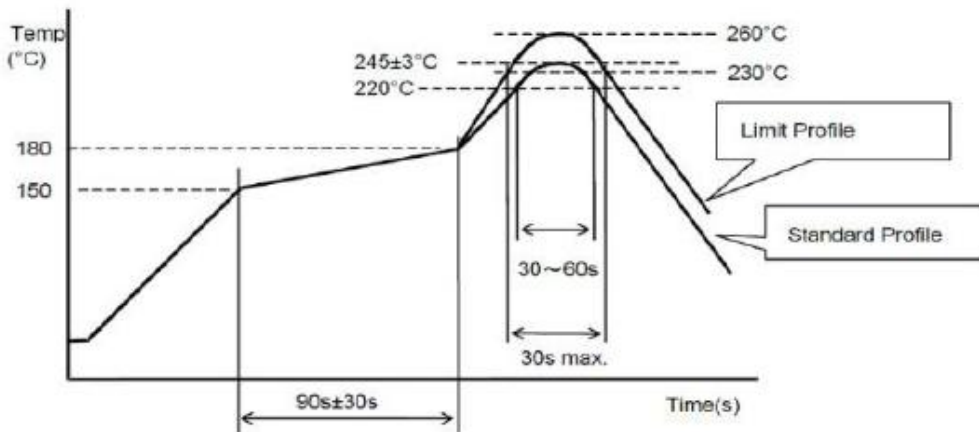
DIMENSION&ELECTRIC CHARACTER

CUSTOMER :	鹿鸣	PART NO.:	
TOMER :	SMD INDUCTOR	SERIES NO:	CYSCM9070FTL-SERIES

Material List

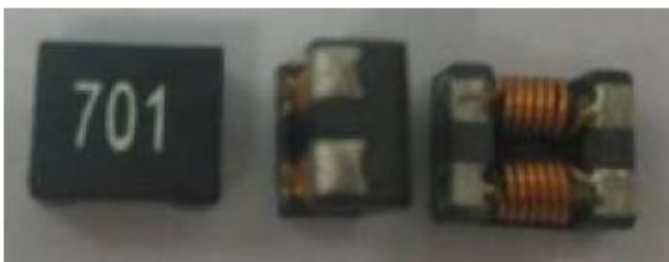
No.	Item	Material	Specification	Supplier	UL
a	Core	Ferrite core	I CORE	SINCORE OR EQU	
b	Wire	Enamelled copper wire	G1P180	ELEKTRISOA OR EQU	E258243
c	Base	Plastic	CS-SCM9070-DY	DY OR EQU	
d	Adhesive	Epoxy resin	ST-500	ST OR EQU	
e	Terminal	Sn /Cu	Sn99.3:Cu0.7	THOUSAND OR EQU	

Recommended Soldering Temperature Graph

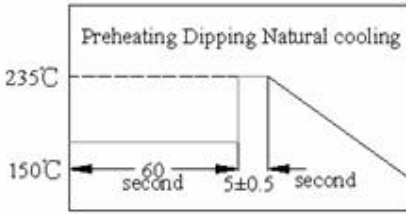
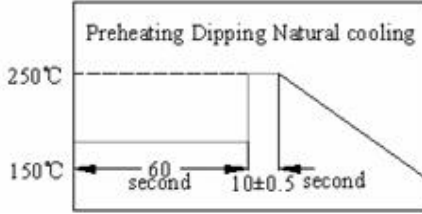
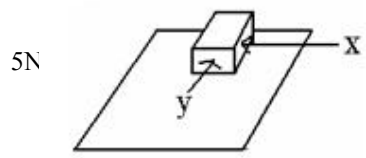


	Standard Profile	Limit Profile
Pre-heating	150~180°C、90s±30s	
Heating	above 220°C、30s-60s	above 240°C、30s max
Peak temperature	245°C±3°C	260°C、10s
Cycle of reflow	2 times	2 times

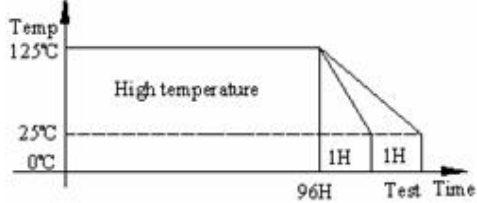
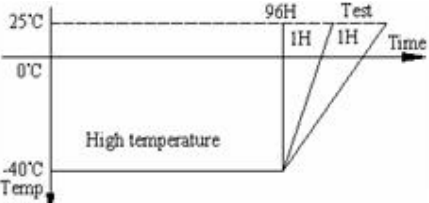
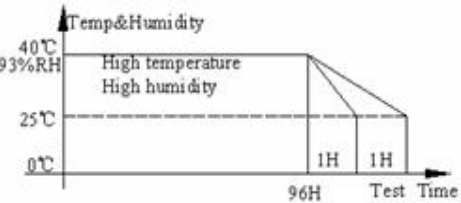
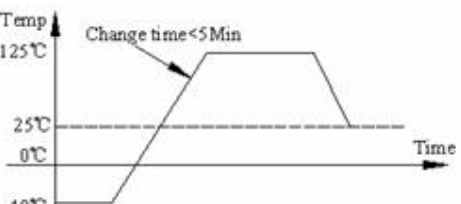
Product photos



APPROVED BY	CHECKED BY	PREPARED BY
Vincent	Yasir	chenlinli

Operation Temperature	-40°C to +125°C (Includes temperature when the coil is heated)
External Appearance	On visual inspection, the coil has no external defects.
Solder Ability Test	<p>More than 90% of terminal electrode should be covered with solder.</p> <p>1 After fluxing, component shall be dipped in a melted solder bath at 235°C ± 5°C for 5 ± 0.5 seconds.</p> 
Heat endurance of Soldering	<p>1. Components should have not evidence of electrical and mechanical damage.</p> <p>2. Inductance: within ±10% of initial value.</p> <p>3. Impedance: within ±10% of initial value.</p> <ul style="list-style-type: none"> ● Preheat: 150 ± 5°C 60 seconds. ● Solder temperature: 250 ± 5°C. ● Flux: rosin. ● Dip time: 10 ± 0.5 seconds. 
Terminal Strength	<p>After soldering of X,Y withstanding at below conditions .The terminal should not Peel off. (Refer to figure at below)</p> 
Insulating Resistance	Over 100MΩ at 100V D.C. between coil and core.
Dielectric Strength	No dielectric breakdown at 30V D.C. for 1 minute between coil and core.
VibrationTest	Inductance deviation within +10% after vibration for 1 hour. In each of three orientations at sweep vibration(10~55~10HZ)with 1.5mmP-P amplitudes
Drop test	Inductance deviation within +10% after being dropped once with 981m/s ² (100G) shock Attitude upon a rubber block method shock testing machine, in three different orientations
<p>v Application Notice/Handling</p> <p>1. Storage Conditions To maintain the solder ability of terminal electrodes: (1) Temperature and humidity conditions: less than 40°C and 70% RH. (2) Products should be used within 6 months. (3) The packaging material should be kept where no chlorine or sulfur exists in the air.</p> <p>2. Handling (1) Do not touch the electrodes(soldering terminals)with fingers as this may lead to deterioration of solderability. (2) The use of tweezers or vacuum pick-ups is strongly recommended for individual components. (3) Bulk handling should ensure that abrasion and mechanical shock are minimized.</p>	

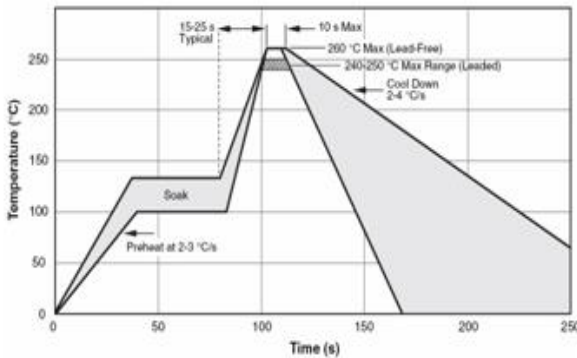
GENERAL CHARACTERISTICS

TEST	Required Characteristics	Test Method/Condition
<p>High Temperature Storage Test</p> <p>Reference documents: MIL-STD-202G Method108A</p>	<p>1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$</p>	 <p>Temperature: $125^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Time: 96 ± 2 hours. Tested not less than 1 hour, nor more than 2 hours at room.</p>
<p>Low Temperature Storage Test</p> <p>Reference documents: IEC 68-2-1A 6.1 6.2</p>	<p>1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$</p>	 <p>Temperature: $-40^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Time: 96 ± 2 hours. Tested not less than 1 hour, nor more than 2 hours at room.</p>
<p>Humidity Test</p> <p>Reference documents: MIL-STD-202G Method103B</p>	<p>1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$</p>	 <p>1. Dry oven at a temperature of $40^{\circ}\text{C} \pm 2^{\circ}\text{C}$ for 96hours 2. Measurements At the end of this period 3. Exposure: Temperature: $40^{\circ}\text{C} \pm 2^{\circ}\text{C}$. Humidity: 93 ± 2hoys. 4. Tested while the chamber. 5. Tested not less than 1 hour. Nor more than 2 hours at room temperature.</p>
<p>Thermal Shock Test</p> <p>Reference documents: MIL-STD-202G Method107G</p>	<p>1. No case deformation or change in appearance 2. $\Delta L/L \leq 10\%$ 3. $\Delta Q/Q \leq 30\%$ 4. $\Delta DCR/DCR \leq 10\%$</p>	 <p>First -40°C for 30 Minutes, last 125°C for 30 Minutes as 1 cycle. Go through 20 cycles.</p>

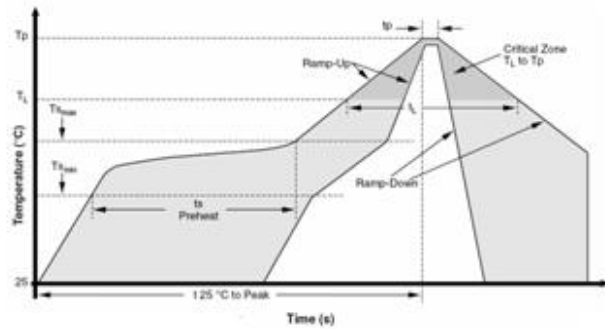
Application Notice/Handling

- (1) Temperature and humidity conditions : less than 40°C and 70% RH.
- (2) Products should be used within 6 months.
- (3) The packaging material should be kept where no chlorine or sulfur exists in the air.
- (4) Do not touch the electrodes (soldering terminals) with fingers as this may lead to deterioration of solder ability
- (5) The use of tweezers or vacuum pick-ups is strongly recommended for individual components.
- (6) Bulk handling should ensure that abrasion and mechanical shock are minimized.

TYPICAL WAVE SOLDER PROFILE FOR LEADED AND LEAD-FREE THROUGH-HOLE PACKAGES



TYPICAL IR REFLOW PROFILE FOR LEADED AND LEAD-FREE SURFACE MOUNT PACKAGES



IPC/JEDEC J-STD-020C, Figure 5-1

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average Ramp-Up Rate (Ts _{max} to Tp)	3 °C/second max.	3 °C/second max.
Preheat		
± Temperature Min (Ts _{min})	100 °C	150 °C
± Temperature Max (Ts _{max})	150 °C	200 °C
± Time (ts _{min} to ts _{max})	60-120 seconds	60-180 seconds
Time maintained above:		
± Temperature (T _l)	183 °C	217 °C
± Time (t _l)	60-150 seconds	60-150 seconds
Peak/Classification Temperature (Tp)	See Table 4.1	See Table 4.2
Time within 5 °C of actual Peak Temperature (tp)	10-30 seconds	20-40 seconds
Ramp-Down Rate	6 °C/second max.	6 °C/second max.
Time 25 °C to Peak Temperature	6 minutes max.	8 minutes max.

Table 4. Classification Reflow Profiles (per IPC/JEDEC J-STD-020C, Table 5.2)

Note 1: All temperatures refer to top side of the package, measured on the package body surface.

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	240 +0/-5 °C	225 +0/-5 °C
≥2.5 mm	225 +0/-5 °C	225 +0/-5 °C

Table 5. SnPb Eutectic Process – Package Peak Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.1)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 + 0 °C *	260 + 0 °C *	260 + 0 °C *
1.6 mm - 2.5 mm	260 + 0 °C *	250 + 0 °C *	245 + 0 °C *
≥2.5 mm	250 + 0 °C *	245 + 0 °C *	245 + 0 °C *

* Tolerance: Process compatibility is up to and including the stated classification temperature (this means Peak reflow temperature + 0 °C. For example 260 °C + 0 °C) at the rated MSL level.

Table 6. Pb-free Process – Package Classification Reflow Temperatures (per IPC/JEDEC J-STD-020C, Table 4.2)

Note 1: The profiling tolerance is + 0 °C, -X °C (based on machine variation capability) whatever is required to control the profile process but at no time will it exceed -5 °C. Process compatibility at the peak reflow profile temperatures as defined in Table 4.2.

Note 2: Package volume excludes external terminals (balls, bumps, lands, leads) and/or nonintegral heat sinks.

Note 3: The maximum component temperature reached during reflow depends on package thickness and volume. The use of convection reflow processes reduces the thermal gradients between packages. However, thermal gradients due to differences in thermal mass of SMD packages may still exist.

Note 4: Components intended for use in a "lead-free" assembly process shall be evaluated using the "lead-free" classification temperatures and profiles defined in Tables 4.1, 4.2 and 5.2 whether or not lead free.

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