

Product Summary

V_{DS}	1200 V
$I_D (T_c=25^\circ\text{C})$	36 A
$R_{DS(on),typ}$	65 mΩ@ $V_{GS}=18\text{V}$

Features

- Low On-Resistance with High Blocking Voltage
- Low Capacitance
- Avalanche Ruggedness
- Halogen Free, Rohs Compliant

Benefits

- High Frequency Operation
- Enabling Higher Switching Frequency
- Increased Power Density
- Reduction of Heat Sink Requirements

Applications

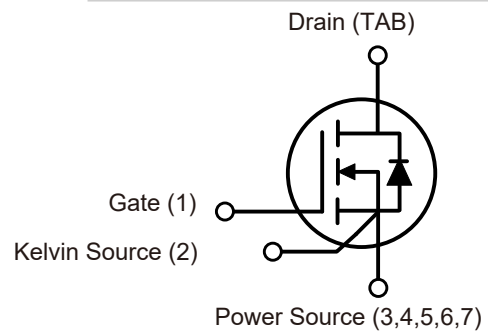
- Switch Mode Power Supplies (SMPS)
- Power Inverter & Solar Inverter
- Motor Drivers & EV Charging Station
- DC/DC Converter

Package Pin Definitions

- TAB - Drain
- Pin1 - Gate
- Pin2 - Kelvin Source
- Pin3,4,5,6,7 - Power Source

Package Parameters

Part Number	Marking	Package
B2M065120R	B2M065120R	TO-263-7

Package: TO-263-7


Maximum Ratings

Symbol	Parameter	Test conditions	Value	Unit
V_{DSmax}	Drain-Source Voltage	$V_{GS}=0V, I_D=100\mu A$	1200	V
$V_{GSmax}^{1)}$	Gate-Source Voltage		-8/22	V
V_{GSop}	Recommend Gate-Source Voltage		-4/18	V
I_D	Continuous Drain Current	$V_{GS}=18V, T_C=25^\circ C$	36	A
		$V_{GS}=18V, T_C=100^\circ C$	24	A
$I_{D,pulse}$	Pulsed Drain Current	Pulse with t_p limited by T_{jmax}	85	A
P_{tot}	Power Dissipation	$T_C=25^\circ C, T_j=175^\circ C$	150	W
T_j	Operating Junction Temperature		-55~175	$^\circ C$
T_{stg}	Storage Temperature		-55~175	$^\circ C$

1) Note: When using MOSFET Body Diode $V_{GSmax}=-4/22V$

Electrical Characteristics (Defined at $T_j=25^\circ C$ unless otherwise specified)
Static Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=100\mu A$	1200			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=5mA$	2.3	2.8	3.5	V
		$V_{GS}=V_{DS}, I_D=5mA, T_j=175^\circ C$		1.9		
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=18V, V_{DS}=0V$			100	nA
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=1200V, V_{GS}=0V$		1	50	μA
		$V_{DS}=1200V, V_{GS}=0V, T_j=175^\circ C$		10	200	
$R_{DS(on)}$	Drain-Source On-State Resistance	$V_{GS}=18V, I_D=20A$		65	90	m Ω
		$V_{GS}=18V, I_D=20A, T_j=175^\circ C$		110		
g_{fs}	Transconductance	$V_{DS}=10V, I_D=20A$		8.5		S

Thermal Characteristics

Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(jc)}$	Thermal Resistance from Junction to Case		1.0		K/W

AC Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=800V$ $f=1MHz, V_{AC}=25mV$		1300		pF
C_{oss}	Output Capacitance			75		pF
C_{rss}	Reverse Transfer Capacitance			3		pF
$R_{G(int)}$	Internal Gate Resistance	$f=1MHz, V_{AC}=25mV$		1.8		Ω

Gate Charge Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
Q_{GS}	Gate to Source Charge	$V_{DS}=800V$ $I_D=20A$ $V_{GS}=-4/+18V$		18		nC
Q_{GD}	Gate to Drain Charge			30		nC
Q_G	Total Gate Charge			60		nC

Switching Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-On Delay Time	$V_{DC}=800V, V_{GS}=-4/18V$ $I_D=20A, R_{G(ext)}=8.2\Omega$ $L_\sigma=80nH, T_j=25^\circ C$ diode: body diode at $V_{GS}=-4V$ Inductive Load Eon includes diode reverse recovery		8		ns
t_r	Rise Time			30		ns
$t_{d(off)}$	Turn-Off Delay Time			21		ns
t_f	Fall Time			12		ns
E_{on}	Turn-On Energy			290		uJ
E_{off}	Turn-Off Energy			80		uJ
$t_{d(on)}$	Turn-On Delay Time	$V_{DC}=800V, V_{GS}=-4/18V$ $I_D=20A, R_{G(ext)}=8.2\Omega$ $L_\sigma=80nH, T_j=175^\circ C$ diode: body diode at $V_{GS}=-4V$ Inductive Load Eon includes diode reverse recovery		8		ns
t_r	Rise Time			30		ns
$t_{d(off)}$	Turn-Off Delay Time			24		ns
t_f	Fall Time			12		ns
E_{on}	Turn-On Energy			310		uJ
E_{off}	Turn-Off Energy			90		uJ

Reverse Diode Characteristics

Symbol	Parameter	Test conditions	Value			Unit
			Min.	Typ.	Max.	
V_{SD}	Diode Forward Voltage	$V_{GS}=-4V, I_{SD}=10A, T_j=25^\circ C$		4.4		V
		$V_{GS}=-4V, I_{SD}=10A, T_j=175^\circ C$		3.7		
I_{SD}	Continuous Diode Forward Current	$V_{GS}=-4V, T_C=25^\circ C$			27	A
$I_{SD,pulse}$	Pulse Diode Current	$V_{GS}=-4V$, pulse width t_p limited by T_{jmax}		90		A
t_{rr}	Reverse Recovery Time	$V_{DC}=800V, I_{SD}=20A$ $-di_F/dt=2200A/\mu s$ $T_j=25^\circ C$		15		ns
Q_{rr}	Reverse Recovery Charge			155		nC
I_{rrm}	Peak Reverse Recovery Current			17		A
t_{rr}	Reverse Recovery Time	$V_{DC}=800V, I_{SD}=20A$ $-di_F/dt=2200A/\mu s$ $T_j=175^\circ C$		20		ns
Q_{rr}	Reverse Recovery Charge			432		nC
I_{rrm}	Peak Reverse Recovery Current			32		A

Typical Performance

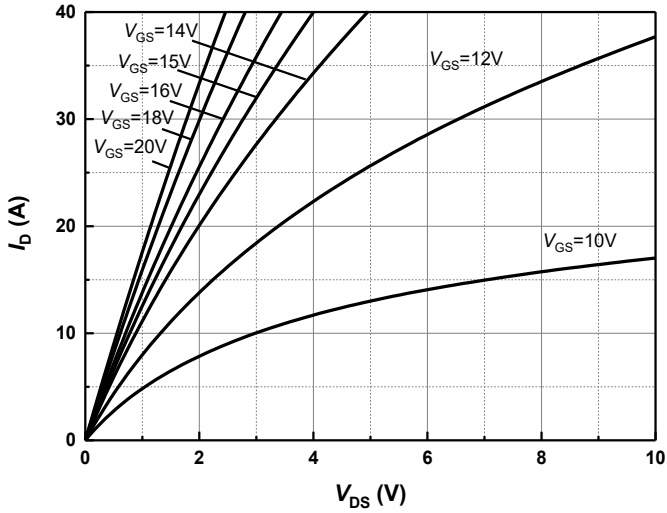


Figure 1 Typical Forward Output Characteristics at $T_j = 25^\circ\text{C}$

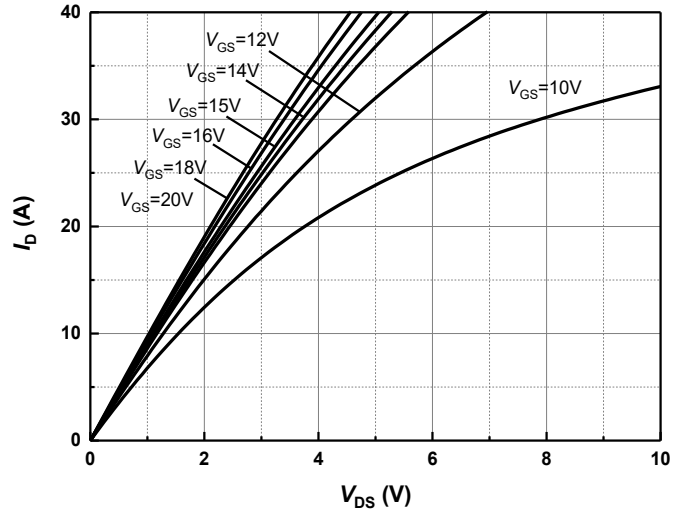


Figure 2 Typical Forward Output Characteristics at $T_j = 175^\circ\text{C}$

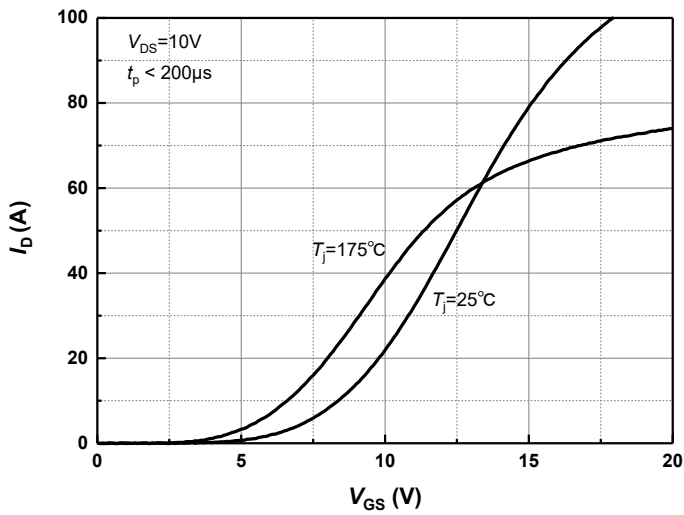


Figure 3 Transfer Characteristics for Various Temperature

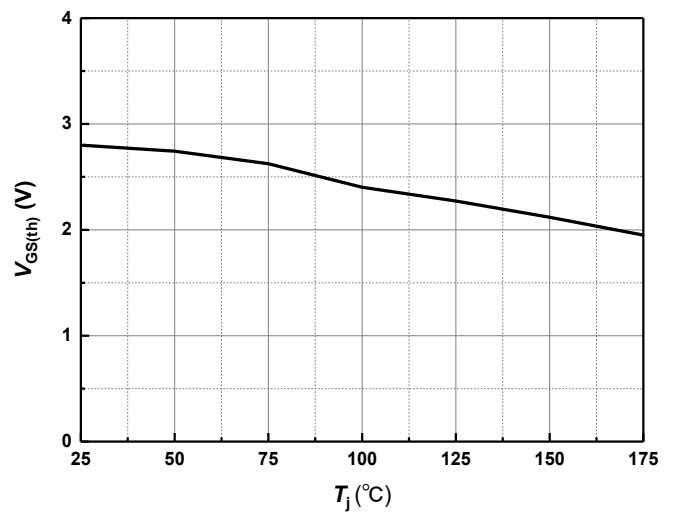


Figure 4 Threshold Voltage for Various Temperature

Typical Performance

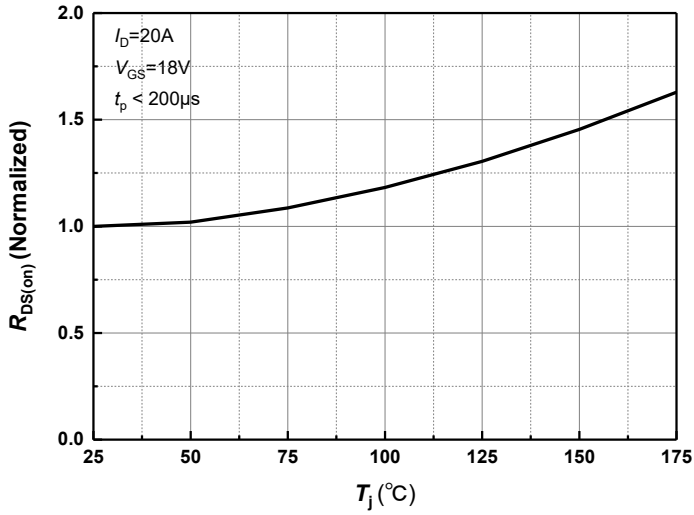


Figure 5 Normalized On-Resistance for Various Temperature

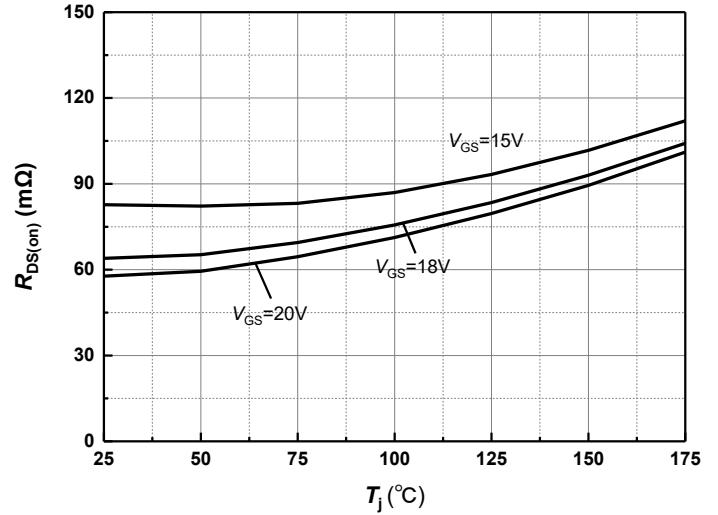


Figure 6 On-Resistance vs. Temperature for Various Gate-Source Voltage

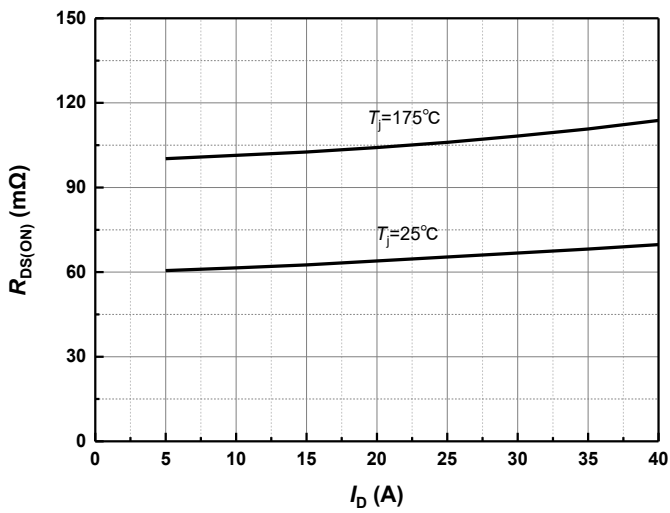


Figure 7 On-Resistance vs. Drain Current for Various Temperature

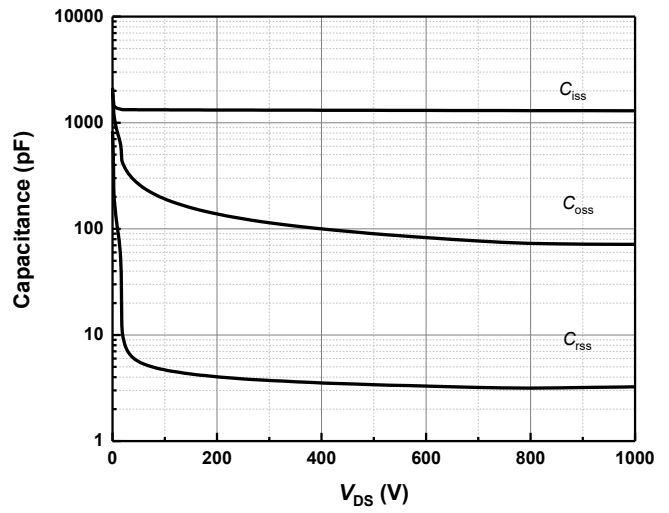


Figure 8 Capacitance vs. Drain-Source Voltage (0 - 1000V)

Typical Performance

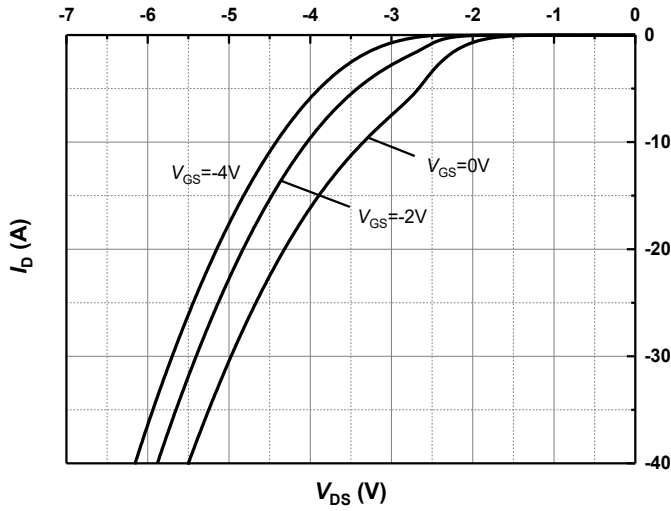


Figure 9 Body Diode Characteristics at $T_j=25^{\circ}\text{C}$

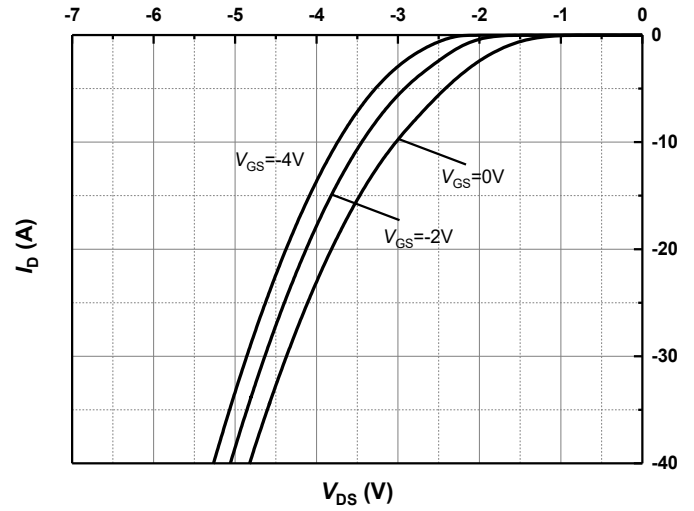


Figure 10 Body Diode Characteristics at $T_j=175^{\circ}\text{C}$

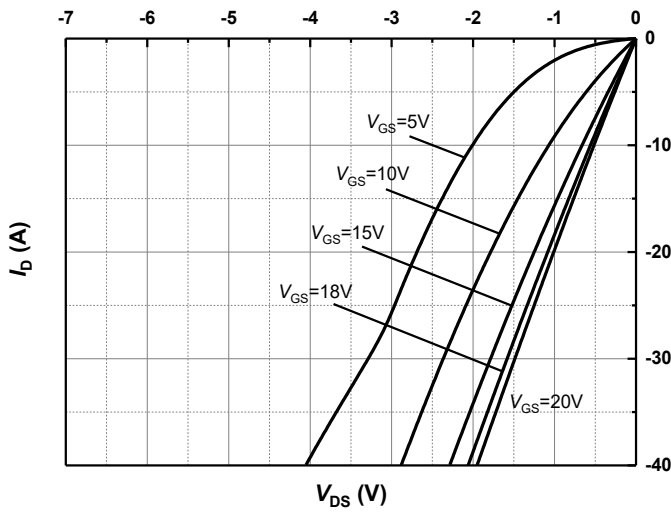


Figure 11 3rd Quadrant Characteristics at $T_j=25^{\circ}\text{C}$

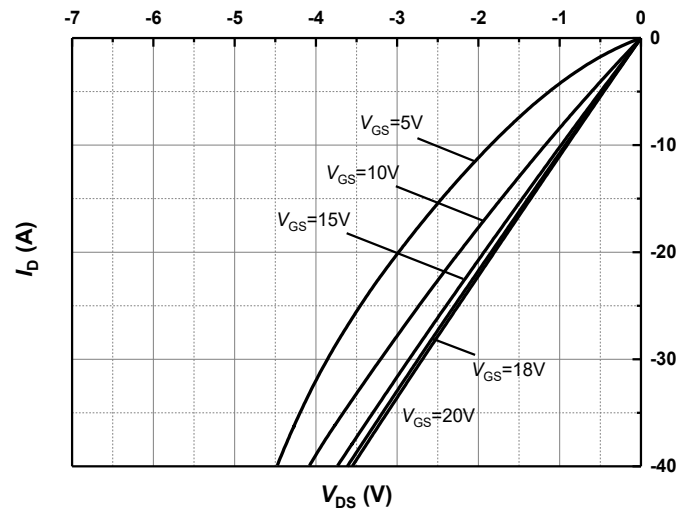


Figure 12 3rd Quadrant Characteristics at $T_j=175^{\circ}\text{C}$

Typical Performance

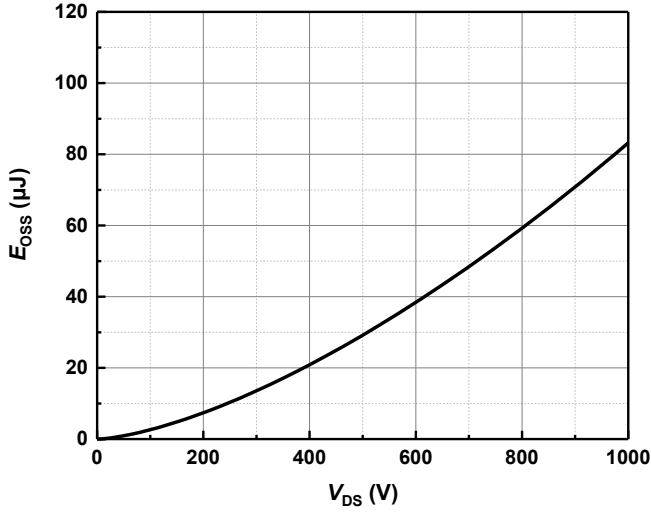


Figure 13 Output Capacitor stored Energy

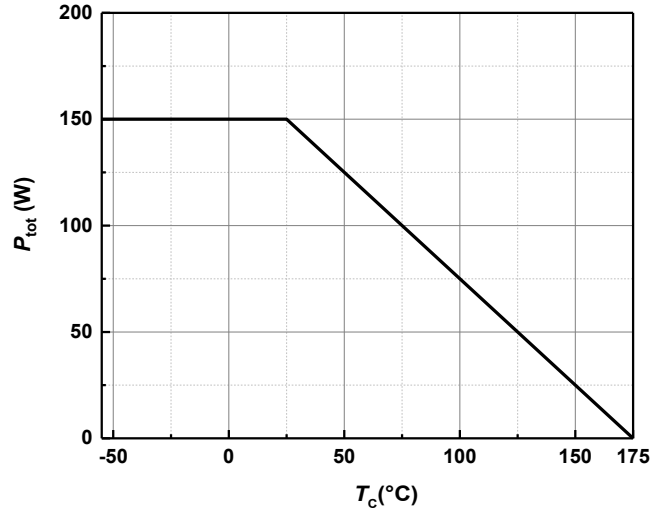


Figure 14 Maximum Power Dissipation Derating vs. Case Temperature

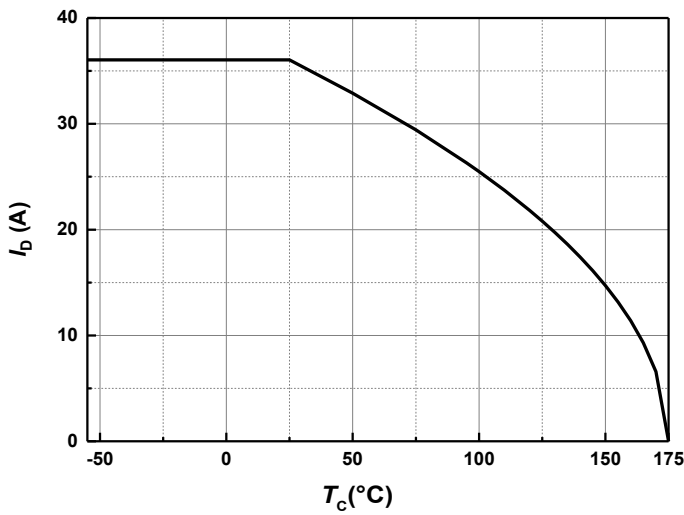


Figure 15 Continuous Drain Current Derating vs. Case Temperature

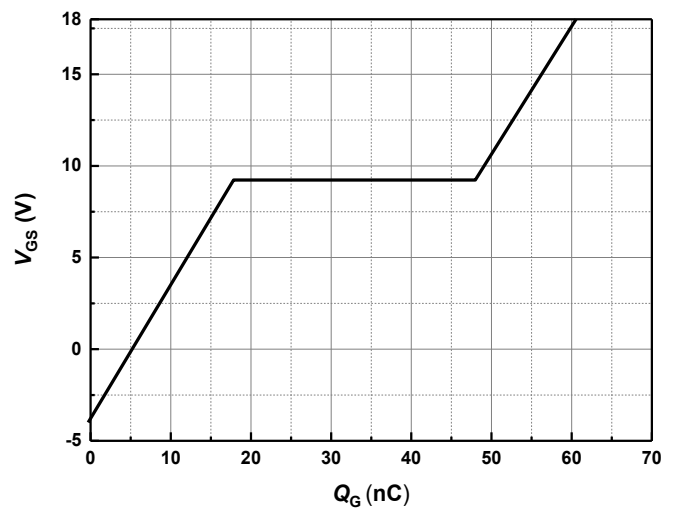


Figure 16 Gate Charge Characteristics

Typical Performance

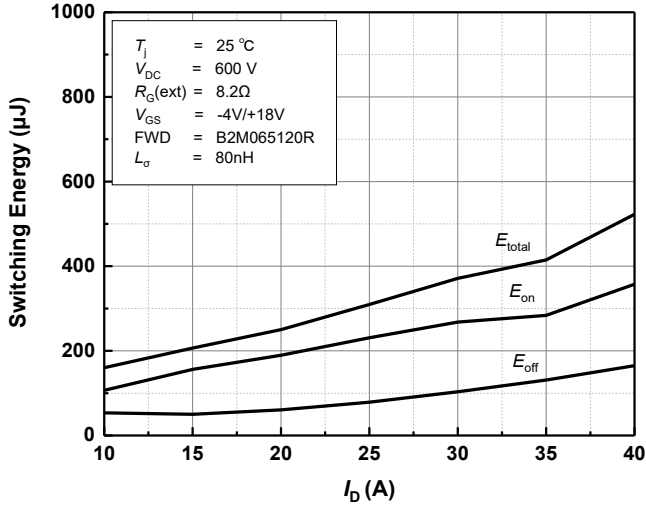


Figure 17 Clamped Inductive Switching Energy vs. Drain Current ($V_{DC} = 600\text{V}$) at $T_j = 25^\circ\text{C}$

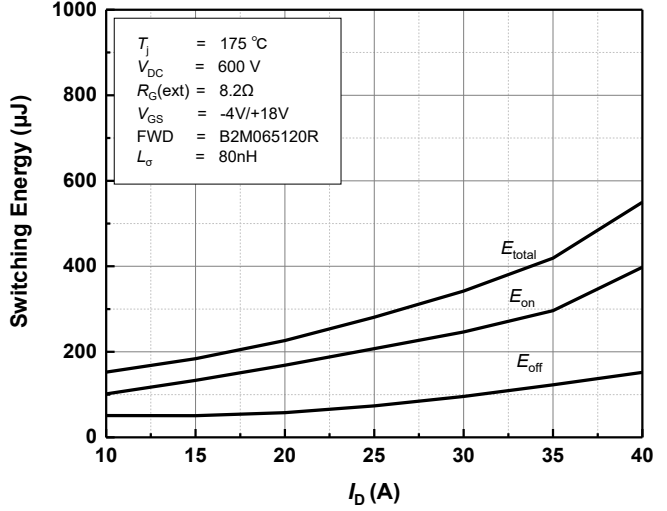


Figure 18 Clamped Inductive Switching Energy vs. Drain Current ($V_{DC} = 600\text{V}$) at $T_j = 175^\circ\text{C}$

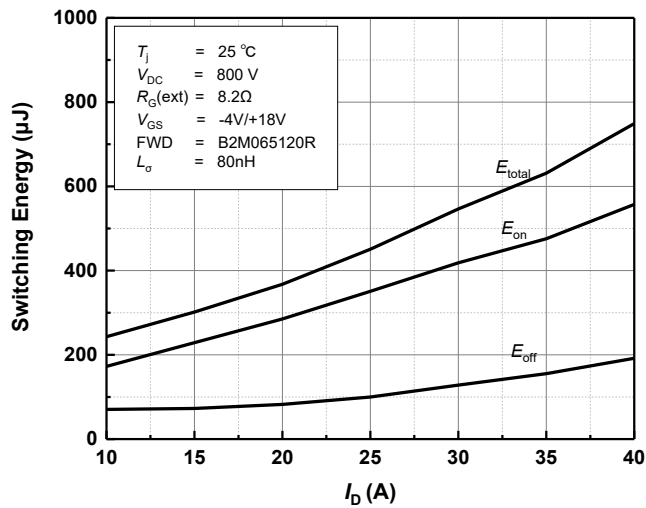


Figure 19 Clamped Inductive Switching Energy vs. Drain Current ($V_{DC} = 800\text{V}$) at $T_j = 25^\circ\text{C}$

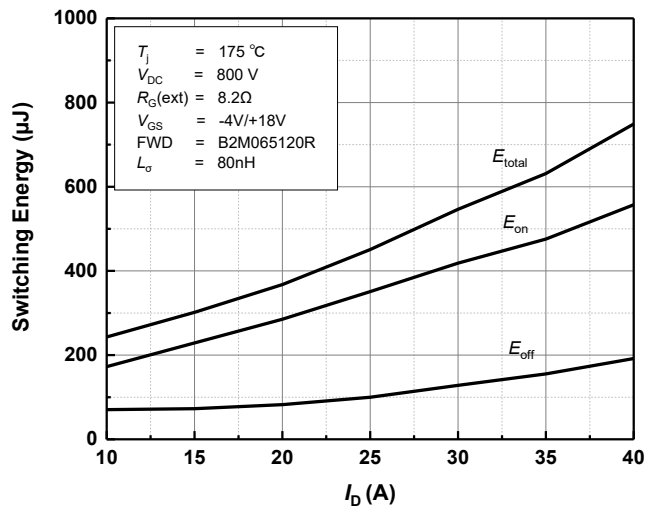


Figure 20 Clamped Inductive Switching Energy vs. Drain Current ($V_{DC} = 800\text{V}$) at $T_j = 175^\circ\text{C}$

Typical Performance

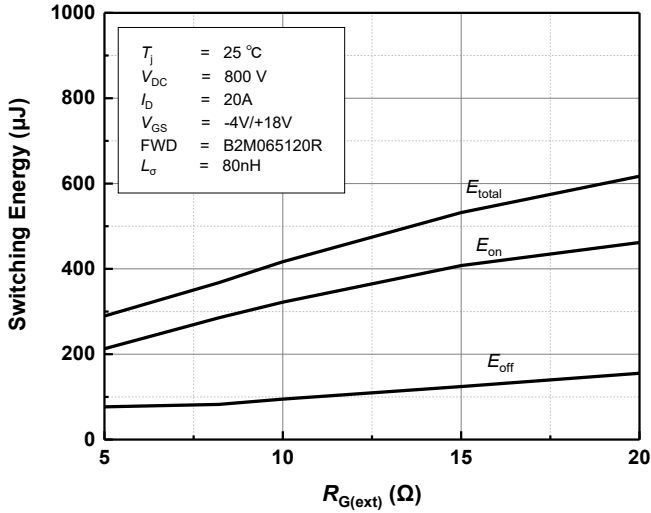


Figure 21 Clamped Inductive Switching Energy vs. External Gate Resistance at $T_j=25^\circ\text{C}$

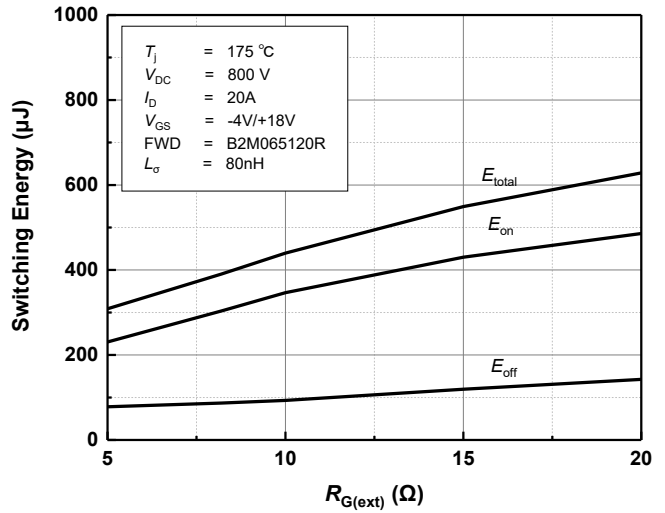


Figure 22 Clamped Inductive Switching Time vs. External Gate Resistance at $T_j=175^\circ\text{C}$

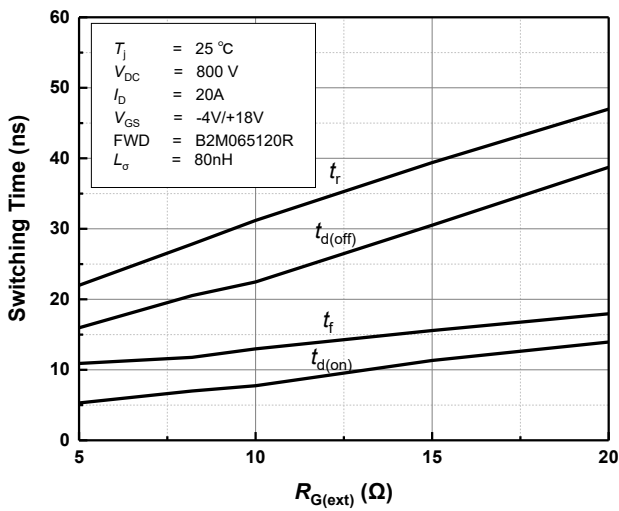


Figure 23 Clamped Inductive Switching Time vs. External Gate Resistance at $T_j=25^\circ\text{C}$

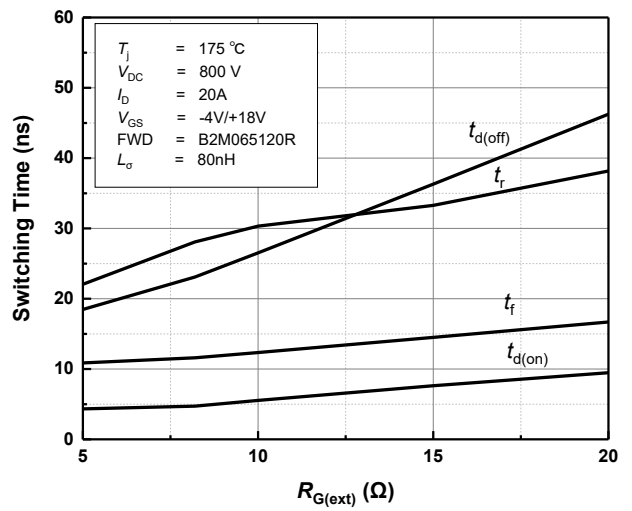


Figure 24 Clamped Inductive Switching Time vs. External Gate Resistance at $T_j=175^\circ\text{C}$

Typical Performance

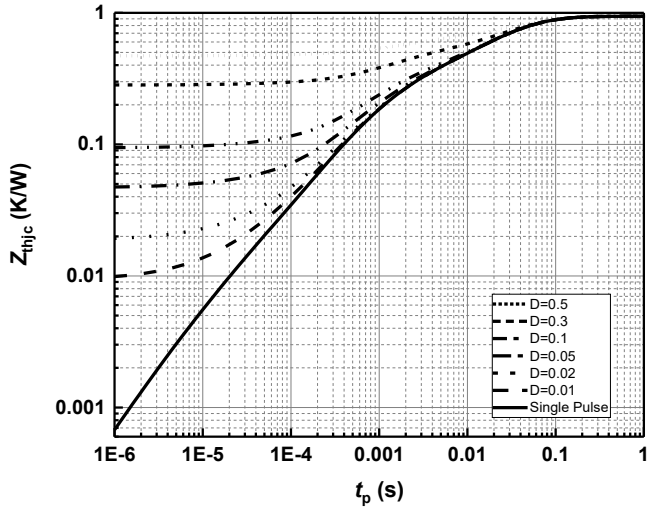


Figure 25 Transient Thermal Impedance (Junction - Case)

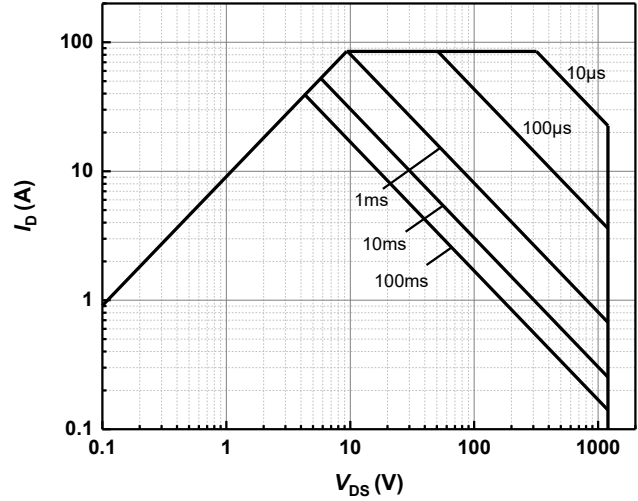
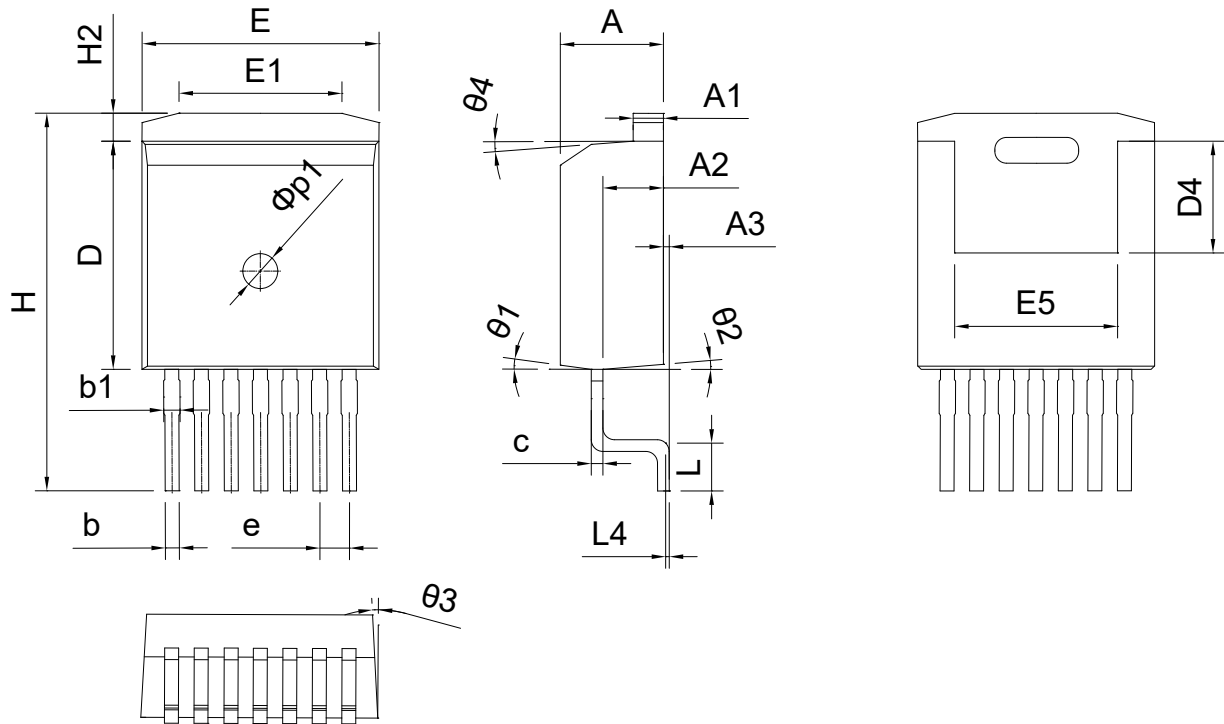


Figure 26 Forward Biased Safe Operating Area

Package Dimensions


SYMBOL	mm		
	MIN	NOM	MAX
A	4.30	4.43	4.56
A1	1.20	1.30	1.40
A2	2.45	2.60	2.75
A3	0.00	0.13	0.25
b	0.50	0.60	0.70
b1	0.60	0.70	0.90
c	0.45	0.50	0.60
D	8.93	9.08	9.23
D4	4.65	4.80	4.95
E	10.08	10.18	10.28
E1	6.50	7.00	7.50
E5	6.82	7.22	7.62
e	1.27 BSC		
H	15.00	15.50	16.00
H2	0.98	1.20	1.42
L	1.90	2.20	2.50
L4	0.25 BSC		
φ p1	1.40	1.50	1.60
θ1	3°	5°	7°
θ2	3°	5°	7°
θ3	3°	5°	7°
θ4	3°	5°	7°

Revision History

Document Version	Date of Release	Description of Changes
Rev. 0.0	2023-06-05	Draft datasheet created.

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