

be POWER SOLUTIONS & PROTECTION

DSP1N5D14

DSP1D Series Dual Output DC-DC Converters

The DSP1D Series is specifically designed to convert a nominal 5 volt input into two isolated output voltages.

The dual semi-regulated output voltages were designed to allow analog circuits and three-terminal regulators to operate within their most efficient input voltage range.

This series achieves high power densities through the use of 350 kHz fixed-frequency switching converters.



- RoHS lead solder exemption compliant
- Up to 1 Watt unregulated output power
- Single-In-Line package
- Four-terminal operation
- Efficiencies to 70%
 - Output Voltages: 5V, 7V, 12V, 14V, 15V, 17V
- 700 V isolation
- -40 °C to +85 °C operation



Compliant

DSP1D Series

1. MODEL SELECTION

MODEL	INPUT RANGE [VDC]		OUTPUT			
	MIN	MAX	[VDC]	[mA]	POWER [W]	
DSP1N5D5	4.5	5.5	±5	±75	0.75	
DSP1N5D7	4.5	5.5	±7	±70	1	
DSP1N5D12	4.5	5.5	±12	±40	1	
DSP1N5D14	4.5	5.5	±14	±35	1	
DSP1N5D15	4.5	5.5	±15	±33	1	
DSP1N5D17	4.5	5.5	±17	±30	1	

Model numbers highlighted in yellow are not recommended for new designs.

2. GENERAL SPECIFICATIONS¹

PARAMETER	CONDITIONS / DESCRIPTION	MIN	ТҮР	MAX	UNITS
Isolation					
Isolation Voltage		500			VDC
Capacitance	Input to Output		10		pF
Output Trim Function					
Input Resistance			40		kΩ
Programming Range		+5, -34			%
Environmental					
Case Operating Range (T _c) ²		-40		85	°C
Storage Range		-55		105	°C
Line Regulation			1		%
Load Regulation	20% to 100% Load		5		%
General					
MTBF	Calculated		700,000		hrs
Weight			0.1/28		oz/g
Case Material		Non Co	nductive Pla	stic	

NOTES

All parameters measured at Tc = 25 °C, nominal input voltage and full rated load unless otherwise noted. Derate output power linearly to 0.6 watts from 70 °C to 85 °C.



DSP1D Series

3. **DSP1 SERIES APPLICATION NOTES**

EXTERNAL CAPACITANCE REQUIREMENTS

Output filtering is required for operation. A minimum of 10 F is specified for optimal performance. Output capacitance may be increased for additional filtering, and should not exceed 400 μ F. To meet the reflected ripple requirements of the converter, an input impedance of less than 0.5 Ohms from DC to 350 kHz is required. If a capacitive input source is farther than 2" from the converter, it is recommended to use a 10 μ F, 25 V solid tantalum capacitor.

REGULATION

This converter uses a semi-regulated design. The output will vary as the load is changed, with output decreasing with increasing load. Additionally, output voltage will change in proportion to a change in input voltage. The typical output voltage will change 1% for each 1% change in input voltage.





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DSP1D Series



For more information on these products consult: tech.support@psbel.com

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