

# FNP600/850/1000 AC-DC Power Supply FNR-5-12G/FNR-5-48G Power Shelves



## Key Features & Benefits

- RoHS lead free solder and lead solder exempted products are available
- High density front-ends 10.5 to 16.1 W/in<sup>3</sup>
- Universal input voltage range (90-264 VAC) with PFC
- 1U or 2U height configurations
- Droop current share with ORing FETs
- I<sup>2</sup>C interface status and control
- Standby voltage of 12 VDC @ 0.5 A
- Overtemperature, overload, and overvoltage protection
- Status LEDs: AC OK, DC OK, Overtemperature
- FNP850-12 model has airflow direction from front-to-rear or from rear-to-front (-12R model)
- ON/OFF Enable switch - shuts OFF Vo1; Vaux and fan are operational.

The FNP600/850/1000 power factor corrected (PFC) front ends provide (depending on model) either a 12 VDC or a 48 VDC output for telecom, datacom, and other distributed power applications. Their small 1U by 2U size allows for configurations of either height in hot-swap redundant systems while their internal fan and cooling design permits wide use with reliable operation.

Status is provided with front panel LEDs, logic signals, and via the I<sup>2</sup>C management interface bus. In addition, the I<sup>2</sup>C bus can enable the power supply, control fan speed, and on the 12 VDC models it allows for adjusting the output voltage from 7 to 12 VDC. This powerful feature allows the same power supply to be used in various applications where bus voltages driving isolated dc-dc converters and POL regulators may be different.

Also, the FNP850-12R is uniquely designed with airflow from the rear of the power supply to the front. This airflow direction supports those critical applications where space limitations and/or higher ambient temperatures near the rear of the rack system, prohibit the discharge of higher temperature airflow from regular front-to-rear cooled power supplies.

The FNP600/850/1000's meet international safety requirements and are CE marked to the Low Voltage Directive (LVD).

## Applications

- Telecom
- Datacom
- Distributed Power Systems

### North America

+1-866.513.2839

### Asia-Pacific

+86.755.29885888

### Europe, Middle East

+353 61 225 977

# FNP600/850/1000

## Model Selection

MODEL	NOMINAL OUTPUT VOLTAGE (VDC) <sup>1</sup>	ADJUSTMENT RANGE (VDC)	MAXIMUM OUTPUT CURRENT (Amps)	LINE REGULATION (%)	LOAD REGULATION (%) <sup>2</sup>	RIPPLE & NOISE pk-pk % <sup>3</sup>	COMPATIBLE SHELF
FNP600-12	12	7 to 12	51	0.17	6	1	FNR-5-12
	12 (Standby)	N/A	0.5	8	8	1	
FNP850-12	12	7 to 12	73	0.17	6	1	FNR-5-12
	12 (Standby)	N/A	0.5	8	8	1	
FNP850-12R <sup>4</sup>	12	7 to 12	73	0.17	6	1	FNR-5-12
	12 (Standby)	N/A	0.5	8	8	1	
FNP600-48	48	44 to 50.5	12.6	0.17	6	1	FNR-5-48
	12 (Standby)	N/A	0.5	8	8	1	
FNP1000-48	48	44 to 50.5	21	0.17	6	1	FNR-5-48
	12 (Standby)	N/A	0.5	8	8	1	

<sup>1</sup> Models with 5V and 3.3V standby voltages are also available. (Contact factory.)

<sup>2</sup> Primary 12V and 48V outputs have built-in droop regulation.

<sup>3</sup> Maximum peak-to-peak noise expressed as a percentage of output voltage; 20 MHz bandwidth.

<sup>4</sup> FNP850-12R model has airflow from rear to front.

## Ordering Information

Options	Suffixes to Add to Part Number
RoHS lead solder exemption	No RoHS suffix character required.
RoHS compliant for all 6 substances	Add "G" as the last character of the part number.

## Input Specifications

PARAMETER	CONDITIONS/DESCRIPTION	MIN.	NOM.	MAX.	UNITS
AC Input Voltage	Single-phase continuous input range.	90		264	VAC
Input Frequency	AC input.	47		63	Hz
Hold-up Time	After last AC line peak at full power.	At 115 VAC	20 <sup>1</sup>		ms
Input Current	At full-rated load.	At 90 VAC		14	A rms
Inrush Surge Current	Internally limited. Vin = 230 VAC, T = 25 °C			34	A pk
Power Factor	Per EN61000-3-2	0.97			W/VA

<sup>1</sup> FNP1000-48 model has a hold-up time of 16 ms

## Output Specifications

PARAMETER	CONDITIONS/DESCRIPTION	MIN.	NOM.	MAX.	UNITS
Efficiency:	Full rated load at 230 VAC input.	FNP600-48	88	89.5	%
		FNP1000-48	88	89.5	
		FNP600-12	84	87	
		FNP850-12	84	87	
Minimum Load	Minimum loading required to maintain regulation.	0			A
Output Power		FNP1000		1006	W
		FNP850		856	
		FNP600		600	
Overshoot	Output voltage overshoot at turn-on.			3	%
Transient Response	Maximum recovery time to within 1% of initial set point due to a 50% load change, 1A/μs.	12V or 48V output: Standby output:		400	μs
				2	ms
Turn-On Delay	Maximum deviation:	12V or 48V output: Standby output:		2	%
				4	%
Turn-On Delay	Time required for initial output voltage stabilization after application of AC input..			1.5	Sec
Output Regulation	See Model Selection table.				

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# FNP600/850/1000

## Protection

PARAMETER	CONDITIONS/DESCRIPTION	MIN.	NOM.	MAX.	UNITS	
Overvoltage Protection	FNP600-12 & FNP850-12	Latch-style overvoltage protection. Output adjusted to 12V:	14.4	15	15.6	V
	FNP600-12:	Latch-style overvoltage protection. Output adjusted to 7V:	8.75	9.04	9.33	V
	FNP850-12:	Latch-style overvoltage protection. Output adjusted to 48V:	8.44	8.75	9.1	V
	FNP600-48 & FNP1000-48	Latch-style overvoltage protection. Output adjusted to 48V:	57 <sup>1</sup>		60	V
Overcurrent Protection (Power supply recovers when short is removed.)	FNP600-12	Current limit. 12V output:	54	56	61	A
		12V Standby output:	0.55	0.75	1.0	A
	FNP850-12	Current limit. 12V output:	77	80	88	A
		12V Standby output:	0.55	0.75	1.0	A
	FNP600-48	Current limit. 48V output:	13	14	16	A
	12V Standby output:	0.75		1.75	A	
	FNP1000-48	Current limit. 48V output:	22	23	25	A
		12V Standby output:	0.75		1.75	A
Short-Circuit Protection	Power supply recovers when short is removed.					
Overtemperature/ Fan Failure Warning	FNP 12V or 48V Vo1 supply output will shut down in the event of an overtemperature condition or blocked fan rotor. Supply's fan and Vaux are active. Power supply will recover when OT condition is removed. Amber OT LED will turn ON to indicate fault condition.					
	OT/Fan Fail is an open-collector signal with 20-mA pull-down. High signal indicates a normal operating condition. Output will go low at least 100 ms before OT condition shuts down the power supply. Internally pulled up to 5V with a 5.1 kΩ resistor. Note. <sup>2</sup>					

<sup>1</sup> FNP1000-48 overvoltage protection range is 56 V minimum and 60 V maximum.

<sup>2</sup> A pull-up to 3.3V can be achieved by terminating the logic signal with a 10 kΩ resistor to logic ground.

## Control and Monitoring

PARAMETER	CONDITIONS/DESCRIPTION	MIN	NOM	MAX	UNITS
DC OK	TTL open-collector signal with active 20-mA pull-down. Represents percent of output voltage where signal is OK. Below this voltage, a signal high condition indicates an output which is out of tolerance. Green DC OK LED on front panel indicates normal operation <sup>1</sup> . Internally pulled up to 5V with a 5.1 kΩ resistor. Note. <sup>2</sup>	90		110	%
Input Power Fail Warning	TTL open-collector signal with active 20-mA pull-down. High indicates an input power fail. Power Fail Warning will turn OFF green AC OK LED. Represents the time after warning signal before Vout drops to 95% due to loss of input power. <sup>1</sup> Internally pulled up to 5V with a 5.1 kΩ resistor. Note. <sup>2</sup>	5			ms
Power Supply Present Signal	Resistance of connection to logic ground which allows user to determine if power supply is present. <sup>1</sup>		10		Ω
Current Share	Main output only. Difference in current between two supplies at >10% load value.	FNP600-12:		5	A
		FNP850-12:		7	
		FNP600-48:		2	
		FNP1000-48:		2	
Remote Sense	Total voltage compensation for cable losses with respect to the main output.			0.5	V
Output Enable	Open circuit or logic high from ENA pin to Vo1 RTN shuts OFF Vo1; Vaux and fan are operational. Logic low (2 mA sink capability) or jumper will turn ON Vo1 within 500 ms. Open circuit voltage on enable pin is 3.3 VDC. Externally applied voltage to the enable pin should NOT exceed 7 VDC. Signal is referred to as Logic Return (LRTN).				

<sup>1</sup> Also available on I<sup>2</sup>C data line.

<sup>2</sup> A pull-up to 3.3V can be achieved by terminating the logic signal with a 10 kΩ resistor to logic ground.

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## I<sup>2</sup>C Bus Management Interface

PARAMETER	CONDITIONS/DESCRIPTION
<b>Static</b>	Includes static information such as: part number and revision level, output rating, serial number, date code, and manufacturing location.
<b>Status</b> (Logic 1 or 0)	Power Supply OK. AC Input OK. DC Output OK. Power Supply Seated. Overtemperature. Overcurrent. Fan OK.
<b>Real-Time Monitoring</b>	Output voltage (main output) 0.1V LSD. Output current (main output) 0.1A LSD. Time in service. Enable for main output.
<b>Control Signals</b>	Output voltage to: (0.01V resolution on 12V supplies; 0.05V resolution on all 48V supplies).  Fan speed level.

## Safety, Regulatory, and EMI Specifications

PARAMETER	CONDITIONS/DESCRIPTION	MIN	NOM	MAX	UNITS
Agency Approvals	UL60950-1 (UL), CSA C22.2 60950-1 (cUL), EN60950-1 (TUV), IEC60950-1 and CE (LVD)				
Electromagnetic Interference	FCC CFR title 47 Part 15				
	Sub-Part B, Conducted: EN55022/CISPR 22. Radiated:	B A			Class
Harmonics	Per IEC61000-3-2.	A			Class
Voltage Fluctuation and Flicker	Per IEC61000-3-3.	Pass			
ESD Susceptibility	Per EN61000-4-2, Level 4. Criterion B	8		.	kV
Radiated Susceptibility	Per EN 61000-4-3, Level 3. Criterion A	10		.	V/M
EFT/Burst	Per EN 61000-4-4, Level 4. Criterion B	±4		.	kV
Input Transient Protection	Per EN 61000-4-5, Class 3. Criterion B	Line-to-Line:	1		kV
		Line-to-Ground:	2		kV
RF Conducted Disturbances	Per EN 61000-4-6, Level 3. Criterion A	10		.	V
Voltage Interruptions	Per EN 61000-4-11, performance criterion B 30%.	10			ms
	Per EN 61000-4-11, performance criterion C 60%.	100		.	ms
	Per EN 61000-4-11, performance criterion C 95%.	5			Sec
Voltage Sag Immunity	Per SEMI F47-0999 > 100 VAC. No output voltage interruption.			.	
Leakage Current	Per EN60950-1. At 240 VAC:			3.5	mA

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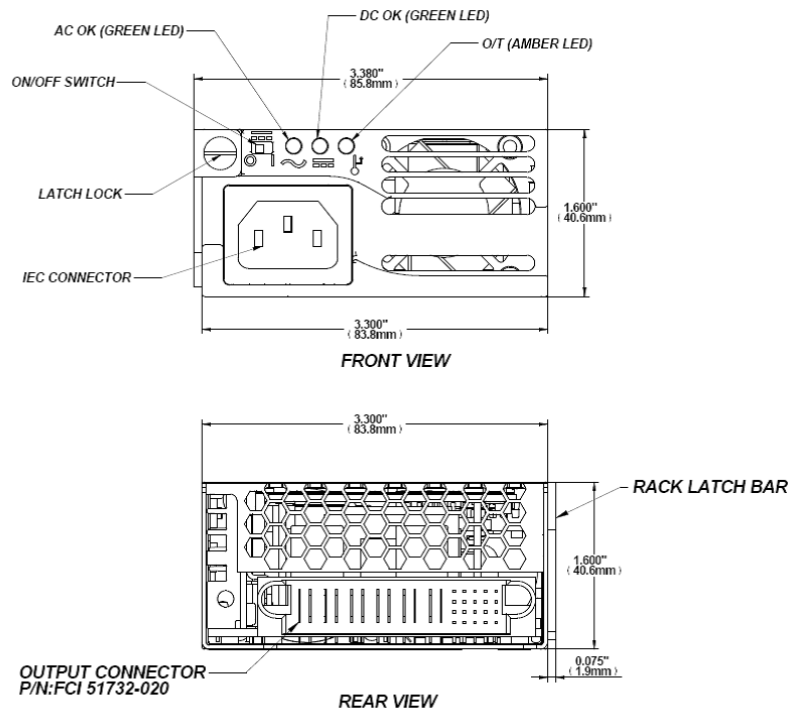
## Environmental Specifications

PARAMETER	CONDITIONS/DESCRIPTION	MIN	NOM	MAX	UNITS	
Altitude	Operating.			10K	ASL ft	
	Non-Operating.			40K	ASL ft	
Operating Temperature	FNP600-12:					
	FNP850-12:	Internal DC fan for cooling.	At 100% load:	0	50	°C
	FNP600-48:		At 50% load:	0	70	
	FNP1000-48:					
Storage Temperature	FNP850-12R:	Internal DC fan for cooling.	At 100% load:	0	40	
			At 94% load:	0	50	°C
			At 50% load:	0	70	
Temperature Coefficient	0 °C to 70 °C (after 15-minute warm-up).			0.02	%/°C	
Relative Humidity	Non-condensing			95	%RH	
Shock	Operating: half-sine, 10 ms, 3-axis.			+20	Gpk	
	Non-Operating: half-sine, 10 ms, 3-axis.			+40	Gpk	
Vibration	Operating: swept sine 5-2000-5 Hz,			1	Gpk	
	5-32 Hz, 0.02iDA, 32-2000 Hz.					
	Non-operating: random 10-2000 Hz.			6.15	Grms	

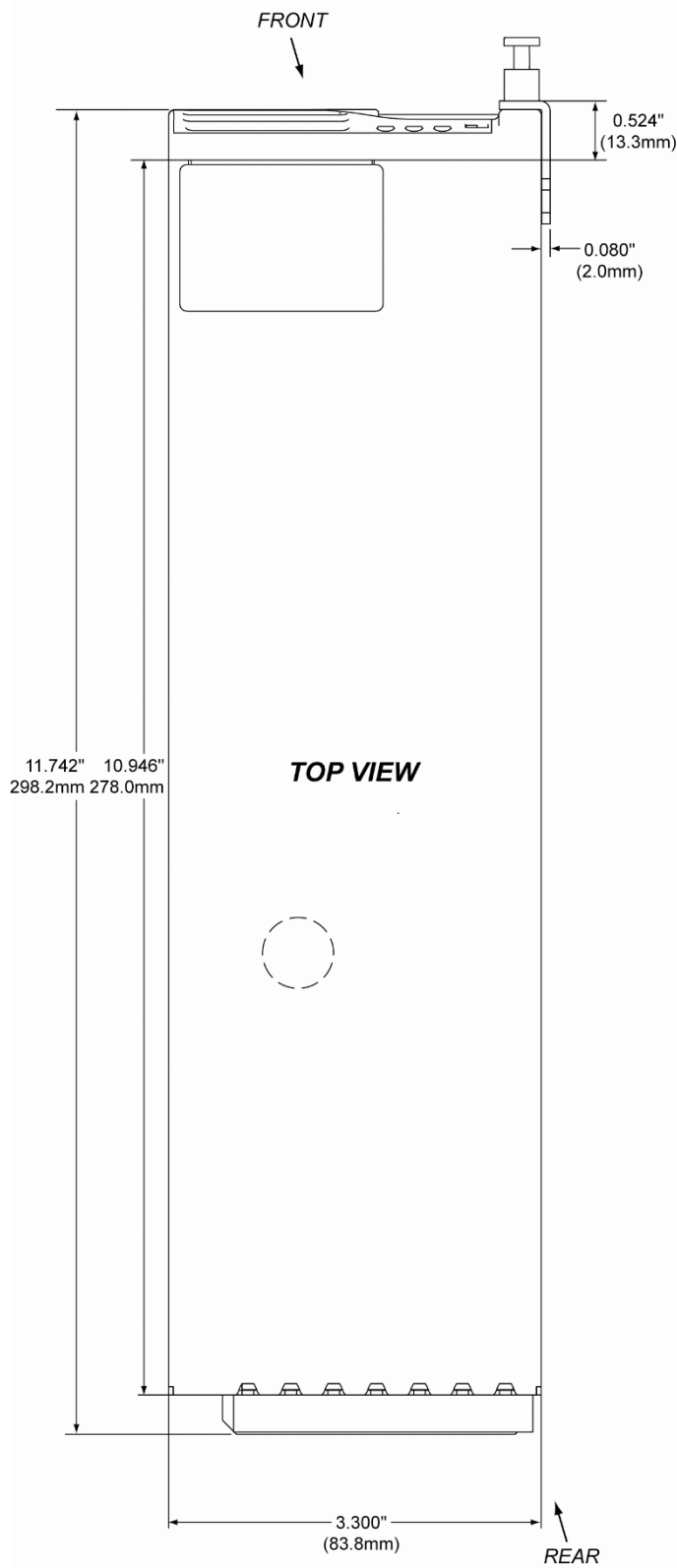
## Reliability

PARAMETER	CONDITIONS/DESCRIPTION	MIN	NOM	MAX	UNITS
MTBF	(Calculated) MILHDBK 217F Ground Benign.	100 000			hrs
	Demonstrated.	250 000			hrs
	Useful Life.	10			yrs

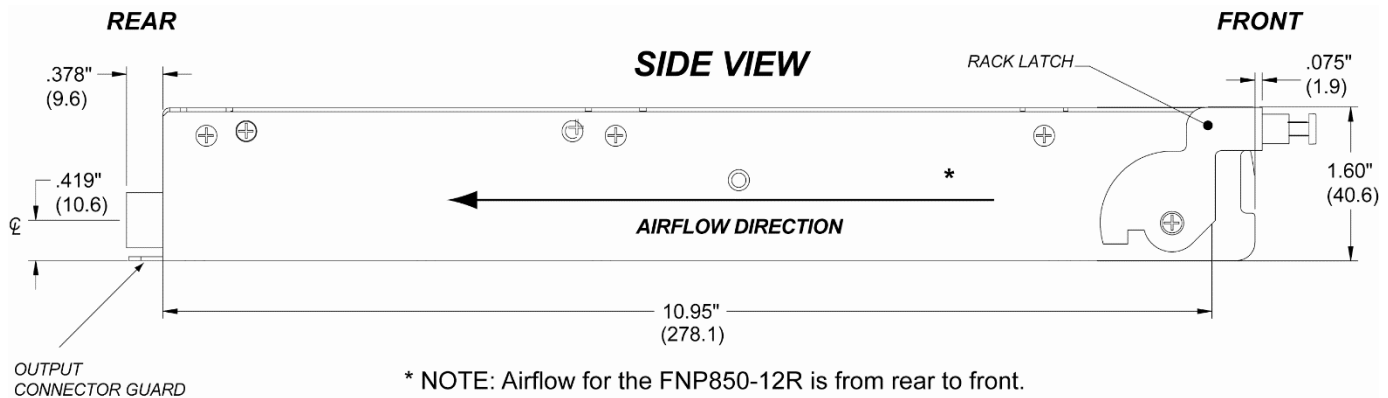
## Mechanical Drawings



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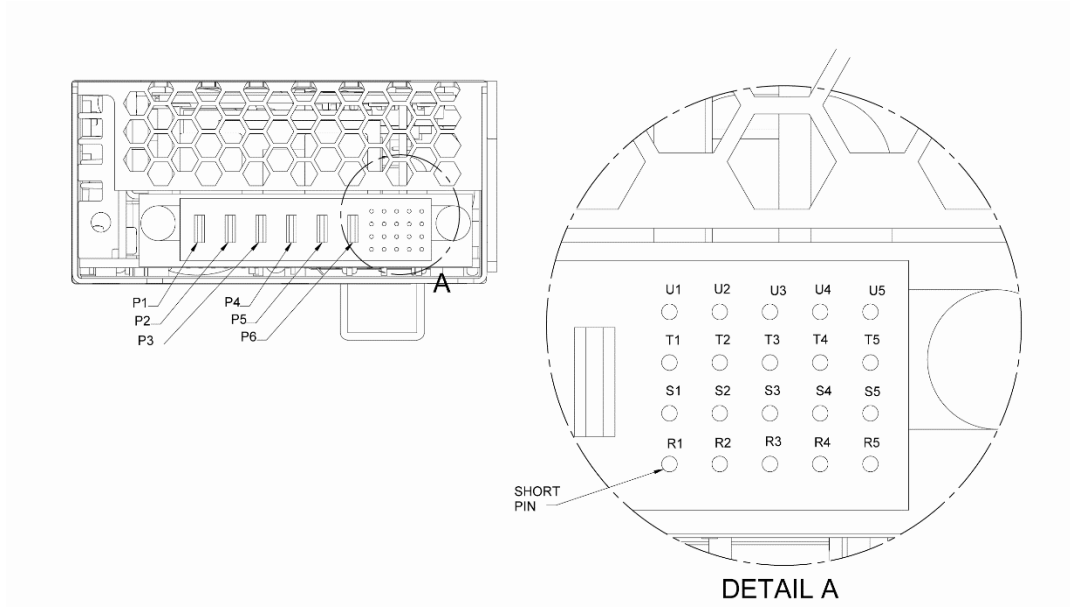


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## Connector Information

<b>Power Supply:</b>	Input - IEC 320 input (Male) standard line cord connection Output - P/N FCI 51732-020LF								
<b>Mating Connections:</b>	Input - IEC 320 output (Socket) Standard line cord (15A) Output - P/N: FCI 51742-10602000AALF (Backplane) P/N: FCI 51762-10602000AALF (Right Angle)								
<b>Input IEC Connector:</b>	<table border="1"> <thead> <tr> <th>Input</th> <th>Location</th> </tr> </thead> <tbody> <tr> <td>Chassis (Safety) Ground</td> <td>Ground</td> </tr> <tr> <td>Line 1 (Line)</td> <td>L</td> </tr> <tr> <td>Line 2 (Neutral)</td> <td>N</td> </tr> </tbody> </table>	Input	Location	Chassis (Safety) Ground	Ground	Line 1 (Line)	L	Line 2 (Neutral)	N
Input	Location								
Chassis (Safety) Ground	Ground								
Line 1 (Line)	L								
Line 2 (Neutral)	N								





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## Output Pin Assignments

SIGNAL	PIN LOCATION	GROUND REFERENCE	NOTE
Over Temperature / Fan Fail	U1	Logic Ground (LRTN)	Open collector 20mA,int.pull-up 5.11kΩ to 5V DC
AC Power Fail Warning	U2	Logic Ground (LRTN)	Open collector 20mA,int.pull-up 5.11kΩ to 5V DC
Power Supply Present	U3	Logic Ground (LRTN)	10Ω resistance int. connected to LRTN
Output Voltage Fault	U4	Logic Ground (LRTN)	Open collector 20mA,int.pull-up 5.11kΩ to 5V DC
Internal Ground	U5	Internal Ground (SRTN) <sup>2</sup>	
ADDR0, I <sup>2</sup> C Address Bus	T1	Internal Ground (SRTN)	
ADDR1, I <sup>2</sup> C Address Bus	T2	Internal Ground (SRTN)	
ADDR2, I <sup>2</sup> C Address Bus	T3	Internal Ground (SRTN)	
ADDR3, I <sup>2</sup> C Address Bus	T4	Internal Ground (SRTN)	
ADDR4, I <sup>2</sup> C Address Bus	T5	Internal Ground (SRTN)	
DATA, I <sup>2</sup> C Data Line	S1	Logic Ground (LRTN)	
CLOCK, I <sup>2</sup> C Clock Line	S2	Logic Ground (LRTN)	
Auxiliary Power +12V	S3	Aux Ground	
Auxiliary Power Ground	S4	Aux Ground	
Logic Ground	S5	Logic Ground (LRTN)	
Output Enable <sup>1</sup>	R1	Logic Ground (LRTN)	Open circuit or “High” to LRTN shuts OFF Vo1
Vsense+	R2	Vsense-	
Vsense-	R3	Vsense-	
Output Margin	R4	Internal Ground (SRTN)	OPTIONAL; External resistance needed to adjust Vo1 lower than it is set originally
Active Current share	R5	Internal Ground (SRTN)	OPTIONAL
Vout+	P1, P2, P3	Vsense-	
Vout-	P4, P5, P6	Vsense-	

<sup>1</sup> Short pin length

<sup>2</sup> SRTN (Internal Ground) is internally connected to Vout-

## FNR-5-12G and FNR-5-48G Power Shelves

Each rack (power shelf) is 1U high with backplane and designed for up to five front-end models in parallel or in n+1 operation. Each power shelf has:

- Output terminals with two M4-screws on each power tab.
- Two fast-on contacts for system earthing.
- Address coding over five pole DIP switch on each unit, 37-pin D-Sub connector with I<sup>2</sup>C-lines, monitoring signals and support functions.
- Provides a start-up synchronization circuit and EMV filters.

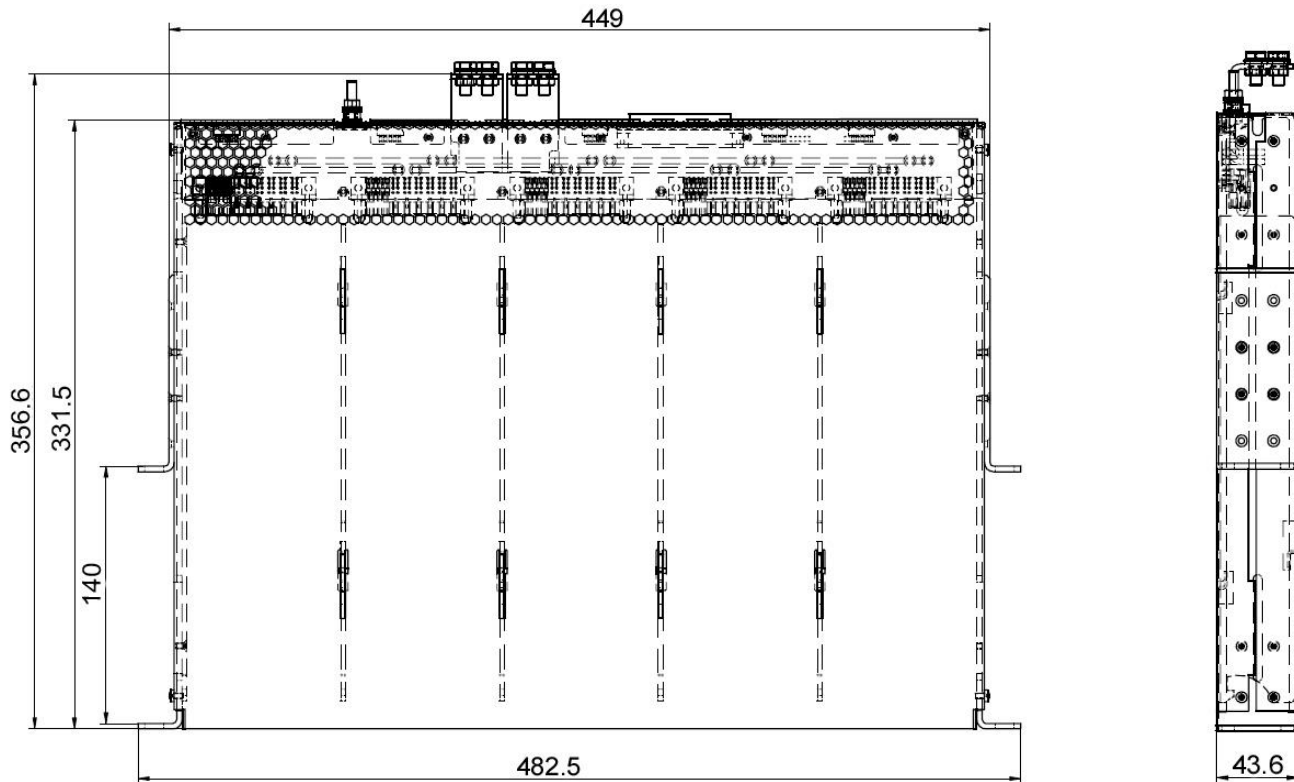
Overall Mechanical Dimensions (W x H x D): 17.68" (449 mm) x 1.72" (43.6 mm) x 13.05" (331.5 mm)





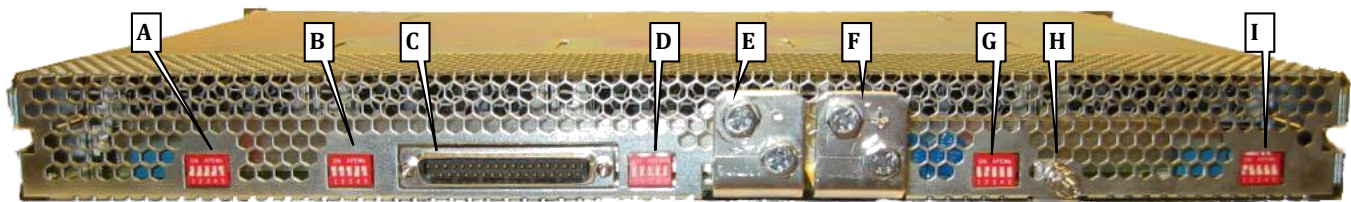
# FNP600/850/1000

## FNR-5-12G and FNR-5-48G Power Shelf Front View



Mechanical Data (FNR-5-12G and FNR-5-48G Power Shelves)

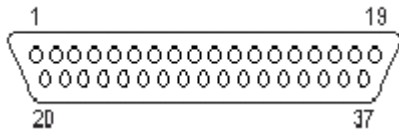
## Output Connector Descriptions (FNR-5-12G & FNR-5-48G)



LOCATION	DESCRIPTION
A	5-Bit DIP switch for I <sup>2</sup> C addressing of PSU 1
B	5-Bit DIP switch for I <sup>2</sup> C addressing of PSU 2
C	37-pin SUB-D connector, controlling and auxiliary power (output 2)
D	5-Bit DIP switch for I <sup>2</sup> C addressing of PSU 3
E	Output 1 minus
F	Output 1 plus
G	5-Bit DIP switch for I <sup>2</sup> C addressing of PSU 4
H	Earth connection
I	5-Bit DIP switch for I <sup>2</sup> C addressing of PSU 5

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## SUB-D Output Connector Pinout and Signal Specification



OUTPUT CONNECTOR DESCRIPTION	PIN LOCATION	TYPE	LOW LEVEL HIGH LEVEL	V max I max
Overtemperature / Fan Fail PSU1	1	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
Power Supply Present PSU 1	2	Resistor 10 $\Omega$ connected to logic GND	Open Pull up	10 V 10 mA
Power Supply Present PSU 2	3	Resistor 10 $\Omega$ connected to logic GND	Open Pull up	10 V 10 mA
Open	4			
Overtemperature / Fan Fail PSU 3	5	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
AC Fail / Power down warning PSU 3	6	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
Power Supply Present PSU 3	7	Resistor 10 $\Omega$ connected to logic GND	Open Pull up	10 V 10 mA
DC Fail / Output voltage fault PSU 3	8	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
Overtemperature / Fan Fail PSU 2	9	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
NC	10, 11			
Output inhibit PSU 1-5	12	Active low (DC-DC stage off when pin is open or on high potential) Referenced to logic GND	<0.8 V >2.0 V	10 V 3.5 mA
V sense +	13	Open or connected to Vo1+ at the load Internally (PSU) connected to Vo1+ over 100 $\Omega$		dU < 3 Vpp 30 mA
V sense -	14	Open or connected to Vo1- at the load Internally (PSU) connected to Vo1- over 100 $\Omega$		dU < 3 Vpp 30 mA
NC	15			
AC Fail/ Power-down warning PSU 4	16	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
DC Fail/ Output voltage fault PSU 4	17	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
AC Fail/ Power-down warning PSU 5	18	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
DC Fail/ Output voltage fault PSU 5	19	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
AC Fail/ Power-down warning PSU 1	20	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
DC Fail/ Output voltage fault PSU 1	21	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA

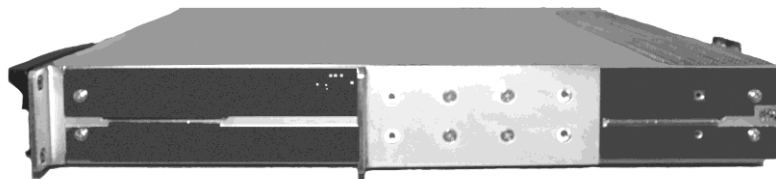
# FNP600/850/1000

## SUB-D Output Connector Pinout and Signal Specification (continued)

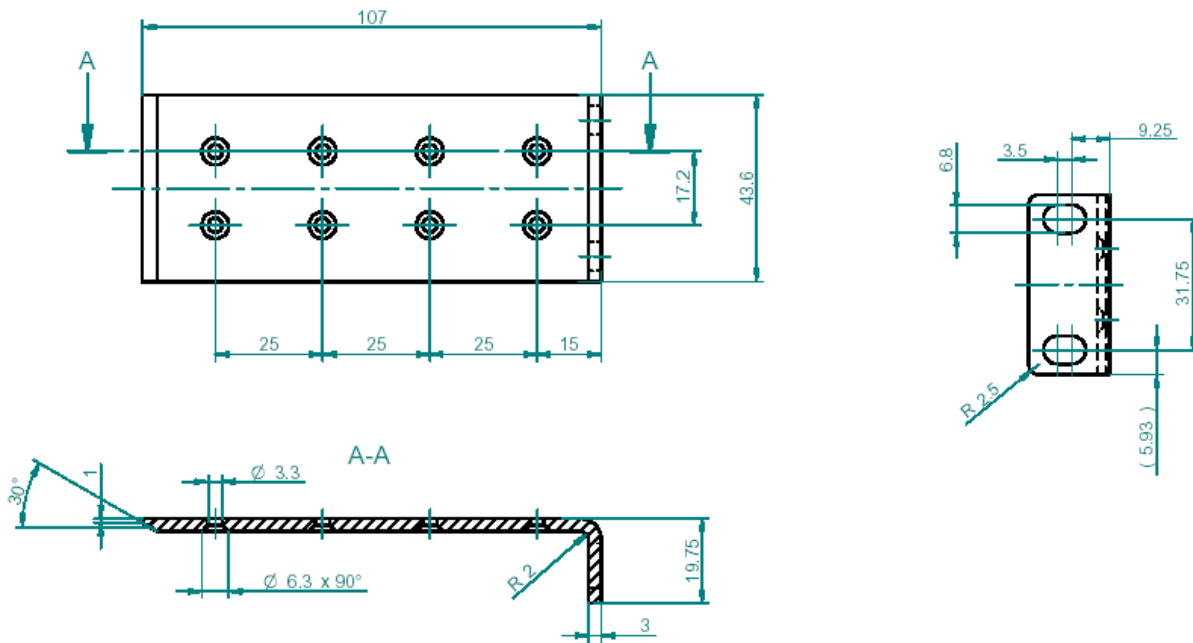
OUTPUT CONNECTOR DESCRIPTION	PIN LOCATION	TYPE	LOW LEVEL HIGH LEVEL	V max I max
AC Fail/ Power-down warning PSU 2	22	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
DC Fail/ Output voltage fault PSU 2	23	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
DATA, I2C data line	24	I2C compatible signal referenced to logic GND	5 V or 3.3 V logic	-
CLOCK, I2C clock line	25	I2C compatible signal referenced to logic GND	5 V or 3.3 V logic	-
Auxiliary power +12 V (Output 2)	26	Vo2+ Aux output, insulated from main output	-	-
Auxiliary power ground (Output 2)	27	Vo2- Aux output, insulated from main output	-	-
Logic Gnd	28	Auxiliary GND, Need to be external connected Vo2-,. Wire separately from auxiliary and main output GND to minimize noise and avoid voltage drops on signal- and I2C return. Leave open if not used.	-	-
Output margin PSU 1	29	Open or connected to V sense- V sense- (+8 % Vo1) or V sense+ (-8 % Vo1)	- -	60V -
Output margin PSU 2	30	Open or connected to V sense- V sense- (+8 % Vo1) or V sense+ (-8 % Vo1)	- -	60V -
Output margin PSU 3	31	Open or connected to V sense- V sense- (+8 % Vo1) or V sense+ (-8 % Vo1)	- -	60V -
Output margin PSU 4	32	Open or connected to V sense- V sense- (+8 % Vo1) or V sense+ (-8 % Vo1)	- -	60V -
Output margin PSU 5	33	Open or connected to V sense- V sense- (+8 % Vo1) or V sense+ (-8 % Vo1)	- -	60V -
Overtemperature / Fan Fail PSU 4	34	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
Power Supply Present PSU 4	35	Resistor 10 $\Omega$ connected to logic GND	Open Pull up	10 V 10 mA
Overtemperature / Fan Fail PSU 5	36	OC-output, protected by 16 V Zener diode and a 10 $\Omega$ resistor in series, referenced to logic GND	<0.4 V @ 20 mA Pull up	15 V 20 mA
Power Supply Present PSU 5	37	Resistor 10 $\Omega$ connected to logic GND	Open Pull up	10 V 10 mA

## Accessories

Center Angular Brackets are set in the middle for shelf mounting:

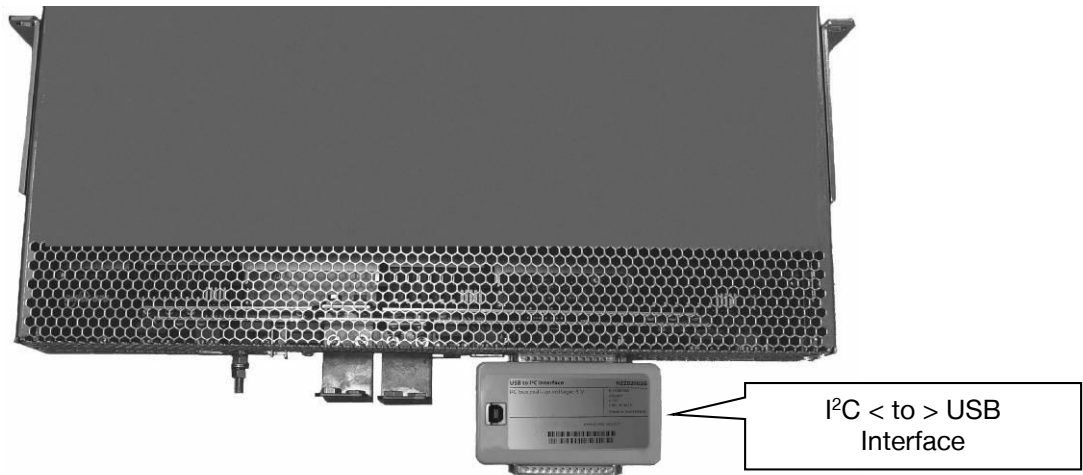


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Center Angular Bracket sets can be ordered from Bel Power Solutions part no.: HZZ01222  
Note: Each Center Angular Bracket set contains 2 brackets and 8 screws.

## I<sup>2</sup>C to USB Interface Demonstration Kit HZZ02002G



An I<sup>2</sup>C to USB Interface Demonstration Kit can be ordered from Bel Power Solution part no.: **HZZ02002G**.

**For more information on these products consult: [tech.support@psbel.com](mailto:tech.support@psbel.com)**

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