# LPM/LMM409 Series AC-DC Modular Power Supply 

5, 10, 12, 20, 24, 36 \& 48 V Single-Output Modules<br>10 \& 20 V Dual-Output Modules



The LPM/LMM409 Series is a modular 900-watt AC-DC power supply that provides a market-leading power density of 14 watts per cubic inch and has an extra low 1 U profile. The LMM is the medically approved version. From here on only the LPM version will be discussed, differences for the LMM version will be noted. The LPM409 offers the flexibility of a modular architecture and the combination of high efficiency and high power density.

Designed for use where a unique set of voltage and current requirements is needed, the supply's four slots can be configured with PCB-based output modules to deliver up to eight outputs. The LPM output modules operate in any chassis position and can provide up to 900 watts total power from a 150 VAC input and 700 watts from an 85 VAC input. Forced-air cooling with airflow direction from input to output is provided by an internal fan.

For LPM409 supplies using less than the four-slot capability, blanking plates are installed for safety purposes and to optimize airflow within the chassis. The supplies are pre-set with default output module settings or with the customer's desired output settings prior to delivery.
The LPM409 chassis can be populated with the output modules listed in Module Selection table. Conformal coating is available on request, please consult with the factory.

## Applications

## LPM409 (Industrial)

- Industrial equipment
- Telecommunications
- Test and measurement
- Automation and peripherals
- Audio/broadcast
- Linear and rotary motion


## LMM409 (Medical)

- Imaging Equipment and Ultrasound
- Anesthesiology
- Surgical Devices
- Diagnostic Equipment
- Patient Beds
- Home Healthcare


## Key Features \& Benefits

- Standard output voltages of 1.5 to 54 VDC
- Efficiencies up to $87 \%$ typical
- Extra-Low 1U profile: 1.6 inch
- Overall Dimensions: $303.7 \times 92 \times 40.64 \mathrm{~mm}(11.9 \times 3.6 \times 1.6 \mathrm{in})$
- High power density design of 14 Watts/cubic inch
- 1 to 4 isolated output slots, fully user configurable
- Power Factor Correction (PFC) IEC 61000-3-2 compliant
- $\quad 700$ W @ 85 VAC or 900 W @ 150 VAC of total output power
- Zero-load operation
- Single-wire current sharing
- Universal input AC range
- Individual control signals on each module
- Auxiliary power 5 V (1 A)
bel

1. MODEL SELECTION

| MODULE | $\mathrm{P} / \mathrm{N}^{3}$ | $\begin{gathered} \text { NO. OF } \\ \text { SLOTS } \\ \text { REQUIRED } \end{gathered}$ | FACTORY-SET SINGLE-OUTPUT [VDC] | MODULE ADJUSTABLE RANGE [VDC] | MAX. OUTPUT CURRENT [AMPS] | MAX. OUTPUT POWER ${ }^{2}$ [WATTS] |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| E | LPM126-OUTA1-05 | 1 | 5 | 2.0 to 5.3 | 53 | 265 |
| F | LPM126-OUTA1-12 | 1 | 12 | 5.2 to 15 | 22 | 265 |
| G | LPM126-OUTA1-24 | 1 | 24 | 14 to 30 | 11 | 265 |
| H | LPM126-OUTA1-36 | 1 | 36 | 29 to 44 | 7.4 | 265 |
| J | LPM126-OUTA1-48 | 1 | 48 | 43 to 54 | 5.5 | 265 |
| K | LPM109-OUTA1-10 | 1 | 15 | 1.5 to 15 | 6 | 90 |
| L | LPM109-OUTA1-20 | 1 | 30 | 3 to 32 | 3 | 90 |
| M | LPM118-OUTA2-10 | 1 | 15 | $2 \times 1.5$ to 15 | $2 \times 6$ | $2 \times 90$ |
| N | LPM118-OUTA2-20 | 1 | 30 | $2 \times 3$ to 32 | $2 \times 3$ | $2 \times 90$ |
| 0 | LPM100-BLAN | 1 |  | Blank Pa | lot Cover) |  |

1 To determine your desired power supply's part number, please refer to Figure 2 for a detailed part number description. Use the LPM Configurator on our website or use our contacts page to locate a distributor for further assistance.
2 For the power derating specification please see section 1.4 below.
$3 \quad$ Add -C for Conformal coating option.

### 1.1. OUTPUT VOLTAGE ADJUSTMENT

Each LPM409 module's output voltage is adjustable by means of a trimmer located on the module. For the modules $\mathrm{E}<->\mathrm{J}$ the trimmer is accessible through the adjustment hole located on the bottom of the power supply (Figure 10). For the modules K<$>\mathrm{N}$ the trimmer is accessible from rear side of the power supply (Figure 5).

### 1.2. PARALLEL CONNECTIONS

Depending on certain situations where current requirements exceed one module's capability, the configurator will process a solution using parallel connections. Paralleling busbars are available to make connections requiring higher current needs. For correct current sharing functionality, it is necessary to interconnect Current Share pins of the modules that will operate in parallel using signal wires. Only E-J modules can be connected in parallel. For more details read section 10.3, see Figure 15.

### 1.3. SERIAL CONNECTIONS

To increase output voltage, it is possible to connect modules in series. Serial busbars are available (see section 10.4) to make connections requiring higher voltage needs. The output of LPM409 is rated as SELV circuit, so the output voltages are guaranteed to be less than 60V. The series combination of modules can exceed this SELV voltage, so in this case the users must take adequate precautions to prevent direct contact with conductive parts. The maximal total voltage must not exceed 200 V . It is recommended to only connect the same modules with the same current rating in series. The voltage sense pins VS+, VS- available on LPM126 modules must be connected according to Figure17.

### 1.4. POWER DERATING

When specifying an LPM409 power supply in an application it is necessary to ensure that LPM409-CHAS and modules (LPM126, LPM118 and LPM109) are operating within their power output capabilities, taking into account the temperature derating and input voltage derating. The maximum permissible output power that may be drawn from LPM409-CHAS and modules assembled in this chassis is given in Figure1.

a.) $\angle P M 409-C H A S$

c.) Modules LPM126-12/24/36/48V

b.) Modules LPM118 / LPM109

d.) Modules LPM118 / LPM109

Figure 1. Power Derating Curves

## 2. PART NUMBERING AND ORDERING INFORMATION

### 2.1 CHASSIS IDENTIFICATION NUMBERING

First left-to-right sequence of the part number (4 characters):
LPM409 Low Profile Modular 4-slot, 900 W, with single-phase AC input. (Figure 2)
NOTE: Chassis and modules are RoHS-6 compliant.


Figure 2. LPM 4-Slot Model Part Number Description

Example: After entering your requirements, the web Configurator could recommend the following optimized part number: LPM409-OHEG-DXXX which represents: Low Profile Modular Series with single-phase AC input, 4-slot, 900W chassis with modules of: blank panel " 0 " in slot 1 and modules $\mathrm{H}, \mathrm{E}$, G in slots 2 to 4 , respectively. Besides the blanking panel in slots 1 , the example's modules in this case would represent desired DC output voltages as follows: Slot $2=\mathrm{H}$ for 36 V , Slot $3=\mathrm{E}$ for 3.3 V and Slot $4=\mathrm{G}$ for 24 V .

Assembly facility code and customer build ID No. are established during actual power supply assembly.
The slots numbering when viewing the unit from the rear, or output side is $1-4$ from left to right.
3. SAFETY SPECIFICATIONS

### 3.1 SAFETY APPROVALS

## LPM409 (INDUSTRIAL) LMM409 (MEDICAL)

- UL/CSA 60950-1 2nd
- IEC 60950-1 2nd
- UL/CSA 60601-1, 3rd
- EN 60950-1 2nd
- IEC 60601-1, 3rd
- CE Mark for LVD
- EN 60601-1, 3rd
(25) (N)CE (1)


### 3.2 INSULATION SAFETY RATINGS

| TEST POINTS | MINIMUM TEST VOLTAGE | INSULATION SAFETY RATING |
| :--- | :--- | :--- |
| Input-to-Chassis | $2120 \mathrm{VDC} / \mathrm{min} .1 \mathrm{~s}$ | Basic Insulation |
| Input-to-Output | $4240 \mathrm{VDC} / \mathrm{min} .1 \mathrm{~s}$ | Reinforced Insulation |
| Outputs-to-Chassis | $200 \mathrm{VDC} / \mathrm{min} .1 \mathrm{~s}$ | Functional Insulation |
| Output-to-Output | $200 \mathrm{VDC} / \mathrm{min} .1 \mathrm{~s}$ | Functional Insulation |

## 4. ENVIRONMENTAL AND RELIABILITY SPECIFICATIONS

### 4.1 ENVIRONMENTAL SPECIFICATION

| PARAMETER | CONDITIONS / DESCRIPTION | MIN | NOM | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Cooling | Internal DC fan, air intake is from the AC connector side exiting at the $D C$ module side |  |  |  |  |
| Audible Noise | Single unit, 4 modules assembled and full loaded, on a table at 1 m distance |  |  | 65 | dBA |
| Operating Temperature | $50^{\circ} \mathrm{C}$ at full power and up to $70^{\circ} \mathrm{C}$ at $50 \%$ load (linearly derated, for details see section 1.4) | -20 | 25 | 50 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature |  | -40 |  | 75 | ${ }^{\circ} \mathrm{C}$ |
| Humidity | $95 \%$ relative humidity @ $25^{\circ} \mathrm{C}$, non-condensing |  |  |  |  |
| Vibration | Operating: Swept $5-500-5 \mathrm{~Hz}$ profile, 3 axis, 5 sweep cycles per axis Non-operating: Swept $5-500-5 \mathrm{~Hz}$ profile, 3 axis, 5 sweep cycles per axis |  |  | 1 4 | Gpk Gpk |
| Shock | Operating: 11 ms , half sine, 10 shocks per face Non-operating: 11 ms , half sine, 3 shocks per face |  |  | $\begin{aligned} & 20 \\ & 40 \end{aligned}$ | $\begin{aligned} & \text { Gpk } \\ & \text { Gpk } \end{aligned}$ |

### 4.2 RELIABILITY

| PARAMETER | CONDITIONS / DESCRIPTION | MIN | NOM | MAX |
| :--- | :--- | :--- | :--- | :--- |
| Calculated MTBF | According to MIL-HDBK217, Ground benign $30^{\circ} \mathrm{C}$, | 250,000 | hours |  |
| NOTE: Calculation was done for LPM409-EFGH | hours |  |  |  |
| Demonstrated MTBF | Tamb $=25^{\circ} \mathrm{C}$ | 250,000 |  |  |

LPM/LMM409 Series

## 5. FAULT PROTECTION

| PARAMETER | CONDITIONS / DESCRIPTION | MIN | NOM | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Input Fuse | One fuse, non-user serviceable, located on line leg of AC input, Fast Acting type |  | 12.5 |  | A |
| Inrush Current Limitation | Provided by NTC | See Input Specification section |  |  |  |
| Short Circuit Protection | Provided by Current Limit circuit | $105{ }^{1}$ |  | $125{ }^{1}$ | $\begin{gathered} \text { \% } \\ \text { lo_nom } \end{gathered}$ |
| Output Overvoltage Protection | Latching (unit needs to be turned-off and on) | 10 |  | 25 | $\begin{gathered} \text { \% } \\ \text { Vo_nom } \end{gathered}$ |
| Over Temperature | OT with auto restart |  |  | 70 | ${ }^{\circ} \mathrm{C} \mathrm{amb}$ |

## 6. EMC SPECIFICATIONS

### 6.1 EMC IMMUNITY

| PARAMETER | CONDITIONS / DESCRIPTION | CRITERION |
| :---: | :---: | :---: |
| Electrostatic Discharge (ESD) | IEC/EN61000-4-2; GR-1089 R2-1, R2-2, R2-3 Level 4: contact: $\pm 8 \mathrm{kV}$, air: $\pm 15 \mathrm{kV}$ | Perform, criterion B |
| RF Susceptibility | IEC/EN 61000-4-3; <br> Level 3: $10 \mathrm{~V} / \mathrm{m}$; 80 MHz to 1000 MHz ; <br> AM $80 \%, 1 \mathrm{kHz}$ radiated; RF electromagnetic field | Perform, criterion A |
| Fast transient / burst | IEC/EN 61000-4-4; Level 3; $\pm 2$ kV, 5 kHz electrical fast transient / burst immunity test | Perform, criterion B |
| Surge | IEC61000-4-5, level 3 ; line to earth: $\pm 2 \mathrm{kV}$, line to line: $\pm 1 \mathrm{kV}$ surge immunity test | Perform, criterion B |
| RF Conducted Disturbance | IEC/EN 61000-4-6; Level 3; GR-108; $10 \mathrm{~V}, 0.15$ to 80 MHz , AM $80 \%$, 1 kHz | Perform, criterion A |
| Voltage Dips / Short Interruptions | IEC/EN 61000-4-11; Voltage dips, interruptions and variations. (Interpretation: dip below Vi min with Po nom = hold-up time 10 ms ) 1a: Dip 30\%, 100 ms <br> 1a: Dip 30\%, 200 ms <br> 1a: Dip $60 \%, 10 \mathrm{~ms}$ <br> 1a: Dip 60\%, 100 ms <br> 1a: Dip $>95 \%, 10 \mathrm{~ms}$ (interruption) <br> 1a: Dip $>95 \%, 100 \mathrm{~ms}$ (interruption) | Perform, criterion B <br> Perform, criterion B <br> Perform, criterion A <br> Perform, criterion B <br> Perform, criterion A <br> Perform, criterion B |

### 6.2 EMC EMISSION

| PARAMETER | CONDITIONS / DESCRIPTION | CRITERION |
| :--- | :--- | :--- |
| Conducted Emission | EN 55022, FCC - EN 55022, and CISPR 22 Class B, | Class B; |
| EN55022, FCC | FCC 47CFR15 unintentional radiators; standalone at all nominal input <br> voltages and measured in Po1: 0,50\%, 100\%; <br> signal connections open | min. $3 \mathrm{~dB} / \mu \mathrm{margin}$ |
|  | EN 55022, FCC - EN 55022, and CISPR 22 Class B, |  |
| Radiated Emission FCC 47CFR15 unintentional radiators; standalone at all nominal input <br> EN55022, FCC voltages and measured in Po1: 0,50\%, 100\%; <br> signal connections open  | Radiated Class B QSP / AVG <br> min. 3 dB margin |  |
| Input Current Harmonics | EN 61000-3-2, sine wave input, Class D; measured standalone at all | Pass |

## 7. ELECTRICAL SPECIFICATIONS

### 7.1 INPUT SPECIFICATIONS



1 For any combination of output modules, any valid load and voltage setting
2 Efficiency typical for standard configuration EFGH and nominal output voltage settings

### 7.2 INPUT - SIGNALS, FEATURES AND INDICATOR DESCRIPTIONS

| SIGNAL NAME | PIN | DESCRIPTION |
| :---: | :---: | :---: |
| Auxiliary Output - 5 V - | $\begin{aligned} & 1,2 \text { vs } 3,4 \end{aligned}$ (RTN_D) | Output is available when the AC input is over 85 VAC. The output nominal output voltage is 5.0 V at maximum current rating of 1.0 A. Over current protection occurs above 1.5 A. The output is referenced to logic return, RTN_D. |
| Input AC Indication - PFAIL - |  | Open Collector output with 20 mA pull-down capability referenced to logic return RTN_D. PFAIL open or High state indicates the warning that the input is out of limits (the input voltage falls below 85 VAC or rises over 264 VAC) 5 ms before the output goes below the lower regulation limit. PFAIL will turn-off the green Input OK LED. <br> PFAIL LOW state indicates that the input voltage is within the operating range. |
| Fan Fail / OTP Ind - FAN_FAIL - |  | Open Collector output pin with 20 mA pull-down capability referenced to logic return RTN_D. FAN_FAIL OPEN OR HIGH state indicates a fan failure/over temperature protection (OTP) condition min. 100 ms before the unit shuts-down. A fan failure/OTP condition will turn-off the green FAN OK LED. <br> FAN FAIL LOW state indicates normal fan operation and no OTP condition. |

### 7.3 OUTPUT SPECIFICATIONS

| PARAMETER | CONDITIONS/DESCRIPTION | MIN | NOM | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Output Power | One fan for internal cooling |  | 900 |  | W |
| Output DC <br> Voltages / Modules | All output modules work in any chassis position and are max 1 U high PCB-based |  |  | - |  |
| Current Share ${ }^{3}$ | Active single-wire current share (modules E-J) Max. difference in currents between two modulespercentage of one module nominal current |  |  | $\pm 10$ | \% |
| Line Regulation | Input from 85 to 264 VAC, 80\% load |  | 1.0 |  | \% Vo_nom |
| Load Regulation | From 0-100\% load, Input > 180VAC, Vo_nom |  | 1.0 |  | \% Vo_nom |
| Thermal Drift | After 15-minute warm-up period |  | 0.02 |  | \%/ ${ }^{\circ} \mathrm{C}$ |
| Total Regulation | Variation of line, load and temperature drift |  | 2.0 |  | \% Vo_nom |
| Output Adjust. Range |  | See Module Selection Table 1 |  |  |  |
| Dynamic Response | Deviation for 10-90\% or 90-10\% load changes at a rate of $1 \mathrm{~A} / \mu \mathrm{s}$, (constant current mode, Vo reach $1 \%$ band around Voset) <br> Deviation for $50-100 \%$ or $100-50 \%$ load steps with $1 \mathrm{~A} / \mu \mathrm{s}$ rate. (constant current mode, Vo reach 1\% band around Voset) |  |  | $\begin{gathered} 4^{1,2} \\ 2000 \\ 3^{1} \\ 400 \end{gathered}$ | \% $\mu \mathrm{s}$ \% $\mu \mathrm{s}$ |
| Output Ripple \& Noise | $\mathrm{BW}=20 \mathrm{MHz}$; Filter $10 \mathrm{nF} / 10 \mathrm{uF}$; over line and load, $25^{\circ} \mathrm{C}$ |  |  | $1 \%$ of Vo_nom ${ }^{1}$ | mVpk-pk |
| CM Noise | Output to chassis, over line and load (Measured across 50 Ohms, with $10 \mu \mathrm{H} / 10 \mathrm{nF}$ in parallel) |  | 220 | 500 | mVpp |
| Overshoot | Output voltage overshoot at turn-on |  | 4 |  | \% Vo_nom |
| Turn-On Characteristics | Turn ON at minimum and nominal output current | Monotonic characteristic |  |  | - |
| Turn-Off Characteristics | Turn OFF at minimum and nominal output current | Monotonic characteristic |  |  | - |
| Turn-On Time | Time required for output within regulation after initial application of AC input <br> Time required for output within regulation after removing inhibit |  |  | 1.5 100 | s ms |
| Hold-up Time | Vo is required to stay within 95\% regulation after AC is removed. Measured from the last AC peak, VAC min and full load. | 10 |  |  | ms |
| Remote Sense ${ }^{3}$ | Total compensation for cable losses |  | 250 | 500 | mV |
| Over-current Protection | Automatic recovery (Hiccup by K-N modules) |  |  | $\begin{aligned} & 130(180 \\ & \text { for } \mathrm{K}-\mathrm{N}) \end{aligned}$ | \% |

### 7.4 OUTPUT - SIGNALS, FEATURES AND INDICATOR DESCRIPTIONS

### 7.4.1 MODULES E, F, G, H, J

| SIGNAL NAME | PIN | DESCRIPTION |
| :---: | :---: | :---: |
| Positive Sense Wire - VS+ - | 1 | Output voltage sense wire. Internally connected to Vout+ via $51 \Omega$. It is recommended to connect sense wire at positive load point. |
| Negative Sense Wire - VS- - | 2 | Output voltage sense wire. Internally connected to Vout- via $51 \Omega$. It is recommended to connect sense wire at negative load point. |
| Current Share <br> - CSH - | 3 | Common wire for parallel connected modules to achieve proper current sharing between the modules. Referenced to the Common pin. Interconnect CS pin on all parallel working modules. Active current share pin enables control of output voltage. Pulling-up this pin to 5 V is possible to increase the output voltage. Pull-down of this pin has no effect. Voltage on this pin is 3.5 V at nominal module current. |
| Common | 4 | Reference pin to CSH |
| N/C | 5 |  |
| N/C | 6 |  |
| Output Good Indication <br> - PG_HI - | 7 | Open collector output with 20 mA pull-down capability, max. 15 V (recommended Rpull-up $=1 \mathrm{k}, \mathrm{Vcc}=5 \mathrm{~V}$ ). Referenced to PG_LO. <br> PG_HI OPEN OR HIGH state indicates that the module output voltage is below lower regulation limit. A PG_HI fail state turns the GREEN DC OK LED to RED. <br> PG_HI LOW state indicates the module output voltage is within normal operation limits. |
| - PG_LO - | 8 | Reference pin to PG_HI |
| Output Inhibit Function <br> - INH_HI - | 9 | An opto-isolated input with 2-10 mA current capability. Apply $+(3-15) \mathrm{V}$ on INH_HI and RTN on INH_LO (e.g. from the Auxiliary Output 5V) to activate the inhibit function. <br> INH_HI OPEN OR LOW state ENABLES the module output. <br> INH_HI HIGH state INHIBITES the module output. |
| - INH_LO - | 10 | Reference pin to INH_HI |

### 7.4.1 MODULES E, F, G, H, J



### 7.5 VISUAL ALARMS DESCRIPTION

| ALARM NAME | LED POSITION | DESCRIPTION |
| :--- | :--- | :--- |
| Input AC Good | Front panel - LED2 <br> (see Fig. $\bar{\prime}$ | ON state LED indicates operation within specified input voltage range. <br> GREEN LED indicator goes to an OFF condition on PFAIL signal failure state. |
| Fan Good | Front panel - LED1 <br> (see Fig. ) | ON state LED indicates normal fan operation and no OTP status. <br> GREEN LED indicator goes to an OFF condition on FAN_FAIL / OTP signal <br> failure state. |
| Output Good | Module connector / rear side <br> (see Fig.5 and Fig.6) | GREEN LED indicates that module output voltage is over minimum regulation <br> limit. <br> GREEN LED indicator goes to RED on PG_HI signal failure state. |

## 8. CONNECTOR DETAILS

### 8.1 INPUT CONNECTOR INFORMATION

LPM409 front panel connectors pinout refers to Fig. 4 and chapter 8.2 and 8.3. The signal connector is Cvilux part number: CI3306P1H10, the mating part is Cvilux Cl3306S0010, Molex 50-57-9406 or Taiwan King Pin P553L-06(LF).
NOTE: See chapter 10.1 Mating Connections and cables for Input cable information.


Figure 3. LPM409 Front Panel Connectors Pinout View

### 8.2 INPUT POWER CONNECTOR PINOUT

| SIGNAL NAME | PIN \# | TYPE | RECOMMENDED WIRES | V MAX |
| :--- | :---: | :--- | :--- | :---: |
| Earth | $(1$ I | Earth / Chassis | Min. $0.75 \mathrm{~mm}^{2}$ |  |
| AC Neutral | N | Input Power AC | e.g. Interpower 86230120 | 264 Vrms |
| AC Line | L | Input Power AC Fused | Max. torque on screws (M4x6): 1.5 Nm | 10 Arms |

## CAUTION:

The plastic cover must be in place when AC input voltage is applied to the unit! The cover may be removed (with the help of a screwdriver) only when the input cable is disconnected from the mains.

### 8.3 INPUT SIGNAL CONNECTOR PINOUT

(Mating connector: Molex 50-57-9406, Pins: Molex 16-02-0082)

| SIGNAL NAME | PIN \# | WIRE COLOR | TYPE | SIGNAL REFERENCE | LOW LEVEL HIGH LEVEL | $\begin{aligned} & \text { V MAX } \\ & \text { I MAX } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $+5 \mathrm{~V}$ | 1 | Red | Aux Output | RTN_D | - | 5.0 VDC |
| $+5 \mathrm{~V}$ | 2 | Red | Aux Output | RTN_D | - | 1.0 ADC |
| RTN_D | 3 | Black | Logic Reference Potential | - | - | - |
| RTN_D | 4 | Black | Logic Reference Potential | - | - | - |
| PFAIL | 5 | Yellow | Open Collector Output + ZD 5.6 V | RTN_D | <400mV@ 20mA Pull up | $20 \mathrm{~mA}$ |
| FAN_FAIL | 6 | Green | Open Collector Output + ZD 5.6 V | RTN_D | <400mV@ 20mA Pull up | $20 \mathrm{~mA}$ |

+35361225977 +14087855200

### 8.4 MODULE POWER OUTPUTS PINOUT

8.4.1 MODULES E, F, G, H, J - A, B BUS-BAR

| SIGNAL NAME | PIN \# | TYPE | SIGNAL <br> REFERENCE | LOW LEVEL <br> HIGH LEVEL | V MAX |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Vout+ | Vout+ | Output Power DC | Vout- | - | See Module |
| Vout- | Vout- | Output Power DC | - | - | Selection Table 1 |
| See Module |  |  |  |  |  |
| Selection Table 1 |  |  |  |  |  |

$\begin{array}{ll}\text { Connector type: } & \text { Bus-bar see Figure 4 } \\ \text { Mating part: } & \text { Ring terminal for M4 screw, with appropriate cross section for wire. }\end{array}$
8.4.2 MODULES K, L, M, N

| SIGNAL NAME | PIN \# | TYPE | SIGNAL | LOW LEVEL |
| :---: | :--- | :--- | :---: | :--- |
| Vout1+ | Vout1+ | Output 1 Power DC | VEFERENCE | HIGH LEVEL |

Connector type: Phoenix Contact 1803277
Mating part: Phoenix Contact 1850660


Note: Bus bar screw M4x10mm, max. torque 1.5 Nm

Figure 4. Modules E, F, G, H, J Power Output Connectors
Figure 5. Modules K, L, M, N Power Output Connectors

### 8.5 MODULE SIGNAL OUTPUT CONNECTOR PINOUT

The output signal connector provides signal information across its 10-pin output, for position of the output signal connector please see Figure 4 and Figure 5.

Connector type: JST S10B-PHDSS-B
Mating part: JST housing PHDR-10VS, Pins SPHD-002T-P0.5 (AWG 28-24) or SPHD-001T-P0.5 (AWG 26-22)
8.5.1 MODULES E, F, G, H, J

| SIGNAL NAME | PIN \# | TYPE | SIGNAL REFERENCE | LOW LEVEL HIGH LEVEL | V MAX <br> I MAX |
| :---: | :---: | :---: | :---: | :---: | :---: |
| VS+ | 1 | Output voltage sense wire. Internally connected to Vout+ via $51 \Omega$ | Vout+ |  | $\begin{gathered} 0.5 \mathrm{~V} \\ 10 \mathrm{~mA} \end{gathered}$ |
| VS- | 2 | Output voltage sense wire. Internally connected to Vout- via $51 \Omega$ | Vout- |  | $\begin{gathered} 0.5 \mathrm{~V} \\ 10 \mathrm{~mA} \end{gathered}$ |
| CSH | 3 | Active Current Share pin | Common |  | 5 V |
| Common | 4 | Reference pin to CSH | - |  | - |
| N/C | 5 | - |  | - | - |
| N/C | 6 | - |  | - | - |
| PG_HI | 7 | Open collector output | PG_LO | $\begin{gathered} <400 \mathrm{mV} @ 5 \mathrm{~mA} \\ \text { Pull up } \end{gathered}$ | $\begin{gathered} 15 \mathrm{~V} \\ 20 \mathrm{~mA} \end{gathered}$ |
| PG_LO | 8 | Reference for PG_HI | - | - | $20 \mathrm{~mA}$ |
| INH_HI | 9 | Input, protected by 18V Zener diode | INH_LO | LED with a serial resistor 2 kOhms | $\begin{gathered} 15 \mathrm{~V} \\ 10 \mathrm{~mA} \end{gathered}$ |
| INH_LO | 10 | Reference for INH_HI | - | - | $10 \mathrm{~mA}$ |

8.5.2 MODULES K, L, M, N

| SIGNAL NAME | PIN \# | TYPE | SIGNAL REFERENCE | LOW LEVEL HIGH LEVEL | V MAX <br> I MAX |
| :---: | :---: | :---: | :---: | :---: | :---: |
| INH_LO_B | 1 | Reference for $\mathrm{INH}_{2} \mathrm{HI}$ _B | - | - | $10 \mathrm{~mA}$ |
| INH_HI_B | 2 | Input, protected by 18 V Zener diode | INH_LO_B | LED with a serial resistor 2 kOhms | $\begin{gathered} 15 \mathrm{~V} \\ 10 \mathrm{~mA} \end{gathered}$ |
| PG_LO_B | 3 | Reference for PG_HI_B | - | - | $20 \mathrm{~mA}$ |
| PG_HI_B | 4 | Open collector output | PG_LO_B | <400mV@5mA Pull up | $\begin{gathered} 15 \mathrm{~V} \\ 20 \mathrm{~mA} \end{gathered}$ |
| N/C | 5 | - | - | - | - |
| N/C | 6 | - | - | - | - |
| INH_LO_A | 7 | Reference for NH _HI_A | - | - | $10 \mathrm{~mA}$ |
| INH_HI_A | 8 | Input, protected by 18 V Zener diode | INH_LO_A | LED with a serial resistor 2 kOhms | $\begin{gathered} 15 \mathrm{~V} \\ 10 \mathrm{~mA} \end{gathered}$ |
| PG_LO_A | 9 | Reference for PG_HI_A | - | - | $20 \mathrm{~mA}$ |
| PG_HI_A | 10 | Open collector output | PG_LO_A | <400mV@5mA Pull up | $\begin{gathered} 15 \mathrm{~V} \\ 20 \mathrm{~mA} \end{gathered}$ |

9. MECHANICAL SPECIFICATIONS

| PARAMETER | CONDITIONS/DESCRIPTION |
| :--- | :--- |
| Overal Dimensions | $303.7 \times 92 \times 40.64 \mathrm{~mm}$ |
| Weight | $11.9 \times 3.6 \times 1.6$ in |
|  | 1.38 kg (including 4 modules) |

All drawing dimensions are shown in millimeters, unless otherwise noted.


Figure 6. Front View


Figure 8. Side View


Figure 9. Top View


Figure 10. Bottom View

## 10. ACCESSORIES

### 10.1 MATING CONNECTIONS AND CABLES

All the power and signal cables and mating connectors are not included in the LPM409 standard package. These all need to be ordered extra.

| Front panel signal cable: | Bel Power Solutions accessory LPM000-LEAD-03 | see Figure 11 |
| :--- | :--- | :--- |
| Output signal cable: | Bel Power Solutions accessory LPM000-LEAD-04 | see Figure $12 a$ |
| Mating connector: | JST, housing PHDR-10VS, pins SPHD-002T-P0.5 (AWG 28-24) |  |
| Output power cable (for LPM126): | $14 \mathrm{AWG}-10 \mathrm{AWG}$ depend on the output current, min. thermal class <br> Output power cable (for LPM109/118): | Bel Power Solutions accessory LPM000-LEAD-05, |



Figure 11. LPM000-LEAD-03: Front panel signal cable (1.8 m)


Figure 12a-LPM000-LEAD-04 Output signal cable ( 0.75 m )


Figure 12b-LPM000-LEAD-05: Output power cable for modules K-N ( 0.75 m )

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### 10.2 SIGNAL OUTPUT WIRE COLORS

| PIN \# | SIGNAL NAME BY MODULES E, F, G, H, J | SIGNAL NAME BY MODULES K, L, M, N | WIRE COLOR |
| :---: | :---: | :---: | :---: |
| 1 | VS+ | PG_HI_A | Red |
| 2 | VS- | PG_LO_A | Blue |
| 3 | CSH | INH_HI_A | Yellow |
| 4 | Common | INH_LO_A | White |
| 5 | N/C | N/C |  |
| 6 | N/C | N/C | - |
| 7 | PG_HI | PG_HI_B | Violet |
| 8 | PG_LO | PG_LO_B | Brown |
| 9 | INH_HI | INH_HI_B | Black |
| 10 | INH_LO | INH_LO_B | Green |

### 10.3 PARALLELING BUSBARS (ONLY FOR MODULES E-J)



## NOTES:

For load current over 100 A use two or more cables connected to separate busbars.
2 By using busbars (paralleling modules) you will need also the signal output leads LPM000-LEAD-04, one for each connected module.
The interconnection is shown in Figure 15 (especially all pins 3 - CSH and all pins 4 - Common must be reconnected).
3 Modules K-N cannot be connected in parallel.

The example of LPM409-0000EE-DXXX assembly configuration (two modules in parallel) is shown in Figure 13.
One set of busbars LPM000-BBAR-07 + 2x LPM-LEAD-04 are necessary for connection.


Figure 13. LPM409-0000EE-DXXX Parallel Configuration
Figure 14. LPM409-000EEE-DXXX Parallel Configuration
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An example of an LPM409-0EEE-DXXX assembly configuration (three modules in parallel) is in Figure14. Two sets of busbars LPM000-BBAR-07 + one set of LPM000-BBAR-08 + 3x LPM-LEAD-04 are necessary to make the interconnections.

Note, for odd number of modules in parallel it is necessary to also use one set of busbar washers LPM000-BBAR-08.
To connect the load for higher output currents, use more cables for each pole. For example, for 200 A output current (LPM409-EEEE-DXXX) connect minimal two power cables for each pole ( $2 \times 42 \mathrm{~mm}^{2}$ for one pole). Use min. $105^{\circ} \mathrm{C}$ thermal class isolation.


Figure 15. LPM409 - Paralleling Connection Diagram for LPM126 Modules
10.4SERIAL BUSBARS (ONLY FOR MODULES E-J)
ITEM $\quad$ DESCRIPTION


Figure 16. LPM409-000EEE-DXXX Serial Configuration


Figure 17. LPM409 - Serial Connection Diagram for LPM126 Modules

## For more information on these products consult: tech.support@psbel.com

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