

#### **FEATURES**

- Industry standard MSA 2x5 footprint
- IPC Class III assembly and construction
- Industrial Temp range -40C to +85C operational
- All metal construction for rugged environments
- Optional Parylene C conformal coating
- Optional AC coupled I/O for TD and RD signals
- TX Disable and RX Signal Detect pins
- Optional RX data squelch on Signal Detect deassert
- Separate +3.3 V power pins per TX/RX port
- Industry standard duplex multimode LC receptacle
- Full compliance to IEEE and ANSI requirements
- EN-60825/ IEC-825 / CDRH Class 1 Compliant

## **BLOCK DIAGRAM**



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MIL-SxK-ST11x (DC Coupled) MIL-SxK-ST31x (AC Coupled) MIL-SxK-ST41x (DC Coupled, Jam) MIL-SxK-ST61x (AC Coupled, Jam) MILITARY SFF OPTICAL TRANSCEIVER

Gigabit Ethernet / 1x Fibre Channel Applications 3.3V, 850nm VCSEL, Multimode, Up to 550M

#### APPLICATIONS

The MIL-SxK-STxxx multimode optical fiber transceivers provide low profile, cost effective solutions for Gigabit Ethernet and 1x Fibre Channel multimode optical fiber data links with a duplex LC connector interface.

These transceivers are fully compliant with the IEEE Gigabit Ethernet and 1x Fibre Channel standards but can be used for any other data communications purpose within their operating parameters.

#### DESCRIPTION

The optic transceivers consist of transmitter and receiver functions combined in a Small Form Factor (SFF) module. The optical transmitter is a high ouput 850nm VCSEL. The transmitter input lines are driven with differential LVPECL signals applied to the Transmit (TX+ and TX-) pins. These signals are internally converted to a suitable modulation current by a CMOS integrated circuit. A Transmit Disable (TDIS) function is provided to enable control of the VCSEL optical output.

The optical receivers consist of PIN and Preamplifier assemblies and CMOS limiting post-amplifier integrated circuits. Outputs from the receivers consist of differential CML data signals on the Receive (RX+ and RX-) pins and a single ended LVTTL signal detect function on the Signal Detect (SD) pin. For the -ST41 and -ST61 options, the RX data is squelched (JAM) upon Signal Detect deassert to prevent garbage data output when no optical signal is present.

MIL - S	X	K	- S T X X	X
Product Family	Shell Options	Application	Wavelength, Fiber, Coupling, Jam Options	Temperature and Coating
MIL-S= Military SEE	N= No GND Tabs	<b>K=</b> GiaE / FC	ST11= 850 nm MM, DC coupled	H= -40 to 85 C.
(IPC Class III)	(Flat Shell)	(1.0625 - 1.25Gbps)	ST31= 850nm MM, AC coupled	No Coating
	<b>T=</b> GND Tabs		ST41= 850nm MM, DC coupled, JAM	<b>M=</b> -40 to 85 C,



ST61= 850nm MM, AC coupled, JAM

Coating

Gigabit Ethernet / 1x Fibre Channel Applications

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## **ABSOLUTE MAXIMUM RATINGS**

Absolute maximum limits mean that no catastrophic damage will occur if the product is subjected to these ratings for short periods, provided each limiting parameter is in isolation and all other parameters have values within the performance specification. It should not be assumed that limiting values of more than one parameter can be applied to the product at the same time.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Storage Temperature	Τ <sub>s</sub>	-55		+100	°C
Lead Soldering Temperature	T <sub>SOLD</sub>			+260	°C
Lead Soldering Time	t <sub>sold</sub>			10	Seconds
Supply Voltage	V <sub>cc</sub>	-0.5		+4.5	V
Data Input Voltage	V	-0.5		V <sub>cc</sub>	V
Differential Input Voltage (p-p)	V <sub>D</sub>			2.2	V
Output Current	Ι <sub>ο</sub>			50	mA

#### **RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating Temperature Limit	T <sub>A</sub>	-40		+85	°C
Supply Voltage	V <sub>cc</sub>	+3.135		+3.465	V
TX Common Mode Voltage	V <sub>CM</sub>		2.0		V
TX Differential Input Voltage (p-p)	V <sub>D</sub>	0.20		2.20	V
Transmit Disable Voltage	V <sub>TD</sub>	2.0		V <sub>cc</sub>	V
Transmit Enable Voltage	V <sub>ten</sub>	V <sub>EE</sub>		0.8	V
RX Data Output Load	RL		50		Ohms

## **DETAIL OF DATA I/O STAGES**







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## TRANSMITTERS

VCCTX = 3.135V to 3.465V,  $T_{A}$  = Operating Temperature Range

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Optical Output Power <sup>1</sup>	P。	-9.5		-4	dBm
Optical Output Wavelength	λ <sub>ουτ</sub>	830	850	860	nm
Spectral Width	$\Delta\lambda_{\rm RMS}$			0.85	nm
Extinction Ratio	ER		10		dB
Supply Current	I <sub>cc</sub>		55	75	mA
Optical Rise / Fall Time (20% - 80%)	t <sub>R,F</sub>			0.26	nS
RIN				-117	dB/Hz
Coupled Power Ratio	CPR	9			dB
Total Jitter <sup>1</sup>			80	153	pS

1. BER=10<sup>-12</sup> @ 1.25Gbps, PRBS 27-1, NRZ, Compliant with ANSI X3.297 / FC-PH-2

## **RECEIVERS** VCCRX = 3.135V to 3.465V, T<sub>A</sub> = Operating Temperature Range

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Optical Sensitivity, FC (1.0625Gbps) <sup>1</sup> Optical Sensitivity, GE (1.25Gbps) <sup>1</sup>	P,	-22 -21	-23 -22	0 0	dBm dBm
Optical Input Wavelength	$\lambda_{IN}$	770		860	nm
Optical Return Loss	ORL	12			dB
Supply Current	I <sub>cc</sub>		70	120	mA
Signal Detect Assert Time	t <sub>sdas</sub>		<10	100	μS
Signal Detect Deassert Time	t <sub>sdda</sub>		<10	350	μS
Signal Detect Deassert Level <sup>2</sup>	SD	-31			dBm
Signal Detect Assert Level	SD <sub>ON</sub>			-21	dBm
Signal Detect Hysteresis	HYS	1.5	2.25	3.5	dB
RX Data Output - Low	$V_{OL}V_{CC}$	-1.810		-1.475	V
RX Data Output - High	V <sub>OH-</sub> V <sub>CC</sub>	-1.165		-0.880	V

1. BER=10<sup>-12</sup> @ , PRBS 2<sup>7</sup>-1, NRZ, Compliant with ANSI X3.297 / FC-PH-2

2. RX Data outputs are squelched when Signal Detect is deasserted to prevent garbage data output when no optical signal is present.

## LINK DISTANCES

Application	Fiber Specification	Distance
Gigabit Ethernet - IEEE 802.3z	62.5/125 - 160MHz*Km	220M
	62.5/125 - 200MHz*Km	275M
	50/125 - 400MHz*Km	500M
	50/125 - 500MHz*Km	550M
Eibro Channal ANSI X2 207	62.5/125 - 160MHz*Km	300M
	50/125 - 500MHz*Km	500M



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## **CONFORMAL COATING OPTION**

Parameter	Value
Specification	MIL-I-46058C, Type XY
Coating:	Parylene type C
Deposition:	Vacuum deposited
Film Tickness:	1 MIL +/- 0.0002

#### **REGULATORY COMPLIANCE**

Requirement	Feature	Condition	Notes
MIL-STD-883-3015.7	ESD	Class II	2200V
IEC-801-2	ESD	Human Body Model	25KV
IEC-801-3	EMI	Immunity	10V/M
FCC	EMI	Class B	>20dB
EN 55022 (CISPR 22A)	EMI	Class B	10V/M
IEC-825 Issue 1993-11	Eye Safety	Class 1	TUV Certificate Number PENDING
FDA CDRH 21-CFR 1040	Eye Safety	Class 1	CDRH Accession Number PENDING





File Number: Pending



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## MILITARY SMALL FORM FACTOR TRANSCEIVER PIN NUMBER ASSIGNMENTS TOP VIEW SHOWN



#### **PIN FUNCTIONS**

Pin Number	Symbol	Description	Logic Family
MS	MS	Mounting Studs Connect to chassis ground	N/A
1	VEERX	Receiver Signal Ground	N/A
2	VCCRX	Receiver Power Supply	N/A
3	SD	Signal Detect (1= dectect, 0= no signal)	LVTTL
4	RD-	Receive Data Out -	CML
5	RD+	Receive Data Out +	CML
6	VCCTX	Transmit Power Supply	N/A
7	VEETX	Transmit Signal Ground	N/A
8	TDIS	Transmit Disable (1= disable, 0= enable) internal 20Kohm pull-down (enable)	LVTTL
9	TD+	Transmit Data In +	CML
10	TD-	Transmit Data In -	CML



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#### Notes:

1) L1 and L2 = MuRata BLM21A601S or equivalent ( $600\Omega$  at 100MHz or better).

2) Route the differential pairs (TD +/- and RD +/-) together using  $50\Omega$  impedance matched traces.

3) Use separate power supply filtering for VCCTX and VCCRX, as shown.

4) Use low ESR capacitors such as NPO or COG for AC Coupling of the TD+/- and RD+/- data signals. These capacitors are not required for the MIL-SxK-ST31x or MIL-SxK-ST61x device (this device includes internal 0.047uF capacitors on the TD and RD data lines).

5) Ground Posts (GP) are isolated from Signal Ground (Vee), and may be connected to Chassis Ground (as shown) or to Signal Ground if a Chassis Ground is not available.

6) A CML compliant DC I/O interface is possible for the RJL-ST11H-SO51A (no internal capacitors). Care sould be taken with the external CML interface to reference the CML levels to the same Vcc as used for the transcevier.



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## MSA 2x5 SFF TRANSCEIVER PRINTED CIRCUIT BOARD LAYOUT Top View Shown

Dimensions Are Shown As: mm inches



CROSS-HATCHED AREAS SHOULD HAVE NO SIGNAL TRACES ON THE TOP LAYER



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#### PART NUMBER SUMMARY AND OPTIONS

Part Number	Flat Shell	Clip Shell	DC	AC	JAM	No JAM	Conf Coat
MIL-SNK-ST11H	X		Х			Х	
MIL-SNK-ST11M	X		Х			Х	X
MIL-SNK-ST31H	X			X		Х	
MIL-SNK-ST31M	X			X		Х	X
MIL-SNK-ST41H	X		Х		X		
MIL-SNK-ST41M	X		Х		X		X
MIL-SNK-ST61H	X			Х	Х		
MIL-SNK-ST61M	X			Х	Х		X
MIL-STK-ST11H		X	Х			Х	
MIL-STK-ST11M		X	Х			Х	X
MIL-STK-ST31H		Х		Х		Х	
MIL-STK-ST31M		X		X		Х	X
MIL-STK-ST41H		X	Х		X		
MIL-STK-ST41M		X	Х		X		X
MIL-STK-ST61H		X		X	X		
MIL-STK-ST61M		X		X	X		X

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