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PATENTED: THIS PRODUCT IS **COVERED BY U.S. PATENT 5,736,910**

COMPONENTS FOR A CONNECTED PLANET®

DOCUMENT / PART NO.: SI-52003-F

TITLE: 10/100BT, PoE; TAB UP, SHIELDED

ISSUE	DESCRIPTION OF CHANGE									REVIEWED BY				APPROVED BY				N#		
Α0	USE NEW FORMAT AND PRODUCTION RELEASE									David Chan				Danny Kwan				06257		
A 1	Update the circuit drawing									Bain Liu			Steve Weller			06383				
									-											
PAGE	1	2	3	4																
REV.	A1	A1	A1	A1																
PAGE															C5	5128-F REV A2				
REV.		-								V AO										

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ELECTRICAL CHARACTERISTICS @ 25°C

1.0 TURNS RATIO: (P1-P3): (J1-J2) 1CT : 1CT ±2% (P4-P5): (J3-J6) 1CT : 1CT ±2%

(JT-J2): (J3-J6) 1.2 OHMS

3.0 INDUCTANCE:

(P1-P3) 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS (P4-P5) 350uH MIN. @ 0.1V, 100KHz, 8mA DC BIAS

4.0 LEAKAGE INDUCTANCE: (P4-P5) WITH J6 AND J3 SHORT 0.3uH MAX. @ 1MHz (P1-P3) WITH J2 AND J1 SHORT 0.3uH MAX. @ 1MHz

5.0 INTERWINDING CAPACITANCE: (P4,P5) TO (J3,J6) 60pF TYP **②** 1MHz

(P1,P2,P3) TO (J1,J2) 60pF TYP @ 1MHz

6.0 RETURN LOSS: (P1-P3)=100 OHMS REF. AND (P4-P5)=100 OHM REF.

1MHz TO 30MHz
30MHz TO 60MHz
-(19-20LOG(f/30MHz))

30MHz TO 60MHz —(19—20LOG(f/30MHz)) 60MHz TO 80MHz 12dB MIN.

7.0 DIELECTRIC WITHSTAND:
(J3,J6) TO (P4,P5) 1500Vms
(J1,J2) TO (P1,P3) 1500Vms

8.0 INSERTION LOSS: RS=RL=100 OHMS
1MHz-65MHz -1dB MAX

1MHz-65MHz -1dB MAX
9.0 RISE TIME: RS=RL=100 OHMS

9.0 RISE TIME: RS=RL=100 OHMS OUTPUT VOLTAGE = 1V PEAK PULSE WIDTH = 112nS 3.0nS MAX 3.0nS MAX

10.0 CROSS TALK: 1MHz-65MHz -35dB MIN.

12.0 BALANCED DC LINE CURRENT: 350mA MAX @ 57 VDC CONTINUOUS 500mA MAX @ 57 VDC FOR 200 MILLSECONDS

1.0 DESIGNED TO SUPPORT IEEE

NOTES

PINS

TD+ P1

TD- P3

RD+ P4

RD- P5

VC+ P8

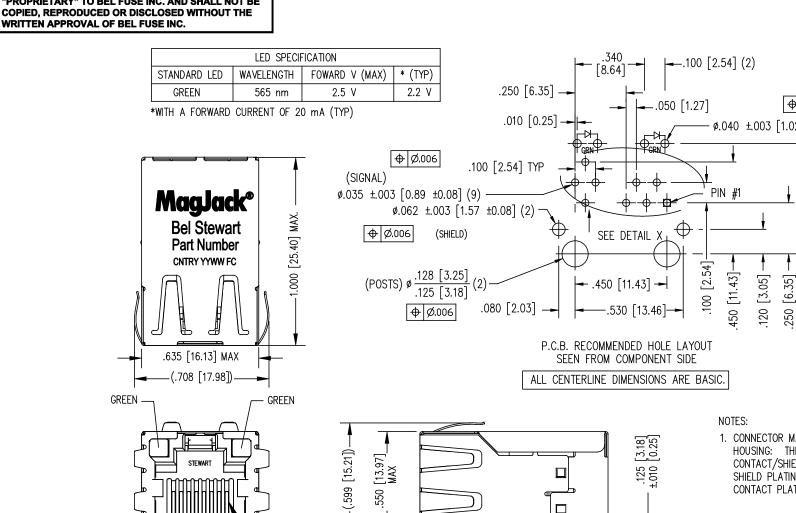
VC- P9

VC+ P10

VC- P11

COMMON CT P2

ORIGINATED BY PART NO. / DRAWING NO. DATE TITLE STANDARD DIM. **MagJack®** SI-52003-F **TOL. IN INCH** Zeng Xiaochun SI-52003-F 10/100BT, PoE X. DRAWN BY DATE FILE NAME TAB UP, SHIELDED XX. SI-52003-F.DWG SI-52003-F **Duan Jun PATENTED** ±0.005 THE INFORMATION CONTAINED HEREIN IS CONSIDERED "PROPRIETARY" TO BEL FUSE INC. AND SHALL NOT BE COPIED, REPRODUCED OR DISCLOSED WITHOUT THE



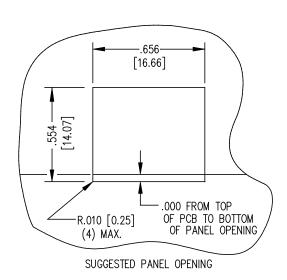
- 1. CONNECTOR M. HOUSING: THI CONTACT/SHIE SHIELD PLATIN
 - 2. PIN NOT ELEC' SEE ELECTRICA
- 3. TOLERANCES C
- 4. ALL TOLERANC
- REFLOW AND 10 SECONDS N

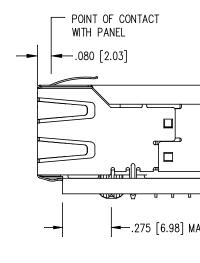
ORIGINATED BY DATE TITLE PART NO. / DRAWING NO. STANDARD DIM. **TOL. IN INCH** SI-52003-F Zeng Xiaochun SI-52003-F 10/100BT, PoE X. DRAWN BY DATE FILE NAME XX. TAB UP, SHIELDED SI-52003-F.DWG SI-52003-F **PATENTED Duan Jun** ±0.005

PIN: J1

.425 [10.79]

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- THE SUGGESTED PANEL OPENING IS INTO
 TO GIVE THE USER THE ABILITY TO I

 REASONABLE JACK / PANEL CLEARA
 YET MAINTAIN RELIABLE GROUNDING
 CAPABILITY.
- ALL TOLERANCES NOT OTHERWISE SPEC
 TO BE ±005 [0.13]

ORIGINATED BY	DATE
Zeng Xiaochun	SI-52003-F
DRAWN BY	DATE
Duan Jun	SI-52003-F

MagJack®
10/100BT, PoE
TAB UP, SHIELDED

PATENTED

TITLE

PART NO. / DRAWING NO.

\$1-52003-F

FILE NAME

\$1-52003-F.DWG

STANDARD DIM.

TOL. IN INCH

.X

.XX

.XX

.XXX

±0.005

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