## BL8023F

## 300mA Bi－Direction Relay Driver

## DESCRIPTION

BL8023F is a bi－direction relay driver circuit，used to control the magnetic latching relay，with large output capability，ultra－low power consumption． It can be widely used in smart meters and other pulses，level control applications．

BL8023F can provide 300 mA typical driving current，which will different according to the relay coil resistance．The input High Level Threshold of BL8023F is 2 V ；it can compatible with most single chip microcontroller．

BL8023F is available in SOT－23－6 and SOP－8 packages．

## TYPICAL APPLICATION



ORDERING INFORMATION

| Part No． | Package | Tape \＆Reel |
| :---: | :--- | :--- |
| BL8023FCB6TR | SOT－23－6 | $3000 /$ Reel |
| BL8023FCD8TR | SOP－8 | $2500 /$ Reel |

## FEATURES

－ 5 to 36 V input voltage range
－Low Power Consumption（IQ＜1uA）
－Input High Level Threshold：2V，compatible with most single chip microcontroller
－Typical Driving Current： 300 mA
Rds（on）$=12 \mathrm{ohm}$（Vin＝12V，PMOSFET＋NMOSFET）
Rds（on）$=10 \mathrm{ohm}$（Vin＝30V，PMOSFET＋NMOSFET）
－Peak Driving Current：500mA＠Vin＝24V
－Environment Temperature：$-40^{\circ} \mathrm{C}^{\sim} 85^{\circ} \mathrm{C}$
－SOT－23－6 and SOP－8 packages

## APPLICATIONS

－Smart Meter


PIN OUT \＆MARKING


SOP－8／DIP8

8023S：Product Code
LL：Lot No．
L：Fab code
LL：Date code

## BL8023F

## ABSOLUTE MAXIMUM RATING

| Parameter |  | Value |
| :--- | :--- | :--- |
| Max Input Voltage |  |  |
| Max Operating Junction Temperature(Tj) | 40 V |  |
|  | SOT23-6 | $\theta_{j a}$ |
|  |  | $\theta_{j c}$ |
|  | $150^{\circ} \mathrm{C}$ |  |

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

## RECOMMENDED WORK CONDITIONS

| Parameter | Value |
| :--- | :---: |
| Input Voltage Range | Max.36V |
| Operating Junction Temperature(Tj) | $-40^{\circ} \mathrm{C}-85^{\circ} \mathrm{C}$ |

## ELECTRICAL CHARACTERISTICS

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| VIN | Input Voltage Range |  | 5 |  | 36 | V |
| $1 q$ | Quiescent Current |  |  |  | 1 | uA |
| Rdson | Switch Rdson | $\mathrm{Vin}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 12 | 18 | ohm |
|  |  | Vin $=30 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 10 | 16 | ohm |
|  |  | $\mathrm{Vin}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=40 \mathrm{ohm}$ |  | 12 | 20 | ohm |
|  |  | Vin $=30 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=400 \mathrm{hm}{ }^{1}$ |  | 10 | 16 | ohm |
| $\mathrm{V}_{\text {TH }}$ | ON Input High Voltage | Vin $=12 \mathrm{~V}$ |  | 2 |  | V |
| $\mathrm{R}_{\text {IN }}$ | Equivalent Input Resistor |  |  | 500 |  | Kohm |
| $\mathrm{V}_{\text {S }}$ | Fly-Wheel Diode Forward Voltage | $\mathrm{Is}=1 \mathrm{~A}$ |  | 0.8 | 1.5 | V |
| $\mathrm{T}_{\mathrm{R}}$ | Rise Time | $\mathrm{VIN}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 40 |  | ns |
| $\mathrm{T}_{\mathrm{D} \text { (ON) }}$ | Turn ON Delay Time | $\mathrm{VIN}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 60 |  | ns |
| $\mathrm{T}_{\mathrm{F}}$ | Fall Time | $\mathrm{VIN}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 30 |  | ns |
| $\mathrm{T}_{\text {DIOFF }}$ | Turn OFF Delay Time | $\mathrm{VIN}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=75 \mathrm{ohm}$ |  | 70 |  | ns |

Note: 1) This condition is not suitable for SOT23-6 package.
2) Input rise/fall time must less than 1 ms , otherwise maybe destroy the chip.

## LOGIC FUNCTION TABLE

| Input A | Input B | Output OA | Output OB | RELAY RESPONSE |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 1 | 0 | ON |
| 0 | 1 | 0 | 1 | OFF |
| 0 | 0 | High-impedance | High-impedance | Hold |
| 1 | 1 | High-impedance | High-impedance | Hold |

## BL8023F

PIN DESCRIPTION

| NAME | PIN \# |  |  |
| :---: | :---: | :---: | :--- |
|  | SOT23-6 | SOP-8 |  |
| OA | 4 | 1 | Output A |
| NC | - | 2,6 | Not connected. |
| A | 3 | 3 | Input A |
| GND | 2 | 4 | Ground. |
| OB | 1 | 5 | Output B |
| B | 6 | 7 | Input B |
| VIN | 5 | 8 | Supply input voltage |

## ELECTRICAL PERFORMANCE

Tested under $T A=25^{\circ} \mathrm{C}$, unless otherwise specified

Turn on delay and rise time
Ch1---Input Ch2---Output



## BLOCK DIAGRAM



## DETAILED DESCRIPTION

## Pulse Triggering

If input is driven by square pulse, connect the inputs to the pulse source directly. Relay will operate as logic table stated (Vin should be less than the power supply voltage, Rs is current-limiting resistor, it can be ignored in the voltage is below 20V, i.e. $\mathrm{Rs}=0$ ).

The recommended pulse width $=100 \mathrm{~ms}$. The length of the intervals should be longer than 100 ms . These intervals include: intervals between forward drive pulse and next backward drive pulse, intervals between forward drive pulse and next forward drive pulse, intervals between backward drive pulse and next forward drive pulse, intervals between backward drive pulse and next backward drive pulse.


## BL8023F



Pulse triggering application diagram

## Relay free-wheel

Relay from ON to OFF, the energy stored in the relay inductor released by the chip's internal body diode and the relay inductor. Until the end of the release of this energy, relay proceeding to the next operation.

## PACKAGE OUTLINE

Package 12 SOT23-6

## BL8023F



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