

Version: 1.0 Document code: HB-WI7.3-715-2021 Effective date : 2021-06-25 Page: 1 of 3

#### SMD1812-750-12V

RoHS 📚

#### Feature

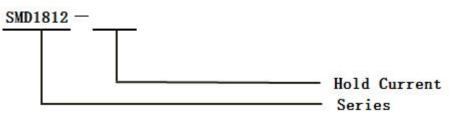
- Resettable over current and over temperature protection
- Small size of 1812
- Fast time-to-trip
- Small footprint
- RoHS complaint
- Low resistance

#### Application

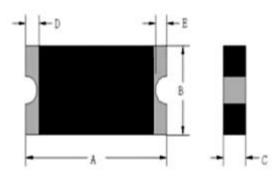
- Computer
- Battery
- Mobile phones

- Industrial controls
- Automotive
- Portable electronics
- Multimedia
- Game machines
- Telephony and broadband

## Part Numbering



## Product Dimensions in Millimeter



Part Number		A		В		С		D		E	
	Min	Max	Min	Мах	Min	Max	Min	Max	Min	Мах	
SMD1812-750-12V		4.80		3.50		1.2	0.25		0.10		

## **Electrical Characteristics**

	I(A)		V <sub>max</sub>	I <sub>max</sub>	$\mathbf{Pd}_{typ}$	T <sub>trip</sub>		R <sub>min</sub>	R <sub>1max</sub>
Part Number	<b>25</b> ℃					<b>25</b> ℃		<b>25</b> ℃	
	Hold	Trip	(V)	(A)	(W)	Current(A)	Time(S)	(Ω)	(Ω)
SMD1812-750-12V	7.5	15.0	12.0	50	2.0	30.0	5.0	0.0005	0.007

**Polymer PTC Thermistor** 

Specifications are subject to change without notice

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Version: 1.0 Document code: HB-WI7.3-715-2021 Effective date : 2021-06-25 Page: 2 of 3

#### Surface-Mount Device

#### SMD1812-750-12V

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I<sub>H</sub>=Hold current: maximum current at which the device will not trip at  $25^{\circ}$ C still air reflow soldering of  $260^{\circ}$ C for 20 sec. I<sub>T</sub>=Trip current: minimum current at which the device will always trip at  $25^{\circ}$ C still air reflow soldering of  $260^{\circ}$ C for 20 sec. V<sub>max</sub>=Maximum continuous voltage device can withstand without damage at rated current

Imax=Maximum fault current device can withstand without damage at rated voltage.

 $T_{trip}$ =Maximum time to trip(s) at assigned current reflow soldering of 260  $^\circ$ C for 20 sec.

Pd<sub>typ</sub>=Typical power dissipation: typical amount of power dissipated by the device when in state air environment.

R<sub>min</sub>= Minimum resistance of device in initial (un-soldered) state.

 $R_{1max}$ =Maximum resistance of device at 25 °C measured one hour after reflow soldering of 260 °C for 20 sec.

Value specified is determined by using the PWB with 0.030 '\*1.5oz copper traces.

Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.

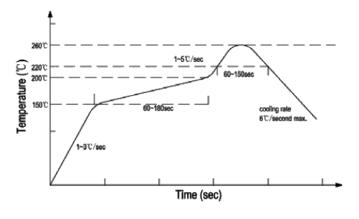
#### **Environmental Specifications**

Test	Test Conditions	Accept /Reject Criteria		
Recommended storage conditions	40°C max, 70% R.H. max	No change		
Passive aging:	85°C, 1000 hours	≤ R <sub>1max</sub>		
Moisture Resistance	85% RH,85℃,1000hrs	≤ R <sub>1max</sub>		
Thermal Shock	MIL-STD-202 Method 107G +85ºC /-40ºC 20 times	≤ R <sub>1max</sub>		
Vibration	MIL-STD-883C, Method 2007.1, Condition A	No change		
Solvent Resistance	MIL-STD-202, Method 215	No change		
Moisture Level Sensitivity	Level 1, J-STD-020C	No change		

#### **Thermal Derating** [Hold Current (A) at Ambient Temperature (°C)]

Dout Number	Maximum Ambient Operating Temperature ( $^\circ\!\mathbb{C}$ )								
Part Number	-40	-20	0	25	40	50	60	70	
SMD1812-750-12V	12.0	10.4	8.8	7.5	6.6	6.1	5.6	5.1	

#### Solder Reflow Recommendation



Reflow --curve



#### Surface-Mount Device

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Recommended reflow methods:IR,hot air oven ,nitrogen oven Devices can be cleaned using standard industry methods and solvents. **NOTE:** 

If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

Caution: Operation beyond the rated voltage or current may result in rupture electrical arcing or flame

### Packaging Quantity and Marking

Device	Marking	Standard Quantity (pcs)			
SMD1812-750-12V		1500			

#### NOTE:

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## CAUTION:

Operation beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame. The devices are intended for protection against occasional over-current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated. Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.

#### **Contact information**

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Rev. letter		Date	
Design	Check	Audit	Approve

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