

Features

- 50 Watts Peak Power per Line ($t_p = 8/20\mu s$)
- Protects two I/O lines
- Low operating voltage: 5V
- Low Leakage Current
- Solid-state technology

IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD) $\pm 15kV$ (air), $\pm 10kV$ (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 3A (8/20 μs)



DFN-3L

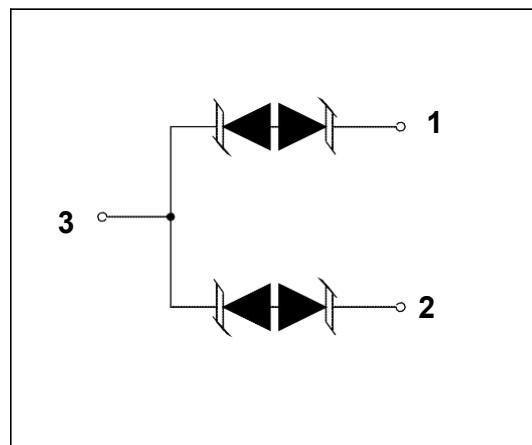
Mechanical Characteristics

- JEDEC DFN-3L package
- Molding compound flammability rating: UL 94V-0
- Marking : Making Code
- Packaging : Tape and Reel per EIA 481
- RoHS Compliant

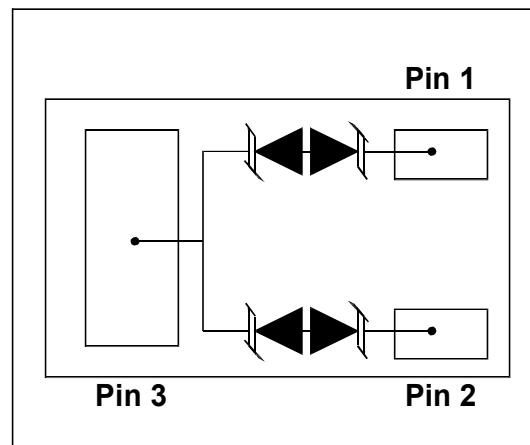
Applications

- FireWire & USB
- Sensitive Analog Inputs
- Portable Electronics
- LAN/WAN equipment
- Video Line Protection
- Microcontroller Input Protection

Circuit Diagram



Schematic & PIN Configuration

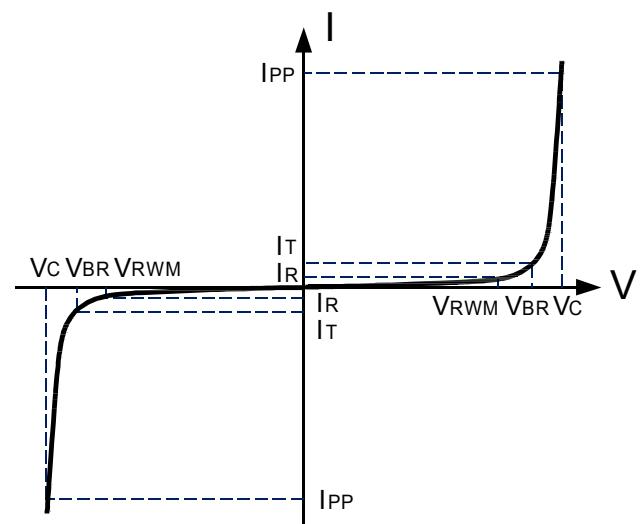


Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p=8/20\mu s$)	P_{PP}	50	Watts
Peak Pulse Current ($t_p=8/20\mu s$)	I_{PP}	3	A
Lead Soldering Temperature	T_L	260(10sec)	°C
Operating Temperature	T_J	-55 to + 125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Parameters (T=25°C)

Symbol	Parameter
I_{PP}	Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Reverse Stand-Off Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current

**Electrical Characteristics**

BDFN3C052L						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}				5.0	V
Breakdown Voltage	V_{BR}	$I_T=1mA$	5.6			V
Reverse Leakage Current	I_R	$V_{RWM}=5V, T=25^\circ C$			500	nA
Clamping Voltage	V_C	$I_{PP}=1A, t_p=8/20\mu s$			9.5	V
Clamping Voltage	V_C	$I_{PP}=3A, t_p=8/20\mu s$		13	15	V
Junction Capacitance	C_j	Between pin3 and pin1/ pin2, $V_R=0V$, $f=1MHz$		3	3.5	pF
		Between pin1 and pin2, $V_R=0V$, $f=1MHz$		1.5	2.0	pF

Typical Characteristics

Fig.1 Peak Pulse Power Rating Curve

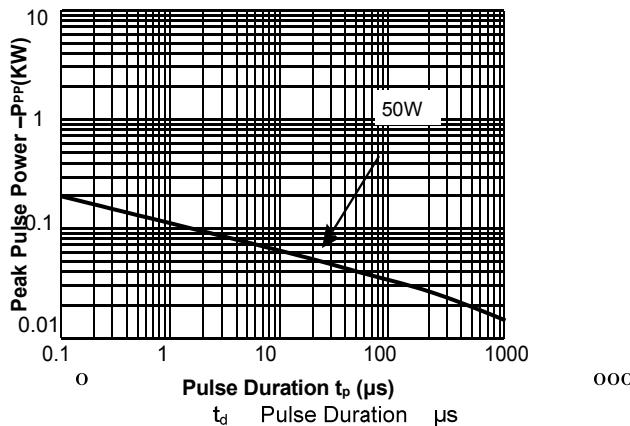


Figure 2: Power Derating Curve

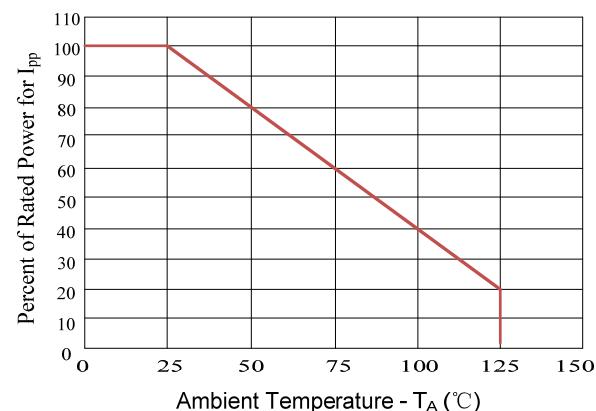


Figure 3: Pulse Waveform

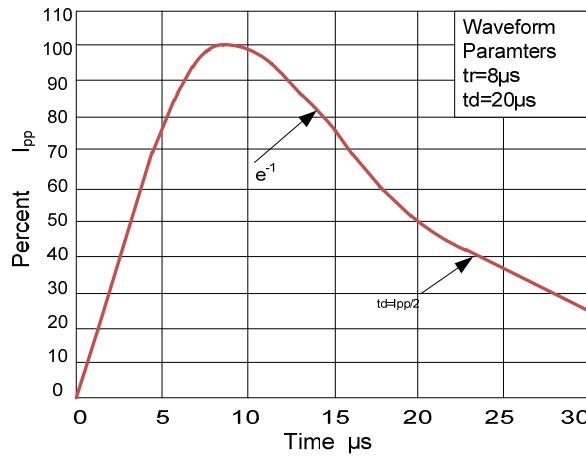
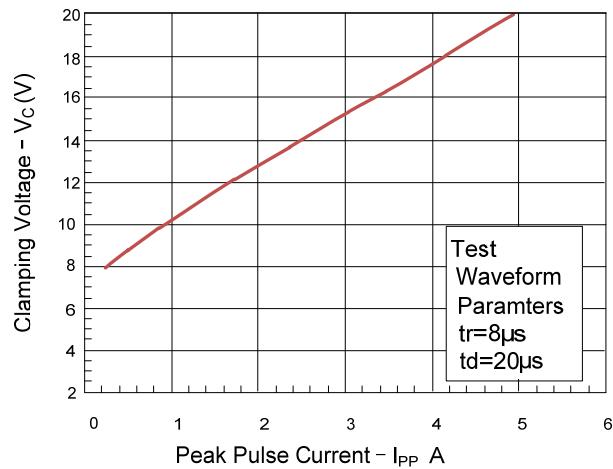
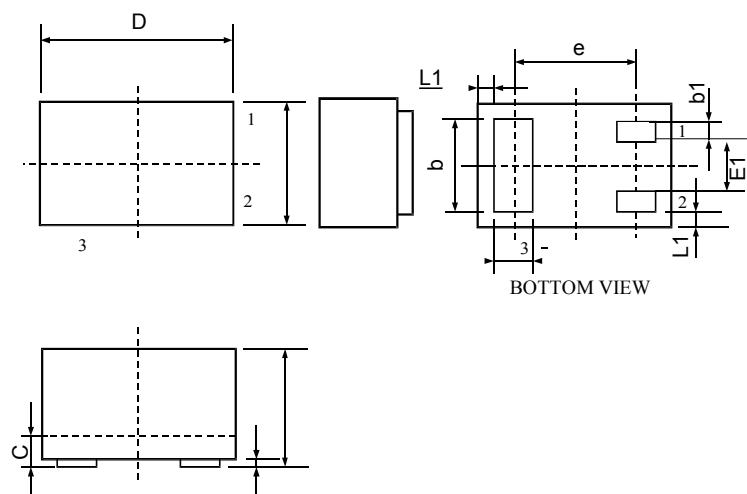
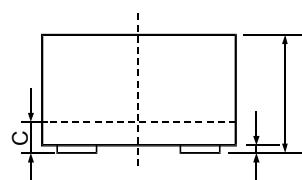
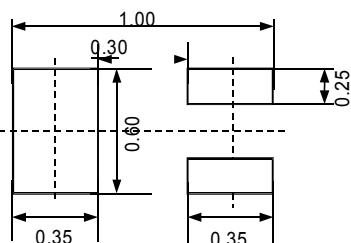


Figure 4: Clamping Voltage vs.Ipp



Outline Drawing – DFN-3L

PACKAGE OUTLINE			
			
		DFN-3L	
SYMBOL		MILIMETER	
		MIN	NOM
A		0.41	0.45
A1		0	0.02
b		0.45	0.50
b1		0.10	0.15
C		0.12	0.15
D		0.95	1.00
e		0.65BSC	
E		0.55	0.60
E1		0.15	0.20
L		0.20	0.25
L1		0.05REF	

Land Pattern**Marking Codes**

Part Number	BDFN3C052L	Marking Code	MF
-------------	------------	--------------	----

Package Information

Qty: 10k/Reel

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for ESD Suppressors / TVS Diodes category:

Click to view products by Bourne manufacturer:

Other Similar products are found below :

[60KS200C](#) [D18V0L1B2LP-7B](#) [D5V0F4U5P5-7](#) [DESD5V0U1BB-7](#) [NTE4902](#) [P4KE27CA](#) [P6KE11CA](#) [P6KE39CA-TP](#) [P6KE8.2A](#)
[SA110CA](#) [SA60CA](#) [SA64CA](#) [SMBJ12CATR](#) [SMBJ33CATR](#) [SMBJ8.0A](#) [ESD101-B1-02ELS](#) [E6327](#) [ESD105-B1-02EL](#) [E6327](#) [ESD112-B1-02EL](#) [E6327](#) [ESD119B1W01005E6327XTSA1](#) [ESD5V0L1B02VH6327XTSA1](#) [ESD7451N2T5G](#) [19180-510](#) [CPDT-5V0USP-HF](#)
[3.0SMCJ33CA-F](#) [3.0SMCJ36A-F](#) [HSPC16701B02TP](#) [D3V3Q1B2DLP3-7](#) [D55V0M1B2WS-7](#) [DESD5V0U1BL-7B](#) [DRTR5V0U4SL-7](#)
[SCM1293A-04SO](#) [ESD200-B1-CSP0201](#) [E6327](#) [SM12-7](#) [SMF8.0A-TP](#) [SMLJ45CA-TP](#) [CEN955 W/DATA](#) [82350120560](#) [VESD12A1A-HD1-GS08](#) [CPDUR5V0R-HF](#) [CPDQC5V0U-HF](#) [CPDQC5V0USP-HF](#) [CPDQC5V0-HF](#) [D1213A-01LP4-7B](#) [D1213A-02WL-7](#)
[MMAD1108/TR13](#) [5KP100A](#) [5KP15A](#) [5KP18A](#) [5KP48A](#) [5KP90A](#)