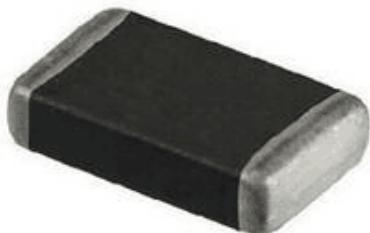


## »Features

- 0402inch/ 1005mm foot print
- Ideal ESD protection for high frequency, low voltage applications.
- Exceeds testing requirements outlined in IEC 61000-4-2
- Ultra low capacitance (0.15pF typ.)
- Very low leakage current
- Fast response time
- Bi-directional
- MSL 3



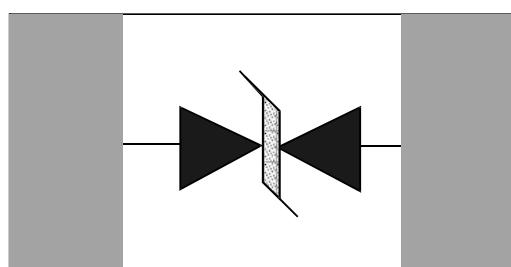
## »Applications

- Smart Phone/Mobile Internet Device
- Laptop/Desktop Computer
- Antennas
- High Speed Ethernet
- USB 2.0 and USB 3.0
- Lightning and Thunder Bolt Interface

## »Mechanical Data

- Surface mount
- RoHS Compliant

## »Schematic & PIN Configuration



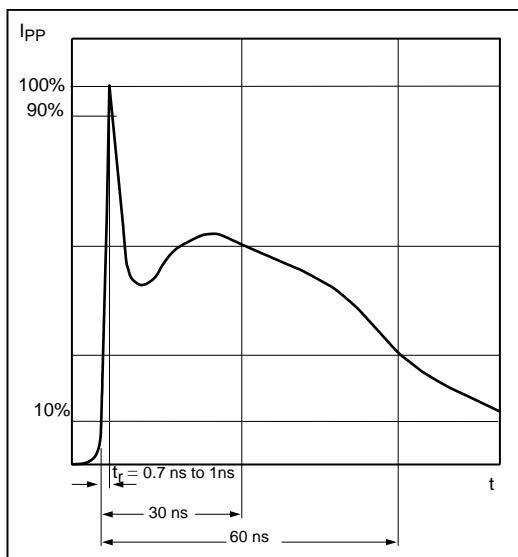
## »Absolute Maximum Rating

Rating	Symbol	Conditions	Value	Units
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>		8 15	kV
Lead Soldering Temperature	T <sub>L</sub>		260(10seconds)	°C
Operating Temperature	T <sub>O</sub>		-55 to + 125	°C
Storage Temperature	T <sub>stg</sub>		-40 to + 125	°C

## »Electrical Characteristics

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Continuous Operating Voltage	V <sub>DC</sub>				14	V
Trigger Voltage	V <sub>T</sub>	IEC61000-4-2 8KV contact discharge		450		V
Leakage Current	I <sub>L</sub>	VDC=14V,T=25 °C			200	nA
Clamping Voltage	V <sub>C</sub>	IEC61000-4-2 8KV contact discharge		40		V
Capacitance	C <sub>P</sub>	Measured at 10MHz		0.15		pF
ESD Pulse Withstand	Pulses	IEC61000-4-2 8KV contact discharge	1000			

## »ESD Wave Form



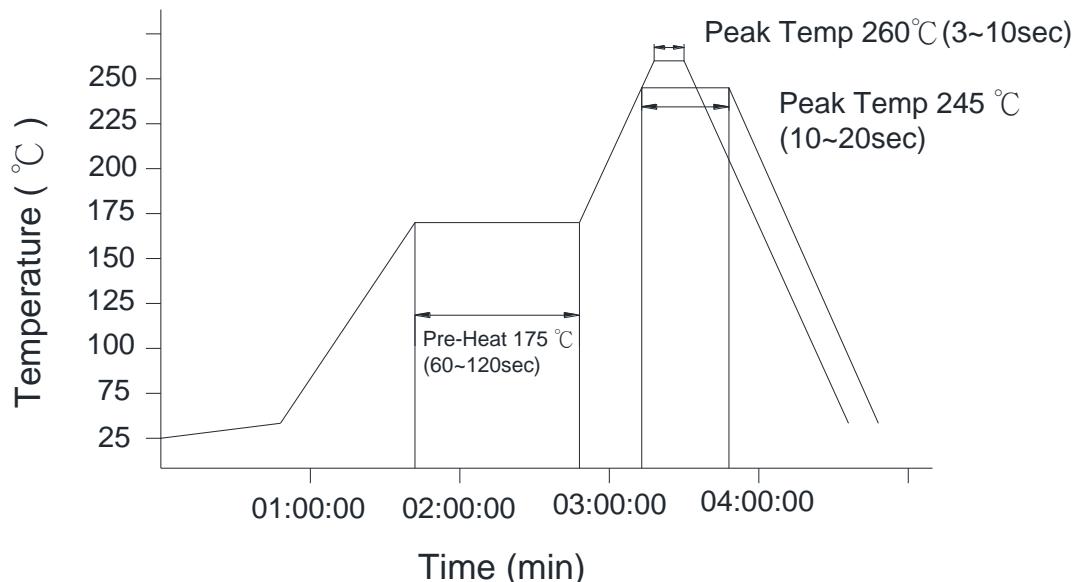
## »Electrical Characteristics

SEVERITY LEVEL	AIR DIRCHARGE	DIRECT DISCHARGE
1	2KV	2KV
2	4KV	4KV
3	8KV	6KV
4	15KV	8KV

## »Environment Reliability Test

Characteristic	Test Method and Description			
High Temperature Storage	The specimen shall be subjected to $125 \pm 2^\circ\text{C}$ for $1000 \pm 2$ hours without load and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10%.			
Temperature Cycle	The temperature cycle of specified temperature shall be repeated five times and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10% and mechanical damage shall be examined.	Step	Temperature	Period
High Temperature Load	After being continuously applied the maximum allowable voltage at $85 \pm 2^\circ\text{C}$ for $1000 \pm 2$ hours, the specimen shall be stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10%.	1	$-40 \pm 3^\circ\text{C}$	$30 \pm 3\text{min}$
Damp Heat Load/ Humidity Load	The specimen should be subjected to $40 \pm 2^\circ\text{C}$ and 90~95% RH, the maximum allowable voltage applied for $1000 \pm 2$ hours and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10%.	2	room temperature	1 hour
Low Temperature Storage	The specimen should be subjected to $-40 \pm 2^\circ\text{C}$ for $1000 \pm 2$ hours without load and then stored at room temperature and normal humidity for one or two hours. The change of varistor voltage shall be within 10%.	3	$125 \pm 3^\circ\text{C}$	$30 \pm 3\text{min}$
		4	room temperature	1 hour

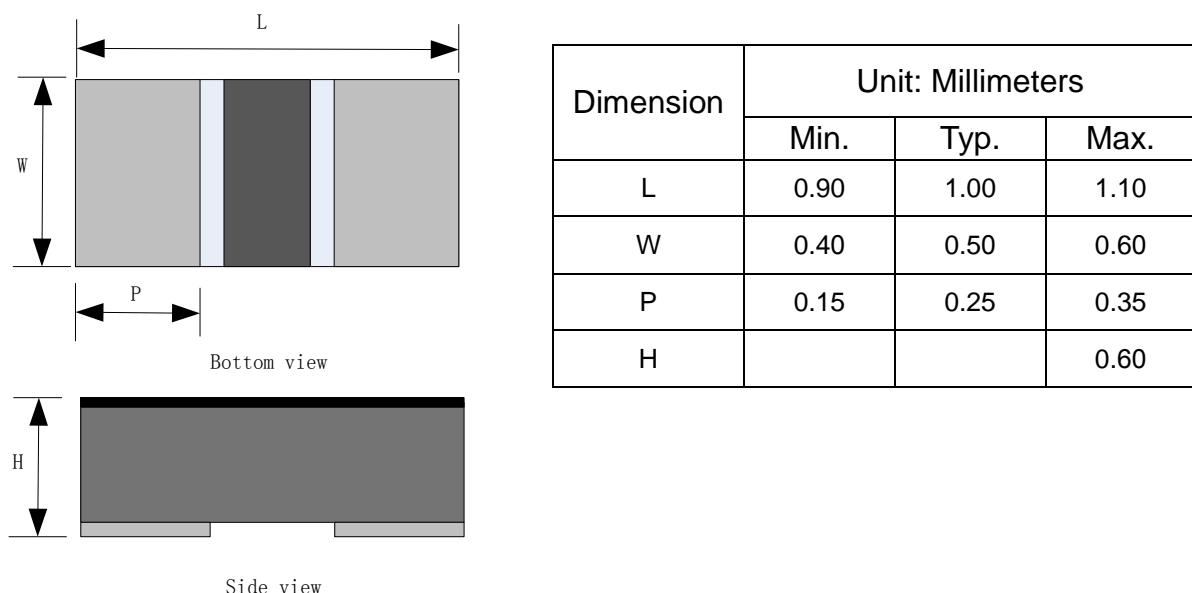
## »Soldering Parameters



### ☆ IR reflow Pb free process suggestion profile

- (1) The solder recommend is Sn96.5/Ag3.5 and thickness recommend as shown in table 5.3
- (2) Ramp-up rate (217°C to peak) +3°C/second max.
- (3) Temp. maintain at 175±25°C 180 seconds max.
- (4) Temp. maintain above 217°C 60~150 seconds

## »Outline Drawing – DFN1006



## »Ordering information

Order code	Package	Base qty	Delivery mode
BPESD0402-140	0402	10k	Tape and reel

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