

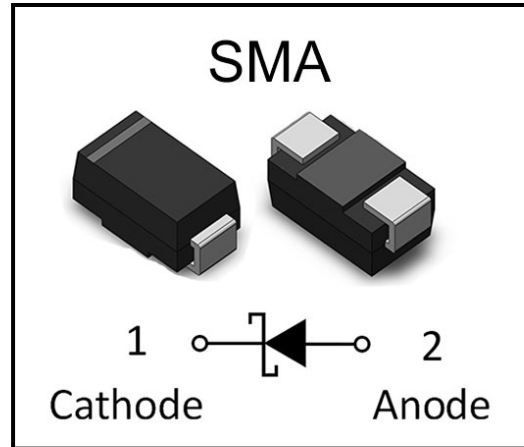
# SS22-SS26

Schottky Barrier Diode

## Features

- For surface mounted applications
- Metal silicon junction, majority carrier conduction
- Low power loss, high efficiency
- Built-in strain relief, ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed:  
250°C/10 seconds at terminals

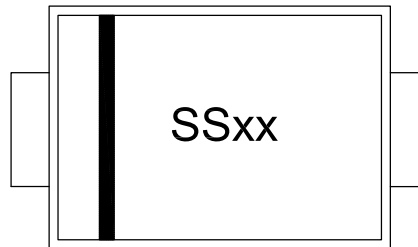
## Package



## Description

- Case: JEDEC DO-214AC molded plastic
- Terminals: Axial leads. Solderable per MIL-STD-750 Method 2026
- Polarity: Color band denotes cathode
- Mounting Position: Any

## Marking



## Ordering information

Part Number	SS22	SS23	SS24	SS25	SS26	SS28	SS210	SS215	SS220
Marking	SS22	SS23	SS24	SS25	SS26	SS28	SS210	SS215	SS220
Base qty	5K	5K	5K	5K	5K	5K	5K	5K	5K



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Maximum Ratings and Electrical Characteristics@T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameters	SS	SS	SS	SS	SS	SS	SS	SS	SS	Units
		22	23	24	25	26	28	210	215	220	
V <sub>RRM</sub>	Maximum Recurrent Peak Reverse Voltage	20	30	40	50	60	80	100	150	200	V
V <sub>RMS</sub>	Maximum RMS Voltage	14	21	28	35	42	56	70	105	140	V
V <sub>DC</sub>	Maximum DC Blocking Voltage	20	30	40	50	60	80	100	150	200	V
I <sub>AV</sub>	Maximum Average Forward Rectified Current	2.0									A
I <sub>FSM</sub>	Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated	50									A
V <sub>F</sub>	Maximum Forward Voltage at 2.0A DC	0.55			0.70		0.85		0.95		V
I <sub>R</sub>	Maximum DC reverse current@T <sub>A</sub> =25°C	0.50					0.10				mA
	Maximum DC reverse current@T <sub>A</sub> =100°C	10					5				
R <sub>θJA</sub> <sup>(1)</sup>	Typical Thermal Resistance	75									°C/W
T <sub>J</sub>	Operating Junction Temperature Range	-55 to +150					-55 to +175				°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150									°C

Note:(1)Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2" x 0.2" (5.0 mm x 5.0 mm) copper pad areas



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Typical Performance Characteristics ( $T_J = 25^\circ\text{C}$ , unless otherwise noted)

Figure 1:  $I_{AV}-T_L$  Curve

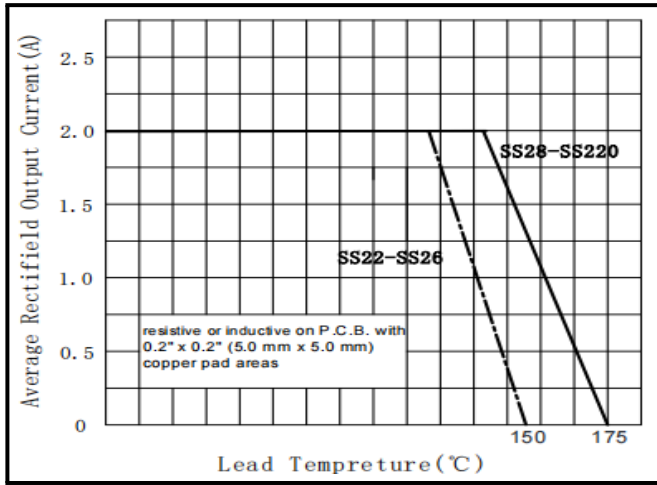


Figure 2: Surge Forward Current Capability

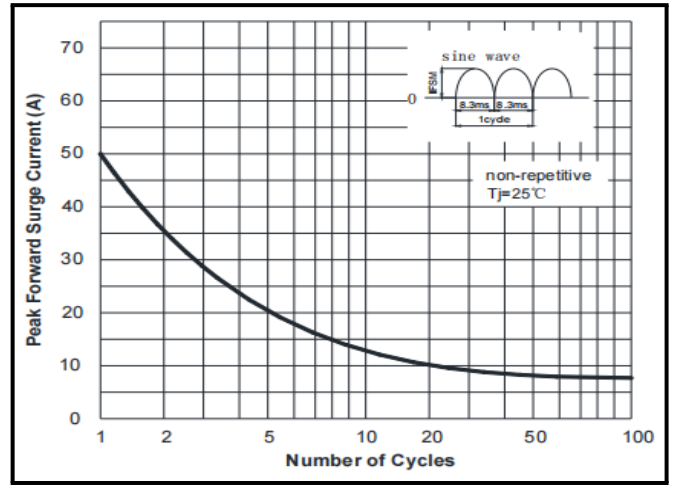


Figure 3: Forward Voltage

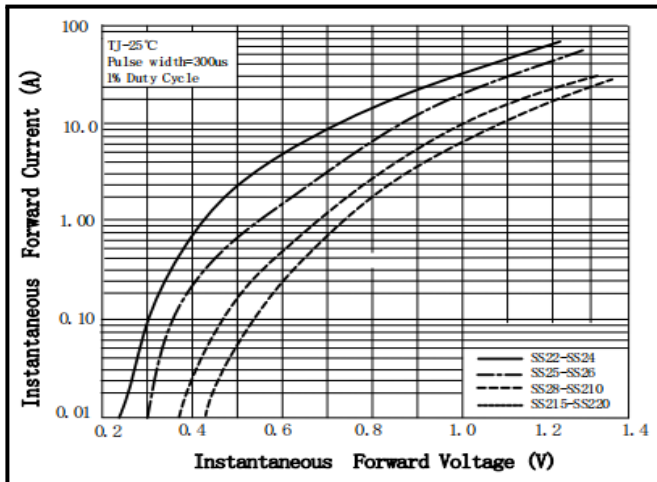
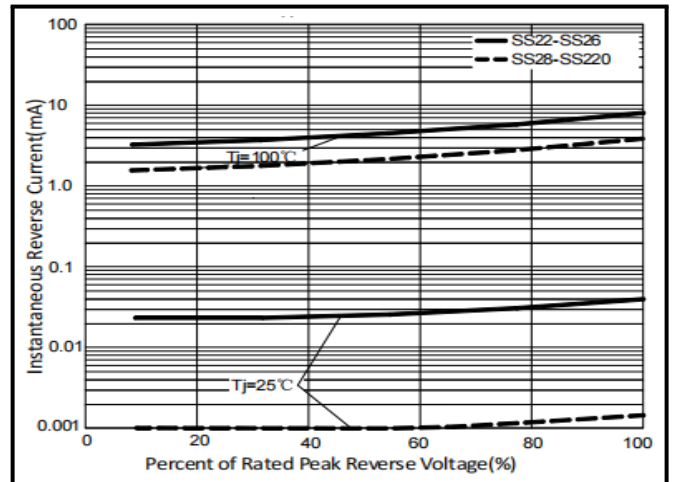


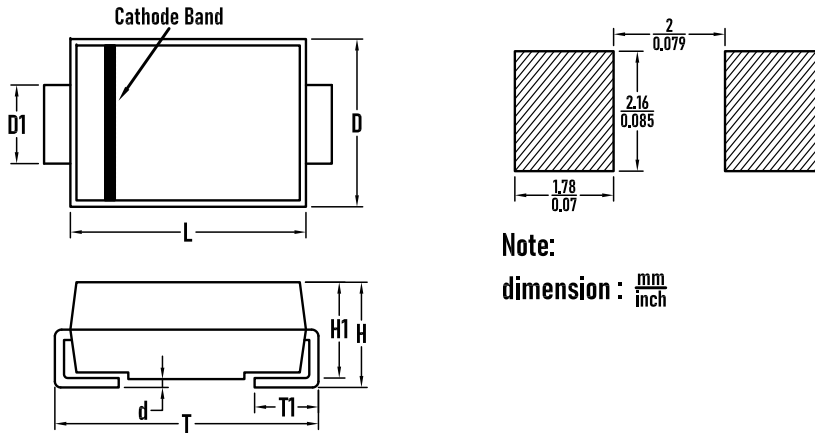
Figure 4: Typical Reverse Characteristics



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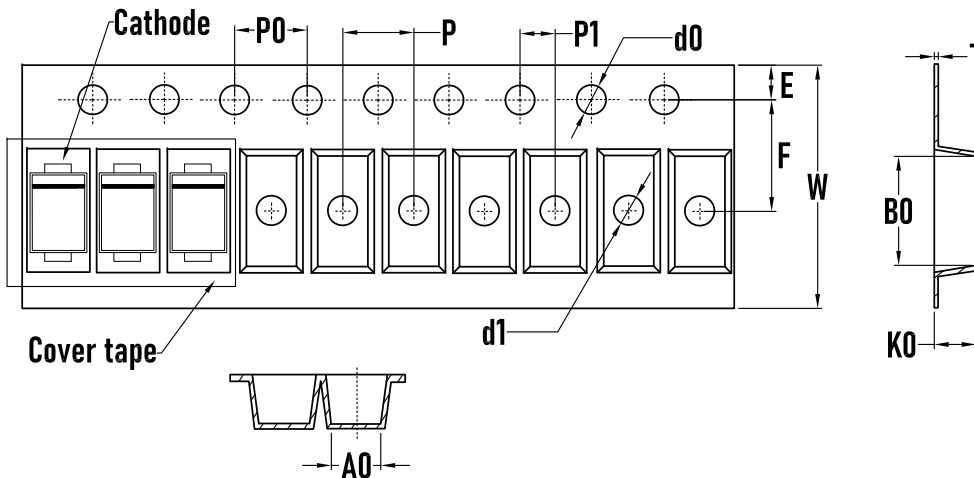
## Outline Drawing - SMA



Note:  
dimension :  $\frac{\text{mm}}{\text{inch}}$

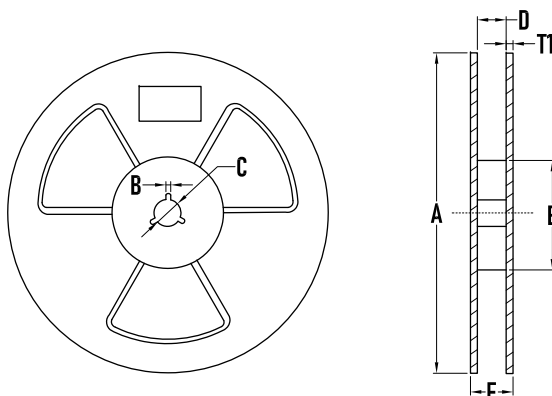
SYMBOL	MILLIMETER	
	MIN	MAX
D	2.50	2.90
D1	1.20	1.60
T	4.80	5.28
T1	0.76	1.52
d	0.051	0.203
H1	1.80	2.20
H	2.00	2.44
L	4.00	4.60

## Packaging Tape - SMA



SYMBOL	MILLIMETER
A0	2.70
B0	5.10±0.1
d0	1.50±0.1
d1	1.50±0.1
E	1.75±0.1
F	5.50±0.1
K0	2.40±0.1
P	4.00±0.1
P0	4.00±0.1
P1	2.00±0.1
W	12.00±0.1
T	0.2±0.02

## Packaging Reel



SYMBOL	MILLIMETER
A	323±2
B	3.0±0.2
C	15.0±0.5
D	13±2
E	73±2
T1	2.2±0.2
Quantity	5000PCS

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Specifications are subject to change without notice.  
Please refer to <http://www.born-tw.com> for current information.

Revision: 2022-Jan-1-A

