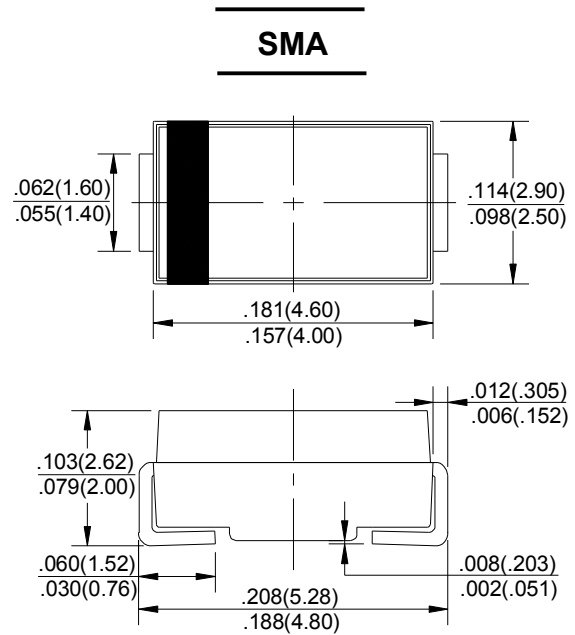


FEATURES

- Plastic package has UL flammability Classification 94V-0
- Glass Passivated chip junction
- Built in strain relief
- Fast switching speed for high efficiency
- High temperature soldering guaranteed: 250°C/10 seconds

MECHANICAL DATA

- Case: JEDED DO-214AC transfer molded plastic
 - Terminals: Solder plated, Solderable per MIL-STD-750, Method 2026
 - Polarity: Color band denotes cathode end
- Weight: 0.003 ounce, 0.093 gram



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	US2A	US2B	US2D	US2G	US2J	US2K	US2M	UNIT	
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V	
Maximum RMS Voltage	V _{RMS}	35	70	140	280	420	560	700	V	
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V	
Maximum Average Forward Rectified Current @T _L =110 °C	I(AV)	2.0							A	
Peak Forward Surge Current 8.3ms Single Half Sine-Wave Super Imposed On Rated Load (JEDEC Method)	I _{FSM}	50							A	
Maximum Forward Voltage at 2.0A DC	V _F	1.0		1.3		1.7			V	
Maximum DC Reverse Current at Rated DC Blocking Voltage @T _J =25°C @T _J =125°C	I _R	5 100							μA	
Maximum reverse recovery time(Note1)	t _{rr}	50				100				ns
Typical Junction Capacitance (Note2)	C _J	50							pF	
Typical Thermal Resistance (Note3)	R _{θJC}	60							°C/W	
Operating Temperature Range	T _J	-55 to +150							°C	
Storage Temperature Range	T _{STG}	-55 to +150							°C	

 NOTE: 1. Reverse recovery condition I_F=0.5A I_R=1.0A I_{rr}=0.25A.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal Resistance Junction to Ambient.

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

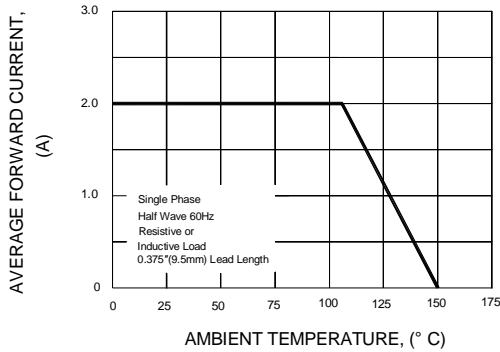


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

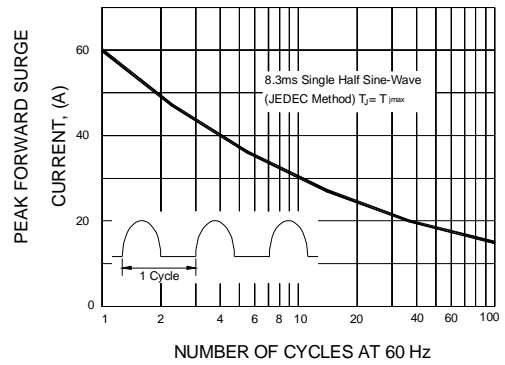


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

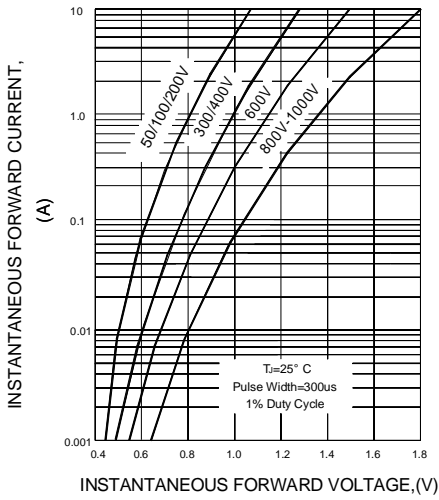


FIG.4-TYPICAL REVERSE CHARACTERISTICS

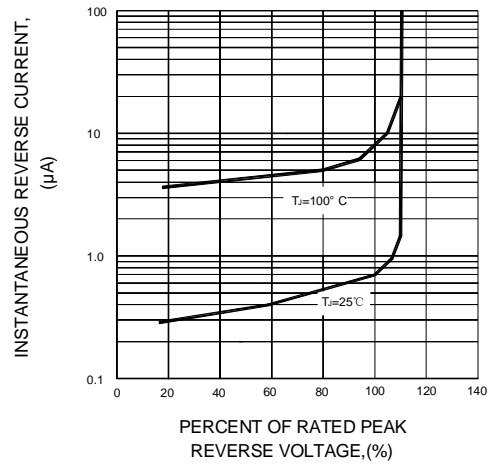


FIG.5-TYPICAL JUNCTION CAPACITANCE

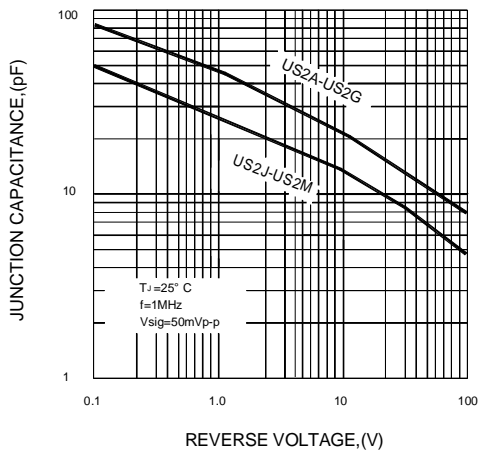
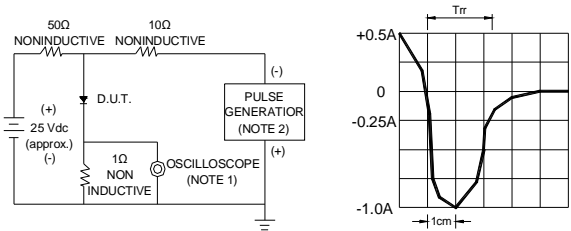


FIG.6-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



- NOTES : 1. Rise Time=7ns max. Input Impedance= 1 magohm. 22pF
2. Rise time=10ns max. Source Impedance= 50 ohms

SET TIME BASE FOR 50/100ns/cm

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