

Features

- Concave terminals
- RoHS compliant*
- 8 bit multiple applications
- Smallest bussed chip array
- E 24 Series from 10 ohms to 1 megohm

CAT25 - Thick Film Chip Resistor Arrays

Electrical Characteristics

Parameters	CAT25	Test Method
Number of Resistors	8	
Resistance Range E24	10 ohms to 1 megohm	
Resistance Tolerance	5 %	JIS-C-5202.5.1
Power Rating/Resistor	62.5 mW	
Rating Temperature	+70 °C	
T.C.R.	±200 ppm/°C	JIS-C-5202.5.2
Maximum Operating Voltage	25 V	
Operating Temperature	-55 °C to +125 °C	

Environmental Characteristics

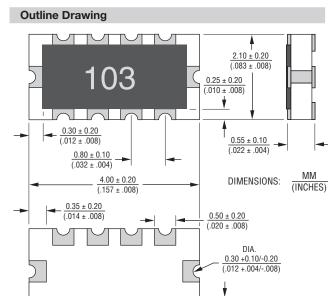
Specification	Characteristics	Test Method
Short Time Overload	±(3 % +0.1 ohm)	JIS-C-5202.5.5
Load Life	±(5 % +0.1 ohm)	JIS-C-5202.7.10
Humidity Load Life	±(3 % +0.1 ohm)	JIS-C-5202.7.9
Resistance to Soldering Heat	±(1 % +0.1 ohm)	JIS-C-5202.6.4
Terminal Strength	±(1 % +0.1 ohm)	JIS-C-5202.6.
Temperature Cycle	±(2 % +0.1 ohm)	JIS-C-5202.7.4
Vibration	±(1 % +0.1 ohm)	JIS-C-5202.6.3
Insulation Resistance	1000 megohms minimum	JIS-C-5202.5.6
Dielectric Withstanding Voltage	50 VRMS	JIS-C-5202.5.7
Lead Solderability	>95 %	JIS-C-5202.6.5

How To Order

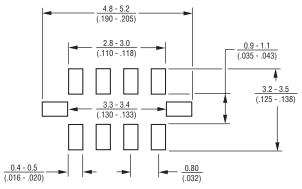
CA T 25 - 103	J A LF
Product CA = Chip Array	
Pin Style T = Concave	
Model	
Resistance Value 103 = 10K ohms	
ToleranceJ = 5 %	
Electrical Circuit — A = 8 Resistors, Bussed Type	
Terminations —	

LF = Tin-plated (RoHS compliant)

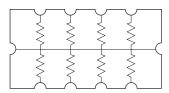
For Standard Values Used in Capacitors, Inductors, and Resistors, click here.



Land Pattern



Electrical Circuit



*RoHS Directive 2015/863, Mar 31, 2015 and Annex. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications.

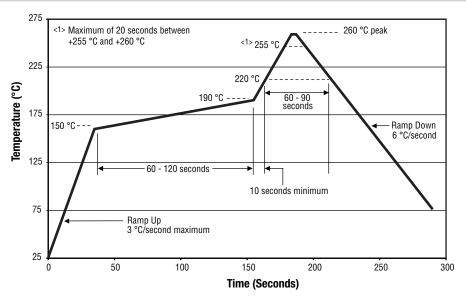
0.40 ± 0.20 $(.016 \pm .008)$

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MM

CAT25 - Thick Film Chip Resistor Arrays

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Soldering Profile for RoHS Compliant Chip Resistors and Arrays

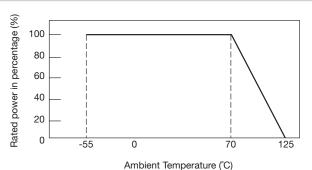
Packaging

12 (.472) embossed tape 4,000 pcs. per reel

Material

Substrate	Alumina 96	
Element	Ruthenium Oxide	
Coating	Glass	
Terminal	Plated Ni+Sn	

Derating Curve



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