

Features

- Lead free device (RoHS compliant*)
- Low profile
- High current capability
- UL 94V-0 classification

Applications

- High frequency switching power supplies
- Inverters
- Free wheeling
- Polarity protection

CD1206-B220~B2100 - Surface Mount Schottky Rectifier Diode

General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components. Bourns offers Schottky Rectifier Diodes for rectification applications, in compact chip package 1206 size format, which offer PCB real estate savings and are considerably smaller than most competitive parts. The Schottky Rectifier Diodes offer a forward current of 2 A with a repetitive peak reverse voltage of 20 V up to 100 V.

Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

Electrical Characteristics (@ TA = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CD1206-				Limit
		B220	B240	B260	B2100	Unit Unit
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	20	40	60	100	V
Maximum RMS Voltage	V _{RMS}	14	28	42	70	V
Maximum DC Blocking Voltage	V _{DC}	20	40	60	100	V
Maximum Average Forward Rectified Current ¹	I _(AV)	2			А	
DC Reverse Current @ Rated DC Blocking Voltage (@T _J = 25 °C)	IR	0.5			mA	
DC Reverse Current @ Rated DC Blocking Voltage (@T _J = 100 °C)	IR	20 10		10	mA	
Maximum Instantaneous Forward Voltage @ 2 A ²	VF	0	.5	0.7	0.85	V
Typical Thermal Resistance ³	$R_{ heta JA}$ $R_{ heta JL}$	75 17			°C/W	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	40		А		

Notes:

- 1 See Forward Derating Curve.
- 2 Pulse test width PW = 300 μ sec, 1 % duty cycle.
- 3 Mounted on P.C. board with 0.2 x 0.2 " (5.0 x 5.0 mm) copper pad areas.

Thermal Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Parameter	Symbol	CD1206-B220~B2100	Unit
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Тѕтс	-55 to +150	°C



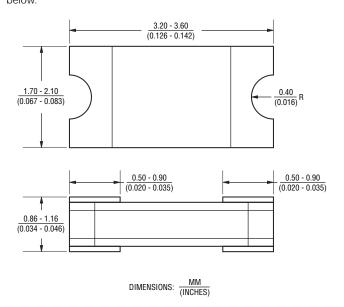
WARNING Cancer and Reproductive Harm - www.P65Warnings.ca.gov

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Product Dimensions

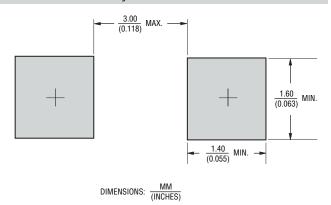
This is a lead free product, packaged with FRP substrate and is epoxy underfilled. The terminals are pure tin plated and are solderable per MIL-STD-750, Method 2026. The package weighs approximately 0.02 g. The package and dimensions are shown below.



Physical Specifications

Case	1206 (3216) Molded plastic
Terminals	Solder plated, solderable per MIL-STD-750,
	Method 2026
Polarity	Indicated by cathode band
	Anv

Recommended Pad Layout



How To Order

CD 1206 - B 2 20

Common Code
CD = Chip Diode

Package
1206

Model Series
B = Schottky Barrier Diode

Forward Current I_(AV)
2 = 2 A

Reverse Voltage
20 = 20 V
40 = 40 V

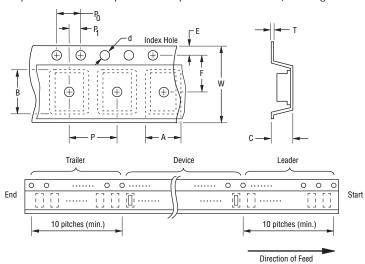
Typical Part Marking

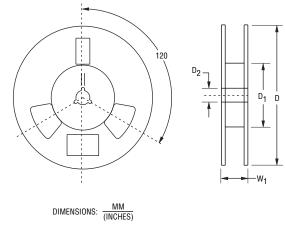
60 = 60 V 100 = 100 V

CD1206-B220	B	220
CD1206-B240	B	240
CD1206-B260	B	260
CD1204 P2100	D :	2100

Packaging Information

The product will be dispensed in Tape and Reel format (see diagram below).





Devices are packed in accordance with EIA standard RS-481-A and specifications shown here.

Item	Symbol	1206
Carrier Width	A	1.70 ±0.10
Carrier Width		(0.067 ±0.004)
Carrier Length	В	3.40 ±0.10
3		(0.134 ±0.004) 1.25 ±0.10
Carrier Depth	С	(0.049 ±0.004)
Sprocket Hole	d	1.55 ±0.05
Spreamet Here		(0.061 ±0.002)
Reel Outside Diameter	D	<u>178</u> (7.008)
Reel Inner Diameter	D ₁	$\frac{60.0}{(2.362)}$ Min.
Feed Hole Diameter	Do	13.0 ±0.20
reed Hole Diameter	D ₂	(0.512 ±0.008)
Sprocket Hole Position	E	1.75 ±0.10
		(0.069 ±0.004)
Punch Hole Position	F	$\frac{3.50 \pm 0.05}{(0.138 \pm 0.002)}$
		4.00 ±0.10
Punch Hole Pitch	Р	(0.157 ±0.004)
Sprocket Hole Pitch	Do	4.00 ±0.10
Sprocket Hole Fitch	P ₀	(0.157 ±0.004)
Embossment Center	P ₁	2.00 ±0.05
Embossment center	• 1	(0.079 ±0.002)
Overall Tape Thickness	Т	0.20 ±0.05
·		(0.008 ±0.002) 8.00 ±0.20
Tape Width	W	(0.315 ±0.008)
Reel Width	W ₁	$\frac{13.5}{(0.531)}$ Max.
Quantity per Reel	_	3,000

Specifications are subject to change without notice.

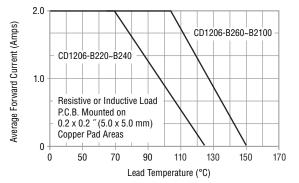
Users should verify actual device performance in their specific applications.

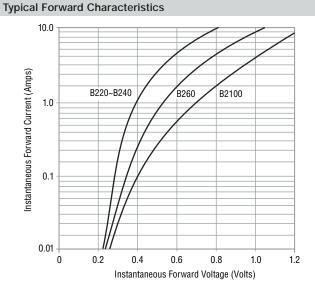
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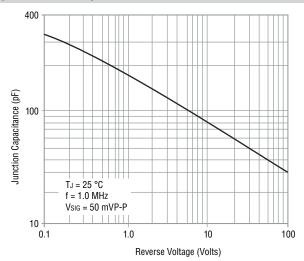
Performance Graphs

Forward Current Derating Curve

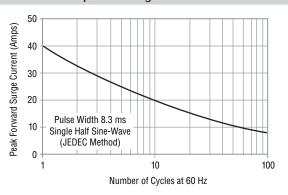




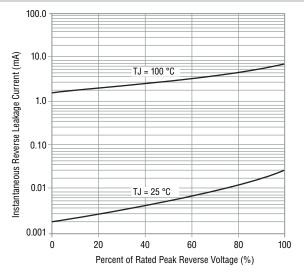
Typical Junction Capacitance



Maximum Non-Repetitive Surge Current



Typical Reverse Characteristics



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