

### **Features**

- EIA 1206 (3216 metric) footprint
- Ceramic cavity laminate design for time lag application
- Surface mount packaging for automated assembly
- UL 248-14 listed
- RoHS compliant\* and halogen free\*\*

## **Applications**

- PoE, PoE+
- Power supplies
- LCD/LED monitors
- DC/DC converters
- Industrial equipment

### SF-1206SP175L-2-A9 - Time Lag Ceramic Cavity Laminate SMD Fuse

#### **Clearing Time Characteristics**

% of Current	Clearing Time @ 25 °C		
Rating	Min.	Max.	
100 %	4 hours	_	
200 %	1 second	120 seconds	

#### **Additional Information**









<u>JCT</u>

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<u>INVENTOR</u>

SAMPLE

#### **Electrical Characteristics**

Model	Rated Current (A)	Resistance (Ω) Typ.***	Rated Voltage	Interrupting Rating	Typical I²t (A²s) ****	Agency Recognition
	( )	( ) )1	3.3	. 3	( -/	cUL: <u>E198545</u>
SF-1206SP175L-2-A9	1.75	0.08	63 VDC	100 A @ 63 VDC	1.5	1

<sup>\*\*\*</sup> Resistance value measured with ≤10 % rated current at 25 °C ambient. Tolerance ± 25 %.

#### **Reliability Testing**

No.	Test	Test Condition	Requirement	Test Reference
1	Solderability	Temperature setup: 235 +0 / -5 °C Time setup: 10 sec.	After test terminal electrode wetting area must be greater than 95 %	IEC 68-2-58
2	Resistance to soldering heat	Temperature setup: 235 ± 5 °C Time setup: 30 sec.	DCR change ≤ ± 15 %	IEC 68-2-58
3	Thermal shock	Temperature setup: 25 °C $\sim$ -65 °C $\sim$ 25 °C $\sim$ 125 °C Time setup: -65 °C (30 min) $\sim$ 25 °C (5 min) $\sim$ 125 °C (30 min) $\sim$ 25 °C (5 min), 5 cycles	DCR change ≤ ± 15 % No mechanical damage	MIL-STD-202G Method 107G Test Condition B
4	Humidity unload	Heat (85 $\pm$ 0.5 °C) High humidity (85 $\pm$ 1 % RH) 240 hours	DCR change ≤ ± 15 % No mechanical damage	MIL-STD-202G Method 103B Test Condition A
5	Salt spray	Salt spray concentration: 5 ± 1 % Test liquid temperature: 35 ± 0.5 °C 96 hours	DCR change ≤ ± 15 % No mechanical damage	MIL-STD-202G Method 101E Test Condition A
6	Bending	The board shall be bent by 1 mm at a rate of 1 mm/sec.	DCR change ≤ ± 15 %	IEC 60127-4
7	Vibration	Frequency setup: 10 ~ 55 ~ 10 Hz Time setup: 1 minute/cycle (X-Y-Z, 120 cycles, 6 hours)	DCR change ≤ ± 15 % No mechanical damage	MIL-STD-202G Method 201A



### WARNING Cancer and Reproductive Harm - $\underline{www.P65Warnings.ca.gov}$

- \* RoHS Directive 2015/863, Mar 31, 2015 and Annex.
- \*\* Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less.

"SinglFuse" is a trademark of Bourns, Inc.

Specifications are subject to change without notice.

<sup>\*\*\*\*</sup> Melting I<sup>2</sup>t calculated at 10 times rated current.

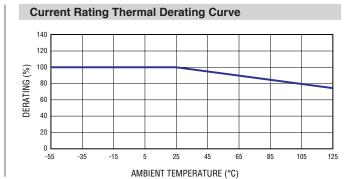
# SF-1206SP175L-2-A9 - Time Lag Ceramic Cavity Laminate SMD Fuse

# BOURNS®

# Environmental Characteristics

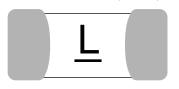
Operating Temperature	-55 °C to + 125 °C
Storage Conditions	
Temperature	+15 °C to +30 °C
Humidity	20 % to 70 %
Shelf Life (from manufacturing date)	2 years
Moisture Sensitivity Level	1
ESD Classification <sup>1</sup>	Class 6

<sup>1</sup>per AEC-Q200-2, HBM



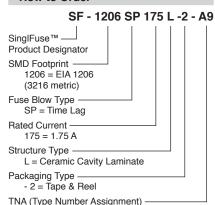
#### **Typical Part Marking**

Represents total content. Layout may vary.



Rated	Part
Current	Marking
1.75 A	L

# How to Order

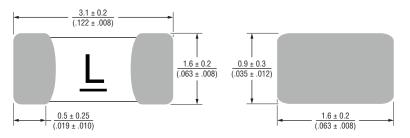


# TNA (Type Number Assignment) Designator

#### Packaging

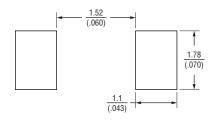
Reel Dimension	7-inch Tape and Reel	
Specification	EIA 481-2	
Quantity	4,000 pieces	
Packaging Code	-2	

### **Product Dimensions**

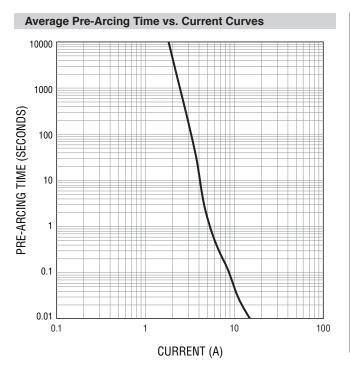


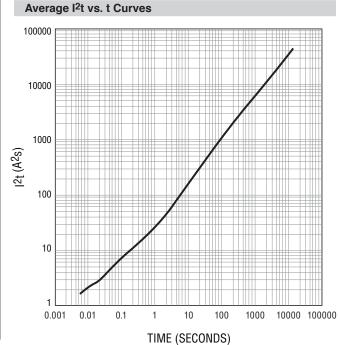
DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

### **Recommended Pad Layout**

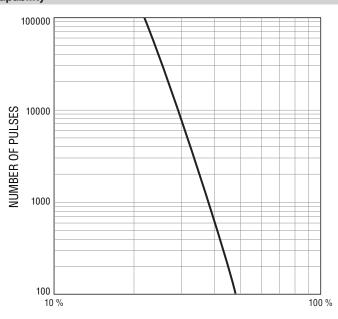


DIMENSIONS:  $\frac{MM}{(INCHES)}$ 





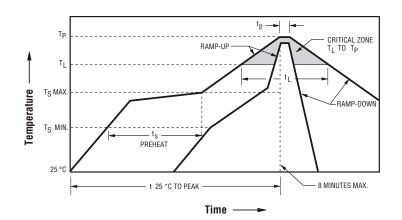
### **Pulse Cycle Withstand Capability**



PULSE I2t / AVERAGE MELTING I2t

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#### **Solder Reflow Recommendations**

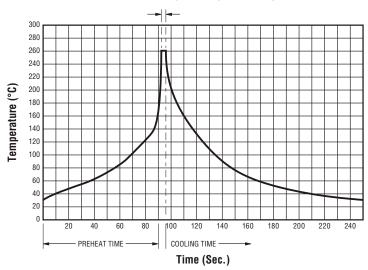


Profile Feature	Pb-Free Assembly
Preheat / Soak: Temperature Min. (T <sub>smin</sub> ) Temperature Max. (T <sub>smax</sub> ) Time (t <sub>s</sub> ) from (T <sub>smin</sub> to T <sub>smax</sub> )	150 °C 200 °C 60-180 seconds
Ramp Up Rate (T <sub>L</sub> to T <sub>p</sub> )	3 °C / second max.
Ramp Up Rate (T <sub>smax</sub> to T <sub>L</sub> )	5 °C / second max.
Liquidous Temperature (T <sub>L</sub> ) Time (t <sub>L</sub> ) maintained above T <sub>L</sub>	217 °C 60-150 seconds
Peak Temperature (Tp)	260 +0/-5 °C
Time within 5 °C of actual peak temperature (tp)	10-30 seconds*
Ramp Down Rate (Tp to TL)	6 °C / second max.
Time 25 °C to Peak Temperature (t 25 °C to peak)	8 minutes max.
Do not exceed	260 °C

<sup>\*</sup> Tolerance for peak profile temperature (Tp ) is defined as a supplier minimum and a user maximum.

### **Solder Wave Recommendations**

### Peak Temperature (Dwell Time)



Profile Feature	Pb-Free Assembly	
Preheat: Temperature Max. (T <sub>smax</sub> ) Time (Min. to Max.)	150 °C 60-90 seconds	
Solder Pot Temperature	260 °C max.	
Solder Dwell Time	2-3 seconds	

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