

Features

- High impedance value
- Current rating up to 400 mA
- RoHS compliant* and halogen free**

Applications

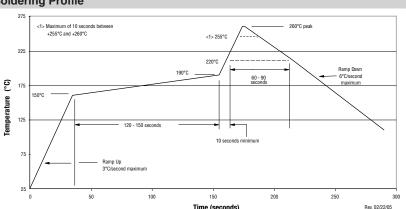
- Noise suppression
- Personal computers
- Display panels

SRF2012A Series - Common Mode Chip Inductors

Electrical Specifications

	Impedance @ 100 MHz		Insulation	DCB	IDC
Part Number	(Ω)	Tolerance (%)	Resistance (MΩ) Min.	Max. (Ω)	Max. (mA)
SRF2012-300YA	30	±25	10	0.20	400
SRF2012-670YA	67	±25	10	0.25	400
SRF2012-900YA	90	±25	10	0.30	400
SRF2012-121YA	120	±25	10	0.30	400
SRF2012-161YA	160	±25	10	0.35	350
SRF2012-181YA	180	±25	10	0.35	350
SRF2012-201YA	200	±25	10	0.40	300
SRF2012-221YA	220	±25	10	0.40	300
SRF2012-261YA	260	±25	10	0.40	300
SRF2012-301YA	300	±25	10	0.50	300
SRF2012-361YA	360	±25	10	0.50	300
SRF2012-371YA	370	±25	10	0.50	300
SRF2012-501YA	500	±25	10	0.50	300
SRF2012-671YA	670	±25	10	0.60	140
SRF2012-901YA	900	±25	10	0.88	100

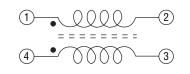
Soldering Profile



How to Order



Schematic





WARNING Cancer and Reproductive Harm www.P65Warnings.ca.gov

- RoHS Directive 2015/863, Mar 31, 2015 and Annex.
- ** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (Cl) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (Cl) content is 1500 ppm or less. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

General Specifications

.....55 °C to +125 °C (Temperature rise included)

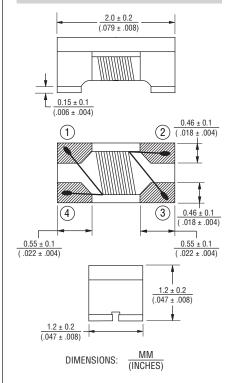
Storage Temperature

.....-55 °C to +125 °C Temperature Rise20 °C max. at IDC

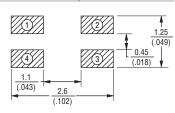
Materials

Core Ferrite
Wire Packaging 2000 pcs. per 7-inch reel

Product Dimensions

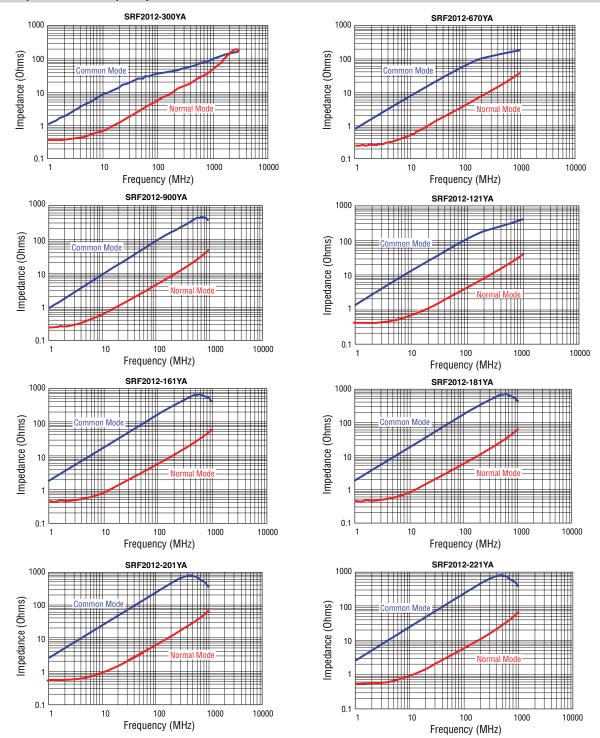


Recommended Layout



DIMENSIONS: $\frac{MM}{(INCHES)}$

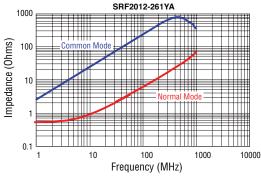
Typical Impedance vs. Frequency Curves

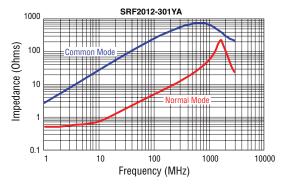


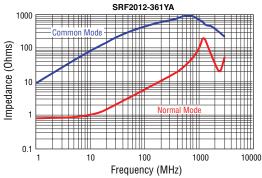
Specifications are subject to change without notice.

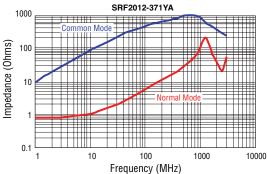
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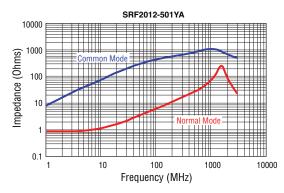
Typical Impedance vs. Frequency Curves (Continued)

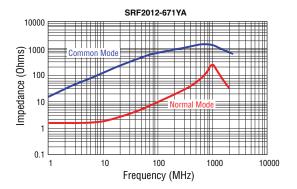


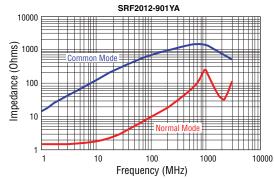










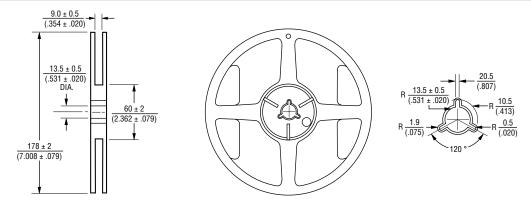


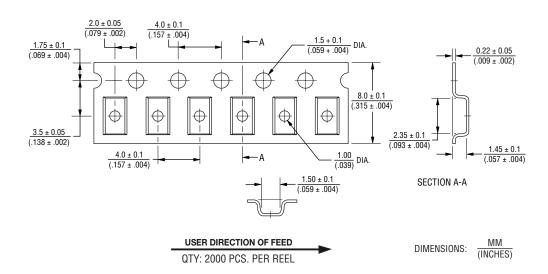
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Users should verify actual device performance in their specific applications.

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Packaging Specifications





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