

Features

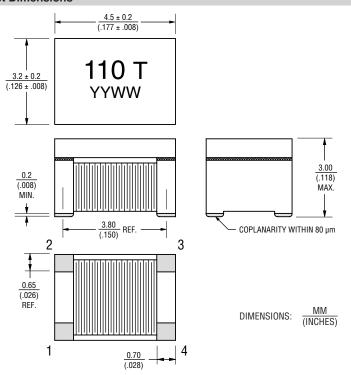
- Shielded construction low radiation
- Bifilar wound
- High temperature
- AEC-Q200 compliant
- RoHS compliant* and halogen free**

SRF4530A Series - Common Mode Chip Inductors, CANbus

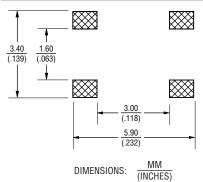
Electrical Specifications @ 25 °C

	Inductance @ 100 kHz / 0.1 V		Leakage L (µH) @ 100 kHz / 0.1 V	Common Mode Impedance @ 10 MHz		DCR	
Bourns Part Number	L (μH)	Tolerance (%)	Тур. (μH)	Min. (kΩ)	Typ. (kΩ)	Max. (Ω)	Irms (A)
SRF4530A-110Y	11.0	+50 / -30	0.10	0.30	0.60	0.50	0.36
SRF4530A-220Y	22.0	+50 / -30	0.15	0.60	1.20	0.60	0.31
SRF4530A-510Y	51.0	+50 / -30	0.25	1.50	3.50	1.00	0.23
SRF4530A-101Y	100.0	+50 / -30	0.30	3.00	7.50	1.50	0.20

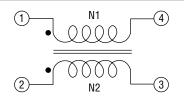
Product Dimensions



Recommended Layout



Schematic



General Specifications Rated Voltage 50 VDC Insulation Resistance10 megohms min. @ 50 VDC **Operating Temperature**-55 °C to +150 °C (Temperature rise included)

Storage Temperature-55 °C to +150 °C Temperature Rise40 °C at rated Irms

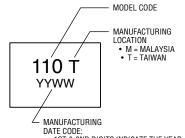
Materials

Core	Ferrite
Wire	Enameled copper
Terminal Finish	Au
Packaging 50	0 pcs. per 7-inch reel

How to Order



Typical Part Marking



- 1ST & 2ND DIGITS INDICATE THE YEAR
 3RD & 4TH DIGITS INDICATE THE WEEK NUMBER



WARNING Cancer and Reproductive Harm www.P65Warnings.ca.gov

- RoHS Directive 2015/863, Mar 31, 2015 and Annex.
- ** Bourns considers a product to be "halogen free" if (a) the Bromine (Br) content is 900 ppm or less; (b) the Chlorine (CI) content is 900 ppm or less; and (c) the total Bromine (Br) and Chlorine (CI) content is 1500 ppm or less.

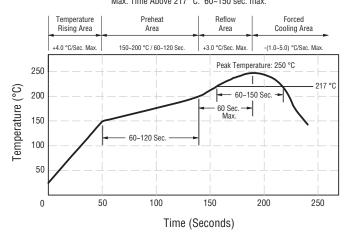
Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

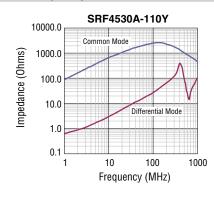
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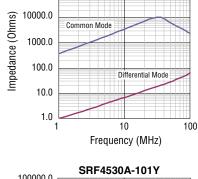
Soldering Profile

Peak Temperature: 250 °C max. Max. Peak Temperature -5 °C: 30 sec. max. Max. Time Above 217 °C: 60~150 sec. max.



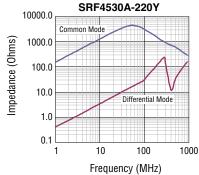
Typical Impedance vs. Frequency

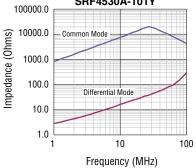




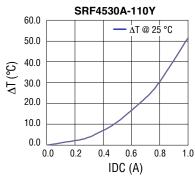
100000.0

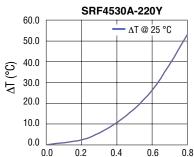
SRF4530A-510Y

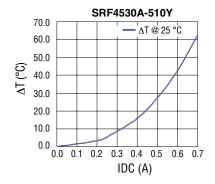


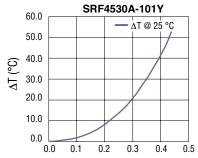


Temperature Rise vs. IDC

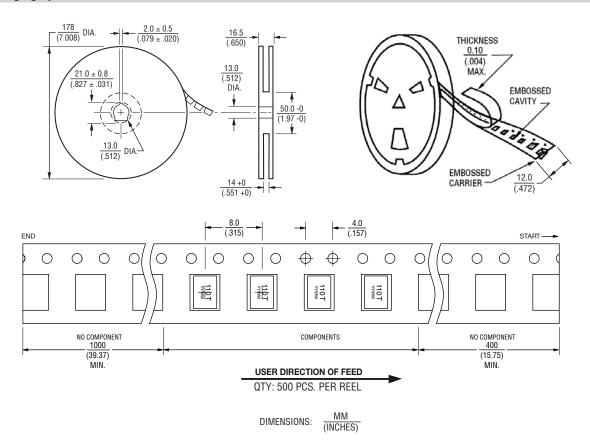








Packaging Specifications



BOURNS®

Asia-Pacific: Tel: +886-2 2562-4117 • Email: asiacus@bourns.com

EMEA: Tel: +36 88 885 877 • Email: eurocus@bourns.com

The Americas: Tel: +1-951 781-5500 • Email: americus@bourns.com

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