100V 8A N-Channel Enhancement Mode Power MOSFET

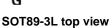
FEATURES

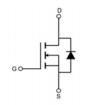
- RDSON \leq 115m Ω @Vgs=10V, Id=3A
- Excellent RDS(ON) and Low Gate Charge
- · Lead free product is acquired

APPLICATION

- Power switching application
- · Hard switched and high frequency circuits
- Uninterruptible power supply







Schematic Diagram

ASSEMBLY MESSAGE

Product Name	Marking	Package	Packaging	
BXT1150N10J	0103M	SOT89-3L	Reel	

ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Parameter		Symbol	Rating SOT89-3L	Unit	
Drain-Source Voltage		V _{DSS}	100	>	
Drain Current		tinuous (T _C = 25°C)	I _D	8	Α
Drain Current	Con	tinuous (T _C = 100°C)	טו	5.6	Α
Drain Current	Pulsed (Note1)		I _{DM}	32	Α
Gate-Source Voltage		V _{GSS}	±20	V	
Power Dissipation T _C =25°C		P _D	2	W	
Maximum Junction Temperature		TJ	150	ů	
Storage Temperature Range		T _{STG}	-55 to 150	°C	

 $\begin{tabular}{ll} \textbf{Note:} & \textbf{1. Repetitive Rating: Pulse width limited by maximum junction temperature} \\ \end{tabular}$



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THERMAL CHARACTERISTICS

Parameter	Symbol	Max.	l lmi4	
Farameter	Symbol	SOT89-3L	Unit	
Thermal Resistance, Junction to Ambient	Reja	62.5	°C/W	

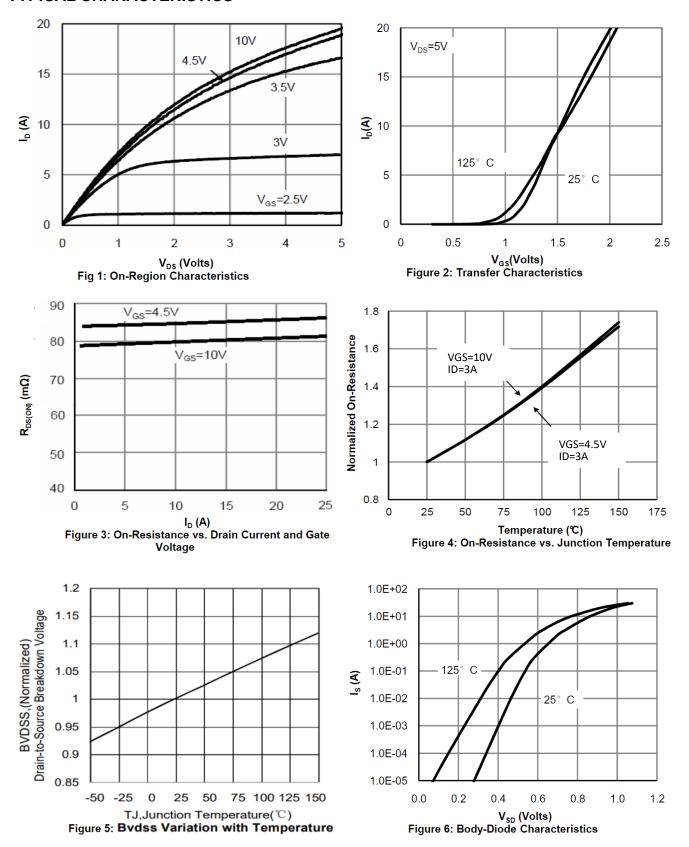
ELECTRICAL CHARACTERISTICS (T_J=25°C,unless otherwise Noted)

Parameter	Symbol	Test Condition	Min.	Тур.	Max.	Unit
OFF CHARACTERISTICS	•			•	•	
Drain-Source Breakdown Voltage	BV _{DSS}	VGS=0V, ID=250µA	100			V
Zero Gate Voltage Drain Current	IDSS	VDS=100V, VGS=0V			1	uA
Gate-Body Leakage Current, Forward	I _{GSS}	VGS=20V			100	nA
Gate-Body Leakage Current, Reverse		VGS=-20V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	VDS=VGS, ID=250µA	1.0	2.0	3.0	V
Drain Course On State Registeres		VGS=10V, ID=3A		85	115	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	VGS=4.5V, ID=3A		110	135	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	Ciss	\/DQ_05\/\\\QQ_0\/		655		pF
Output Capacitance	Coss	VDS=25V, VGS=0V,		25		pF
Reverse Transfer Capacitance	Crss	f=1.0MHz		21		pF
SWITCHING PARAMETERS						
Turn-ON Delay Time	t _{D(ON)}			6.2		ns
Turn-ON Rise Time	t _R	VDD=50V, ID=8A, VGS =		4.1		ns
Turn-OFF Delay Time	t _{D(OFF)}	10V, RG=1.8Ω		21		ns
Turn-OFF Fall-Time	t _F			3.8		ns
Total Gate Charge(Note2)	Q_G	VDS =50V, VGS =10V, ID		21		nC
Gate Source Charge	Q _{GS}			2.2		nC
Gate Drain Charge	Q _{GD}	=3A		3.3		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V _{SD}	IS=8A, VGS=0V			1.4	V
Diode Continuous Forward Current	Is				8	Α
Maximum Pulsed Drain to Source Diode Forward Current	lsм				32	Α

Note: 2. Essentially independent of operating temperature



TYPICAL CHARACTERISTICS





TYPICAL CHARACTERISTICS(Cont.)

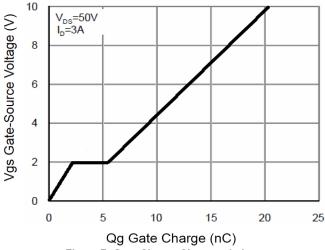
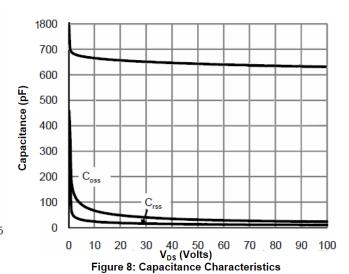


Figure 7: Gate-Charge Characteristics



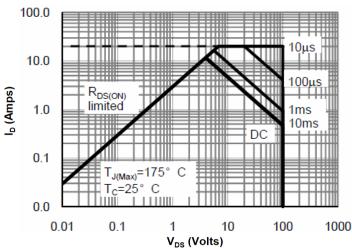
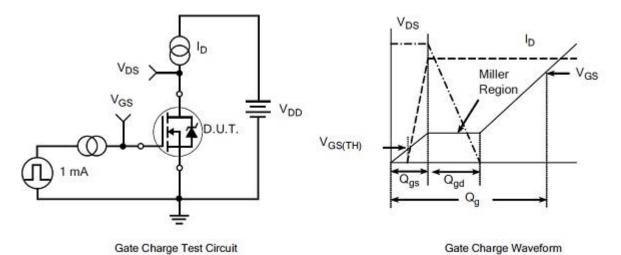
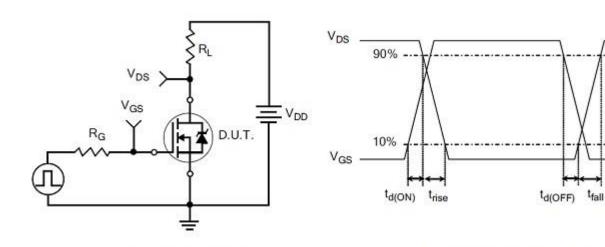


Figure 9: Maximum Forward Biased Safe Operating Area



TEST CIRCUITS AND WAVEFORMS



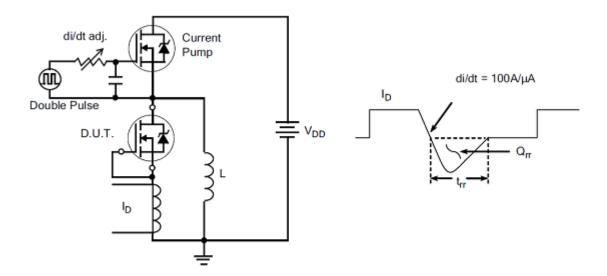


Resistive Switching Waveforms

Version: 1.0

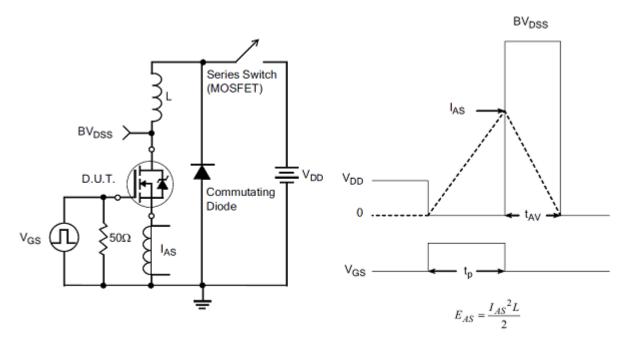
Resistive Switching Test Circuit

TEST CIRCUITS AND WAVEFORMS(Cont.)



Diode Reverse Recovery Test Circuit

Diode Reverse Recovery Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

Version: 1.0

Revision history

Document revision history

Date	Revision	Changes
2-Jul-2021	1.0	First release

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