

## 60V 0.45A N-Channel Enhancement Mode Power MOSFET

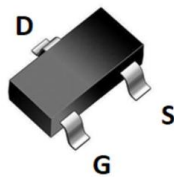
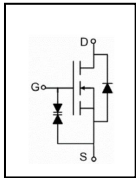
### General Description

This Power MOSFET has been developed using advanced trench process, which is specifically designed to minimize input capacitance and gate charge. This renders the device suitable for use as primary switch in advanced high-efficiency isolated DC-DC converters for telecom and computer applications, and applications with low gate charge driving requirements.

### FEATURES

- $R_{DS(ON)} \leq 2.8 \Omega$  @  $V_{GS}=10V, I_D=0.4A$
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired
- ESD Rating HBM 2.3KV

### SYMBOL



SOT-23 top view

### ASSEMBLY MESSAGE

Product Name	Package	Packaging
BXT2N7002BK	SOT-23	Reel

### ABSOLUTE MAXIMUM RATINGS ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Rating	Unit
			SOT-23	
Drain-Source Voltage		$V_{DSS}$	60	V
Drain Current	Continuous ( $T_C = 25^\circ\text{C}$ )	$I_D$	0.45	A
	Continuous ( $T_C = 100^\circ\text{C}$ )		0.36	A
Drain Current	Pulsed (Note1)	$I_{DM}$	1.8	A
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	0.35	W
Maximum Junction Temperature		$T_J$	150	$^\circ\text{C}$
Storage Temperature Range		$T_{STG}$	-55 to 150	$^\circ\text{C}$

Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature

**THERMAL CHARACTERISTICS**

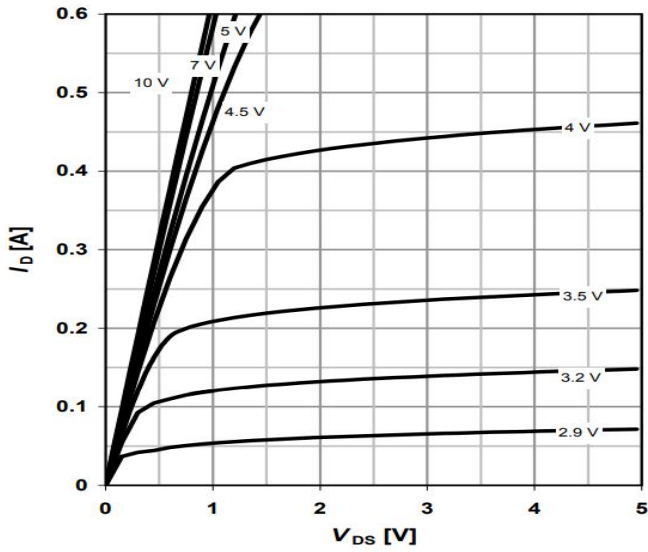
Parameter	Symbol	Max.	Unit
		SOT-23	
Thermal Resistance, Junction-to- Ambient	$R_{\theta JA}$	357	°C / W

**ELECTRICAL CHARACTERISTICS** ( $T_J=25^{\circ}\text{C}$ , unless otherwise Noted)

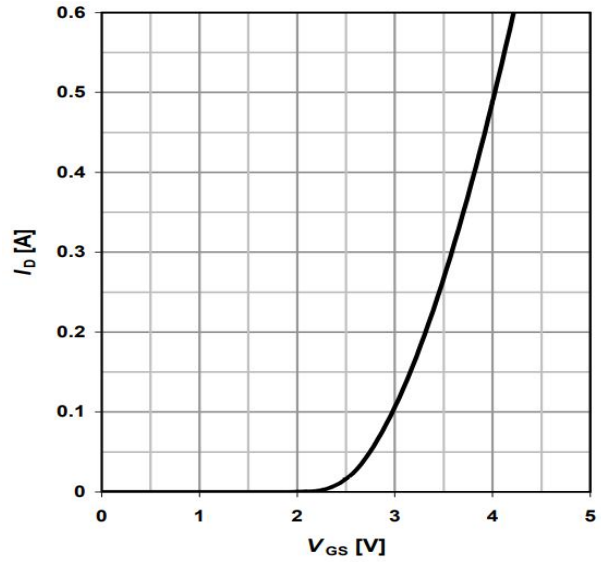
Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$			1	$\mu A$
Gate-Body Leakage Current, Forward	$I_{GSS}$	$V_{GS}=20V$			10	$\mu A$
Gate-Body Leakage Current, Reverse		$V_{GS}=-20V$			-10	$\mu A$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.1	-	2.2	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=0.4A$			2.8	$\Omega$
		$V_{GS}=5V, I_D=0.4A$			3.6	$\Omega$
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0MHz$		21		pF
Output Capacitance	$C_{OSS}$			12		pF
Reverse Transfer Capacitance	$C_{RSS}$			4.1		pF
Total Gate Charge	$Q_g$	$V_{DS} = 10V, I_D = 0.3A, V_{GS}$ $= 4.5V$		1.8		nC
Gate-Source Charge	$Q_{gs}$			0.6		nC
Gate-Drain("Miller") Charge	$Q_{gd}$			0.75		nC
<b>SWITCHING PARAMETERS</b>						
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=30V, I_D=0.2A, V_{GS} =$ $10V, R_G=1\Omega$		14		ns
Turn-ON Rise Time	$t_R$			83		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			40		ns
Turn-OFF Fall-Time	$t_F$			19		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_S=0.45A, V_{GS}=0V$			1.2	V
Diode Continuous Forward Current	$I_S$				0.45	A

Note: 2. Essentially independent of operating temperature

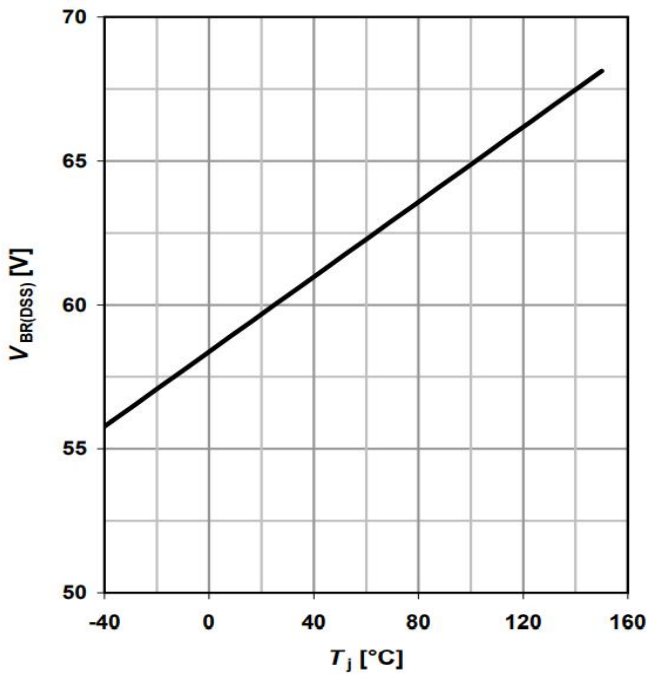
**TYPICAL CHARACTERISTICS**



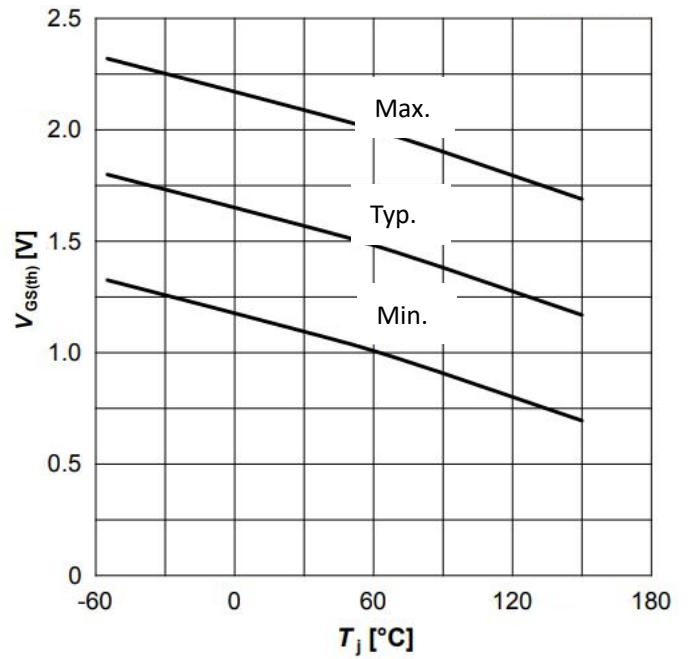
**Figure 1. Output Characteristics**



**Figure 2. Transfer Characteristics**

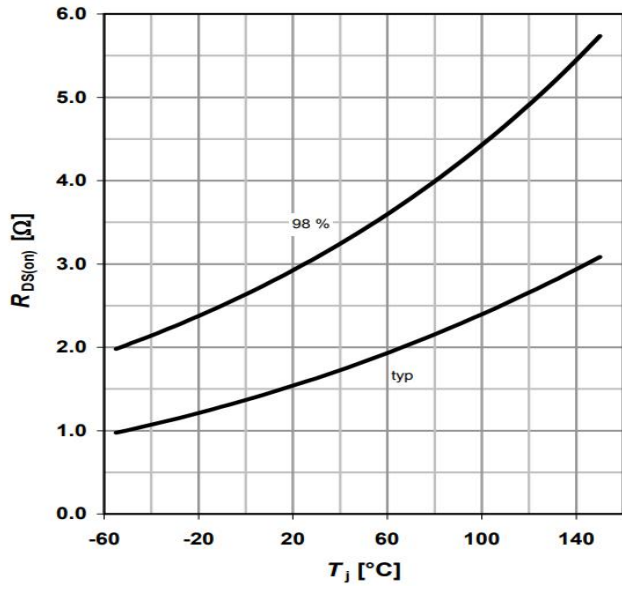


**Figure 3. Breakdown Voltage Variation with Temperature**

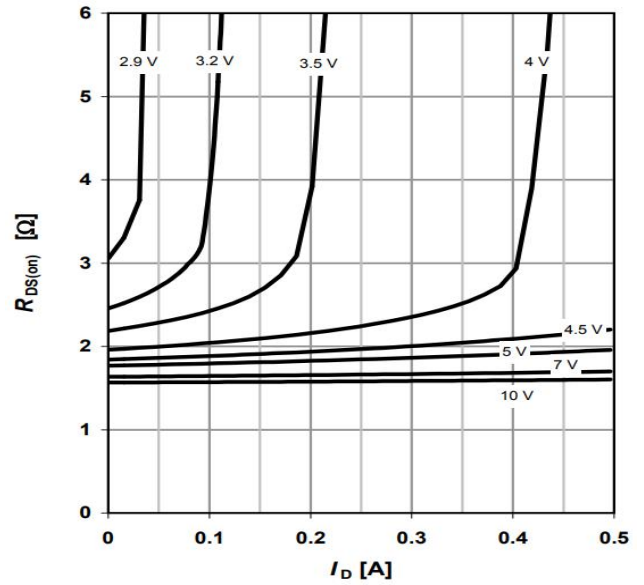


**Figure 4. Gate Threshold Variation with Temperature**

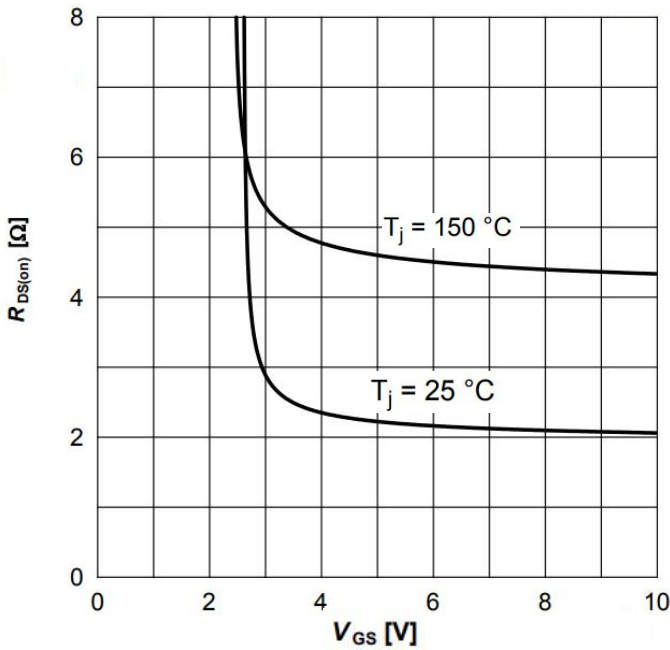
**TYPICAL CHARACTERISTICS(Cont.)**



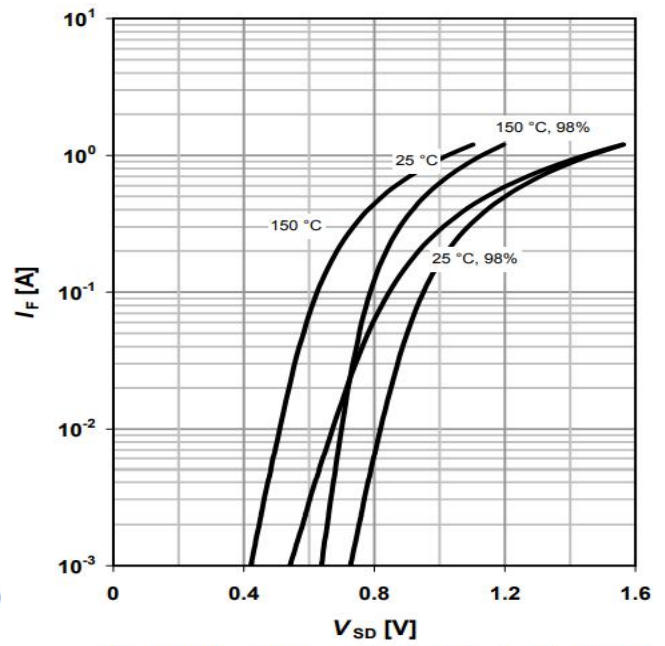
**Figure 5. On-Resistance Variation with Temperature**



**Figure 6. On-Resistance vs. Drain Current**

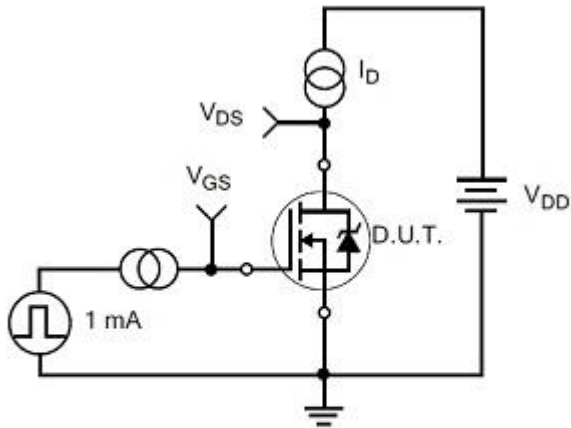


**Figure 7. On-Resistance vs. Gate-to-Source Voltage**

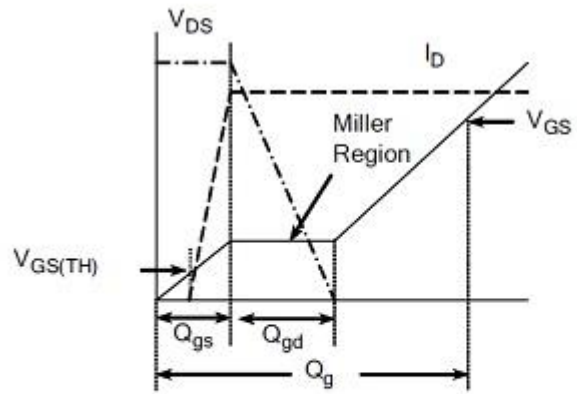


**Figure 8. Source-Drain Diode Forward Voltage**

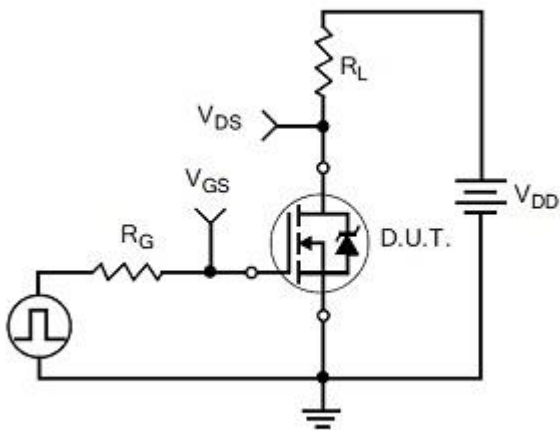
TEST CIRCUITS AND WAVEFORMS



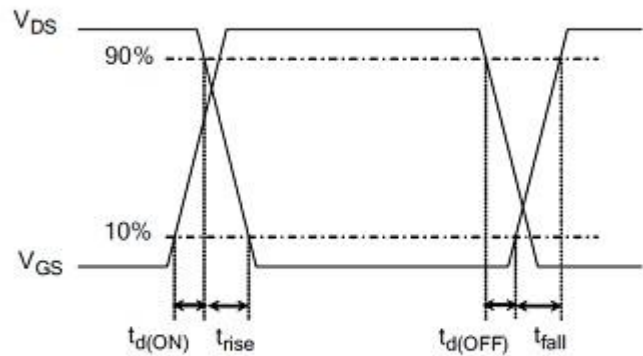
Gate Charge Test Circuit



Gate Charge Waveform

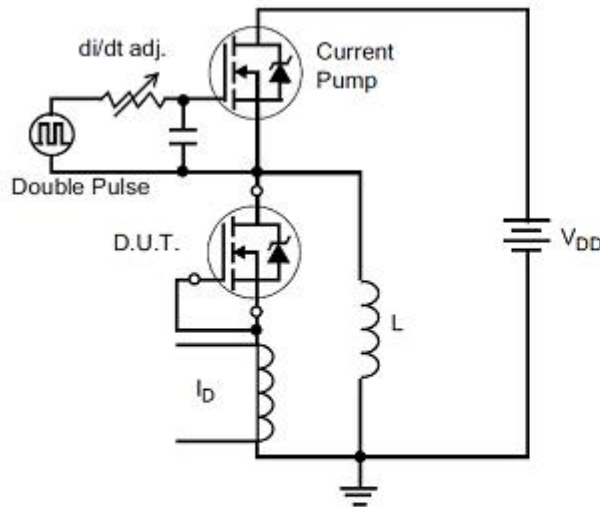


Resistive Switching Test Circuit

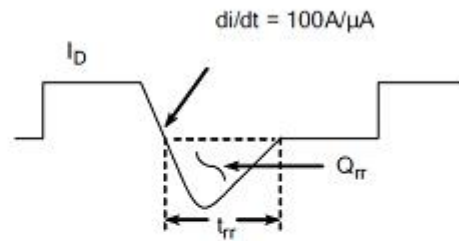


Resistive Switching Waveforms

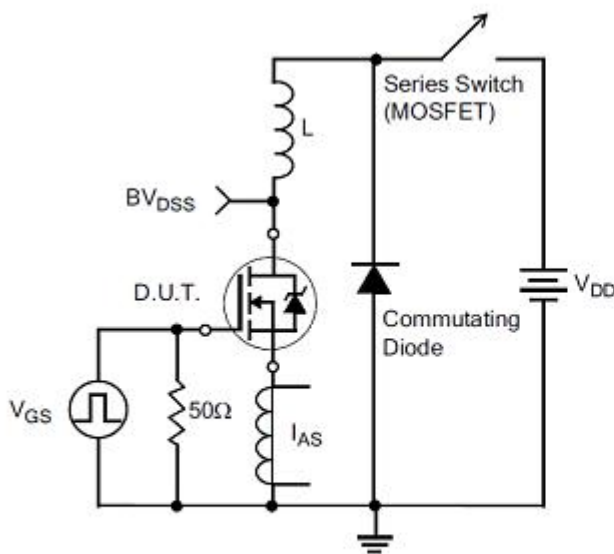
TEST CIRCUITS AND WAVEFORMS(Cont.)



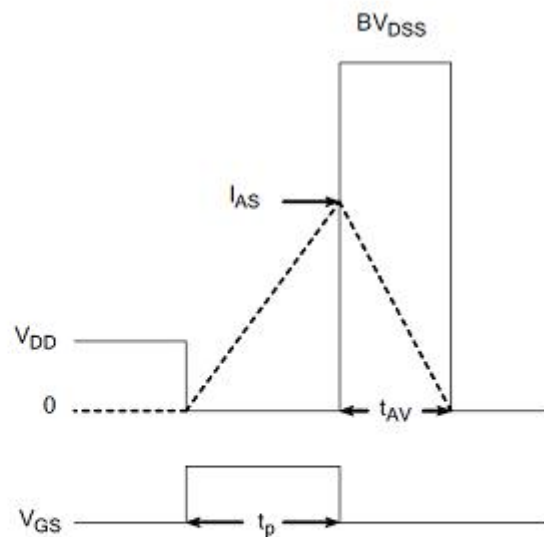
Diode Reverse Recovery Test Circuit



Diode Reverse Recovery Waveform



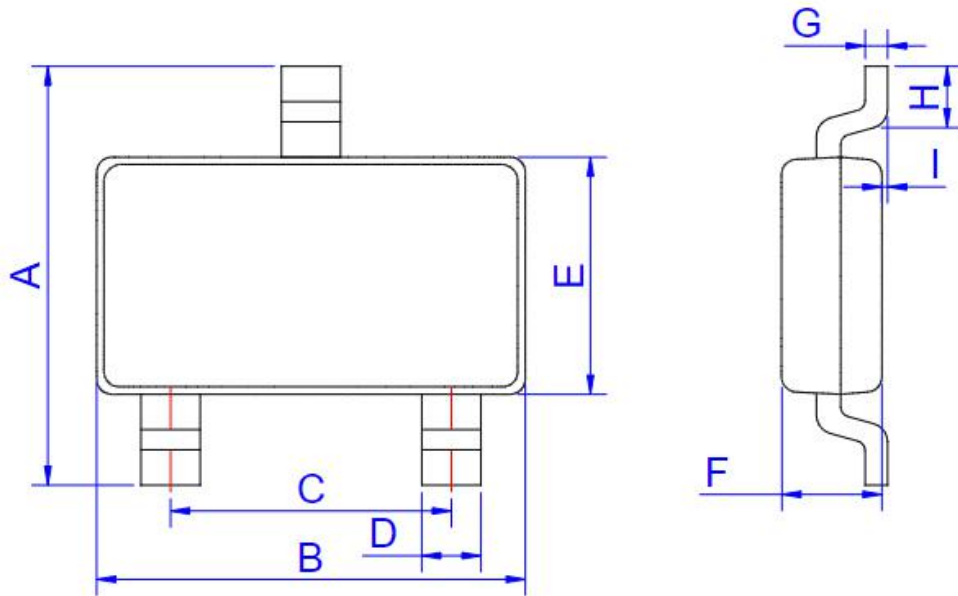
Unclamped Inductive Switching Test Circuit



$$E_{AS} = \frac{I_{AS}^2 L}{2}$$

Unclamped Inductive Switching Waveforms

### SOT-23 Package



### SOT-23

Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.250	2.550	0.089	0.100
B	2.800	3.000	0.110	0.118
C	1.800	2.000	0.071	0.079
D	0.300	0.500	0.012	0.020
E	1.200	1.400	0.047	0.055
F	0.900	1.150	0.035	0.045
G		0.200		0.008
H	0.200		0.008	
I	0.000	0.150	0.000	0.006

## Revision history

### Document revision history

Date	Revision	Changes
18-Jan-2021	1.0	First release



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