

## Electrostatic Discharged Protection Devices (ESD) Data Sheet

### Description

Brightking's LES08AXXL05 components are surge rated diode array designed to protect high speed data line interfaces. It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by electrostatic discharge (ESD), electrical fast transients (EFT), and lightning. The unique design of this device incorporates surge rated, low capacitance steering diodes and TVS diodes in a single package. During transient conditions, the steering diodes direct the transient either the position side of the power supply or to the ground.

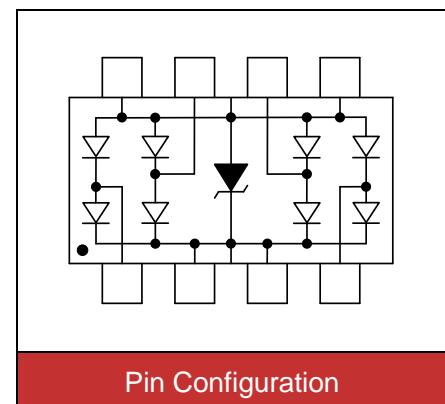


Contact : ±30kV  
Air : ±30kV



### Features

- IEC61000-4-2 ESD 30KV Air, 30KV contact compliance
- SOIC-08 surface mount package
- Protects four I/O lines
- Peak power dissipation of 500W under 8/20μs waveform
- Working voltage: 3.3V, 5V
- Low leakage current
- Low capacitance and clamping voltage
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270 °C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020



### Applications

- Ethernet 10/100/ base T
- Firewire & USB protection
- Set Top Box (STB) protection
- Video card (DVI) protection
- T1/E1 secondary IC side protection
- T3/E3 secondary IC side protection
- HDSL secondary IC side protection
- IC bus protection
- Micro-controller line protection

### Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse power (tp=8/20μs waveform)	P <sub>PP</sub>	500	W
ESD voltage (Contact discharge)	V <sub>ESD</sub>	±30	kV
ESD voltage (Air discharge)		±30	
Storage & operating temperature range	T <sub>STG</sub> , T <sub>J</sub>	-55~+150	°C

Electrical Characteristics ( $T_J=25^\circ\text{C}$ )

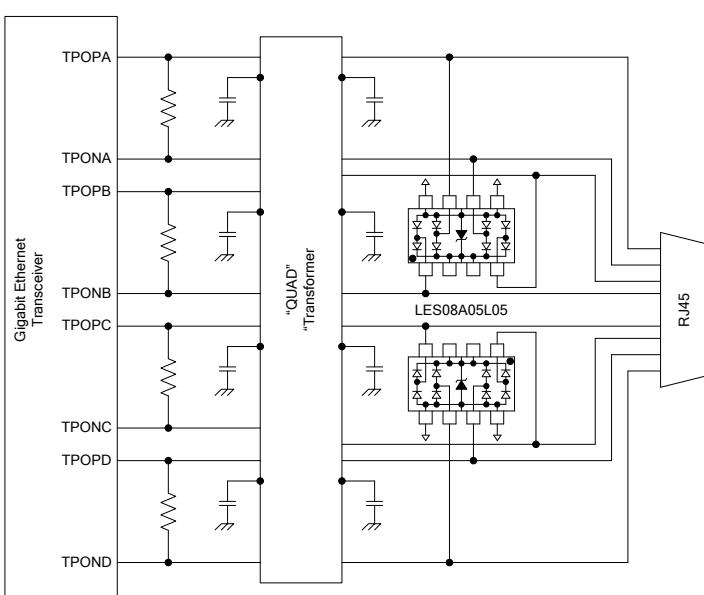
LES08A3.3L05(Marking: B RA3.3-4)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				3.3	V
Reverse breakdown voltage	$V_{BR}$	$I_{BR}=1\text{mA}$	4			V
Reverse leakage current	$I_R$	$V_R=3.3\text{V}$ Each I/O pin			40	$\mu\text{A}$
Clamping voltage ( $tp=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=1\text{A}$			8.5	V
Clamping voltage ( $tp=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=10\text{A}$			10.9	V
Off state junction capacitance	$C_J$	0Vdc, $f=1\text{MHz}$ Between I/O pins and GND			15	pF

LES08A05L05(Marking: B RA05-4)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				5	V
Reverse breakdown voltage	$V_{BR}$	$I_{BR}=1\text{mA}$	6			V
Reverse leakage current	$I_R$	$V_R=5\text{V}$ Each I/O pin			10	$\mu\text{A}$
Clamping voltage ( $tp=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=1\text{A}$			9.8	V
Clamping voltage ( $tp=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=10\text{A}$			13.5	V
Off state junction capacitance	$C_J$	0Vdc, $f=1\text{MHz}$ Between I/O pins and GND			15	pF

## Applications Information



## Typical Characteristics Curves

Figure 1. Power Derating Curve

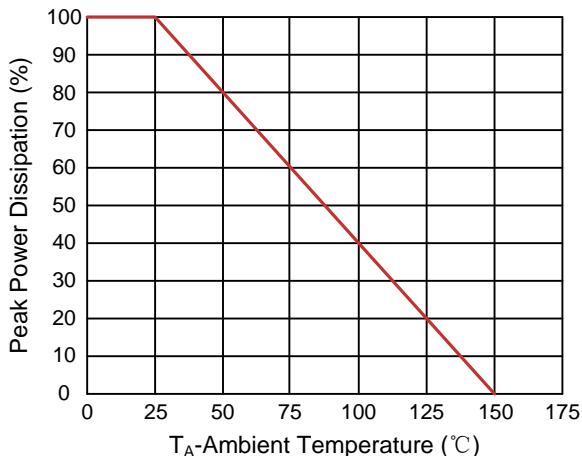


Figure 2. Pulse Waveforms

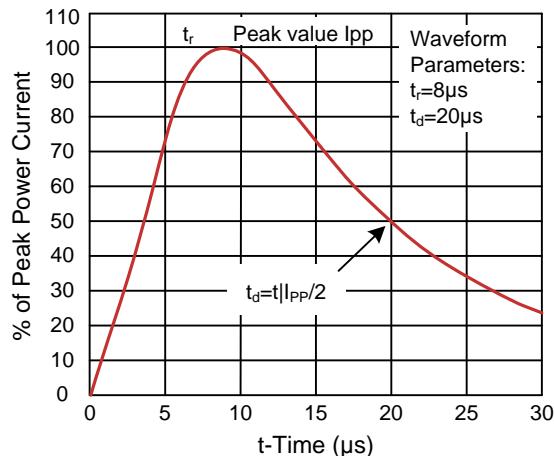


Figure 3. Non-Repetitive Peak Pulse vs. Pulse Time

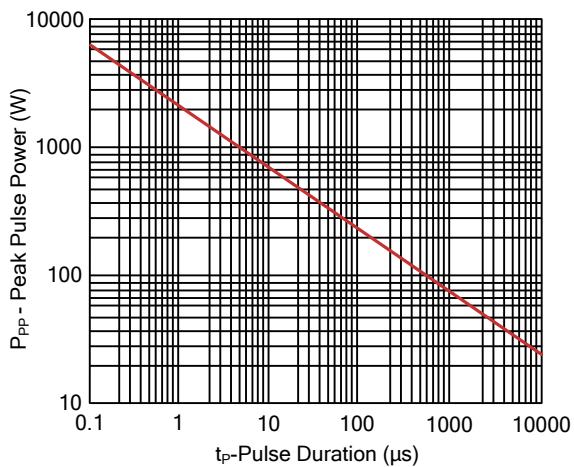
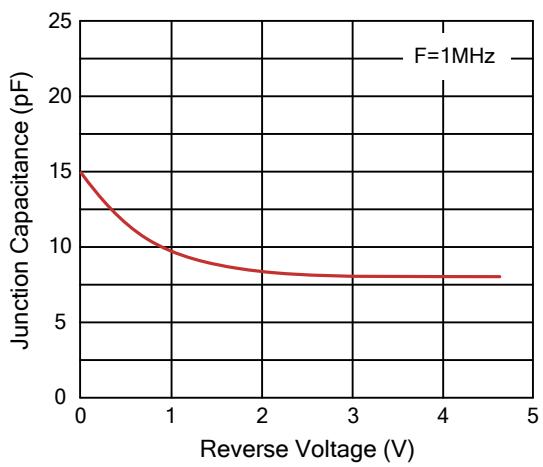
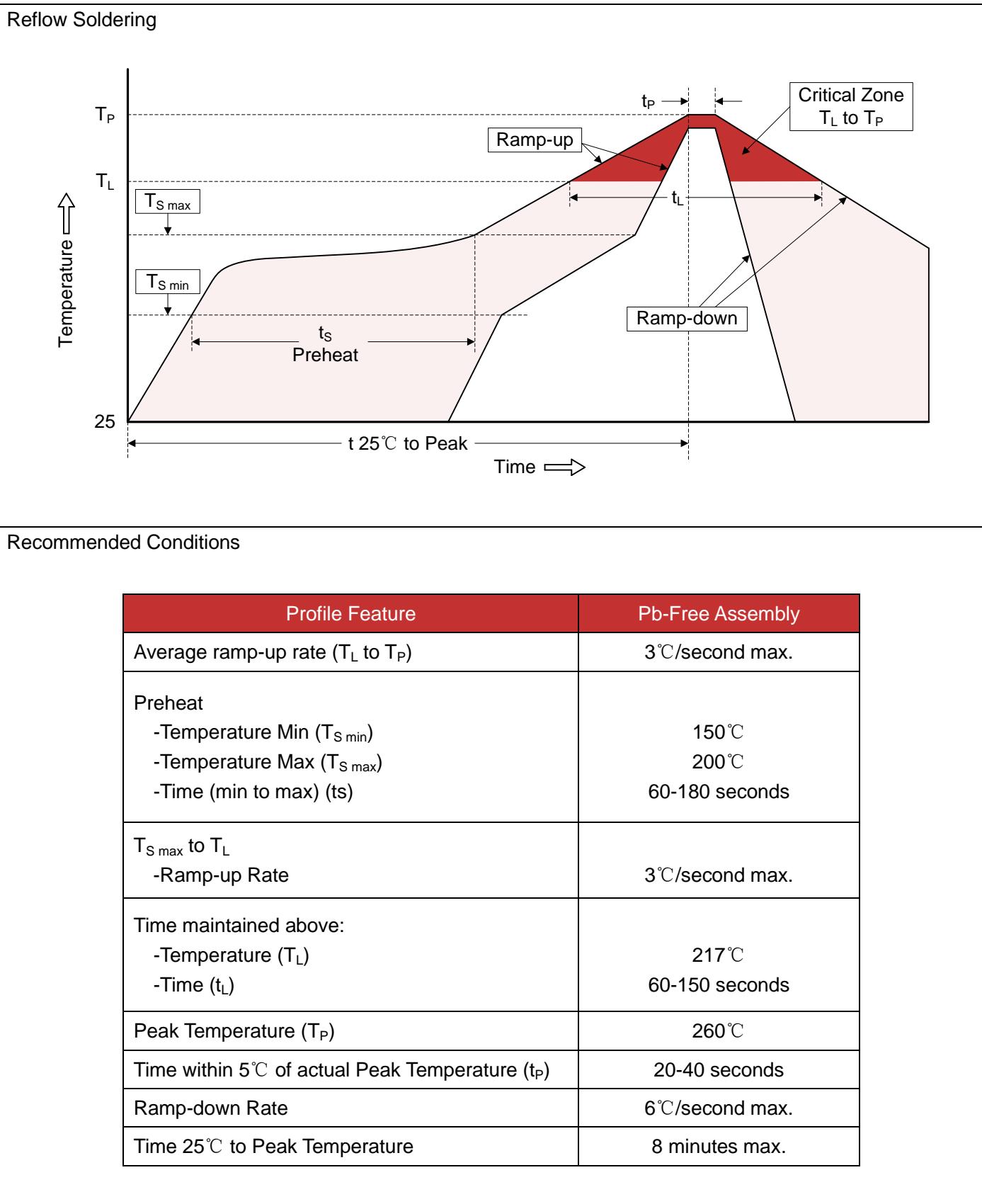


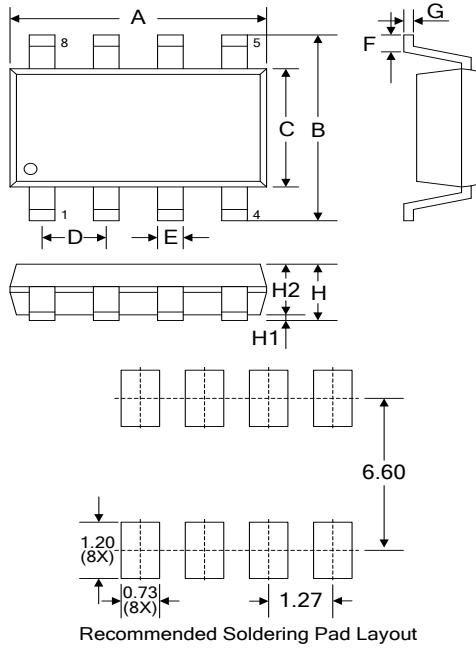
Figure 4. Capacitance vs. Reverse Voltage



## Recommended Soldering Conditions



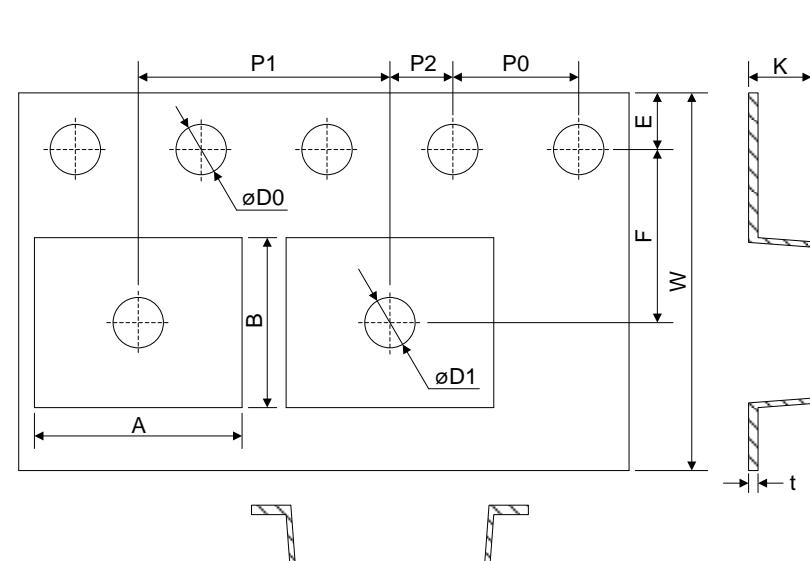
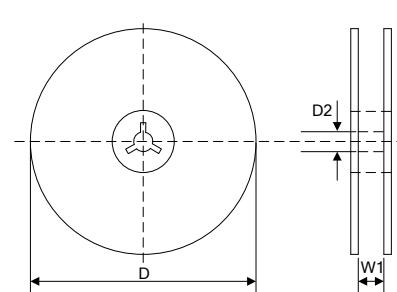
## Dimensions (SOIC-08)



Symbol	Dimension			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.80	5.00	0.189	0.197
B	5.80	6.20	0.228	0.244
C	3.80	4.00	0.150	0.157
D	1.27		0.050	
E	0.33	0.51	0.013	0.020
F	0.40	1.27	0.016	0.050
G	0.19	0.25	0.007	0.010
H	1.35	1.75	0.053	0.069
H1	0.10	0.25	0.004	0.010
H2	1.45		0.057	

Recommended Soldering Pad Layout

## Packaging

Tape	Symbol	Dimension (mm)
	W	12.00±0.30
	P0	4.00±0.10
	P1	8.00±0.10
	P2	2.00±0.10
	D0	Φ1.55±0.10
	D1	Φ1.55±0.05
	E	1.75±0.10
	F	5.50±0.10
	A	6.50±0.10
	B	5.40±0.10
	K	2.00±0.10
	t	0.30±0.05
Reel	D	Φ330.0±3.0
	D2	Φ13.0
	W1	13.5
	Quantity: 2500PCS	

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