

## **Thyristor Surge Suppressors (TSS) Data Sheet**

### **Description**

DO-214AA Thyristor solid state protection thyristor protect telecommunications equipment such as modems, line cards, fax machines, and other CPE.

P Series devices are used to enable equipment to meet various regulatory requirements including GR 1089, ITU K.20, K.21 and K.45, IEC 60950, UL 60950, and TIA-968 (formerly known as FCC Part 68).



#### **Features**

Compared to surge suppression using other technologies, P Series devices offer absolute surge protection regardless of the surge current available and the rate of applied voltage (dv/dt). P Series devices:

- Cannot be damaged by voltage
- Eliminate hysteresis and heat dissipation typically found with clamping devices
- Eliminate voltage overshoot caused by fast-rising transients
- Are non-degenerative
- Will not fatigue
- Have low capacitance, making them ideal for high-speed transmission equipment
- Meets MSL level 1, per J-STD-020

#### **Electrical Parameters**

Parameter	Definition					
V <sub>DRM</sub>	Peak Off-state Voltage – maximum voltage that can be applied while maintaining off state					
Vs	Switching Voltage – maximum voltage prior to switching to on state					
V <sub>T</sub>	On-state Voltage – maximum voltage measured at rated on-state current					
I <sub>DRM</sub>	Leakage Current – maximum peak off-state current measured at V <sub>DRM</sub>					
I <sub>S</sub>	Switching Current – maximum current required to switch to on state					
I <sub>T</sub>	On-state Current – maximum rated continuous on-state current					
I <sub>H</sub>	Holding Current – typical current required to maintain on state					
Co	Off-state Capacitance – typical capacitance measured in off state					
I <sub>PP</sub>	Peak Pulse Current – maximum rated peak impulse current					
V <sub>C</sub>	Clamping Voltage – maximum voltage measured at I <sub>PP</sub>					



#### **Electrical Characteristics**

Part Number	Type ①	V <sub>DRM</sub> (V)	V <sub>S</sub> (V)	V <sub>T</sub> (V)	I <sub>DRM</sub> (μΑ)	I <sub>S</sub> (mA)	I <sub>T</sub> (A)	I <sub>H</sub> (mA)	C <sub>O</sub> (pF)	I <sub>PP</sub> 10/1000μs (A)	V <sub>C</sub> @ I <sub>PP</sub> Max.(V)	Marking
P0080SB	LV1	6	25	4	5	800	2.2	50	110	80	25	LV1

#### Notes:

- All measurements are made at an ambient temperature of 25°C. I<sub>PP</sub> applies to -40°C through +85°C temperature range.
- ullet Off-state capacitance(C<sub>0</sub>) is measured at 1 MHz with a 2V bias and is typical value.
- Rating Surge Voltage: 6KV (10/700µs)
- ① Specific code by request

#### **Thermal Considerations**

Package DO-214AA/SMB	Symbol	Parameter	Value	Unit
	$T_J$	Operating Junction Temperature	-40 to +150	$^{\circ}\!\mathbb{C}$
	Ts	Storage Temperature Range	-40 to +150	$^{\circ}$ C
	$R_{\theta JA}$	Junction to Ambient on printed circuit	90	°C/W

#### **Characteristics Curves**

Figure 1. V-I Characteristics

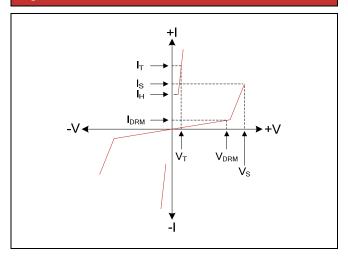


Figure 3. Normalized Vs Change versus Junction Temperature

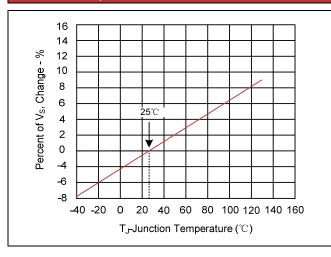


Figure 2. tr × td Pulse Wave-form

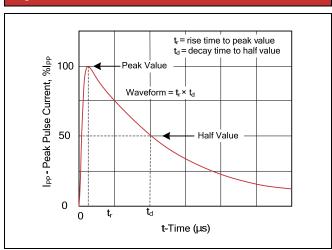
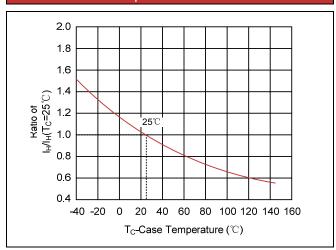
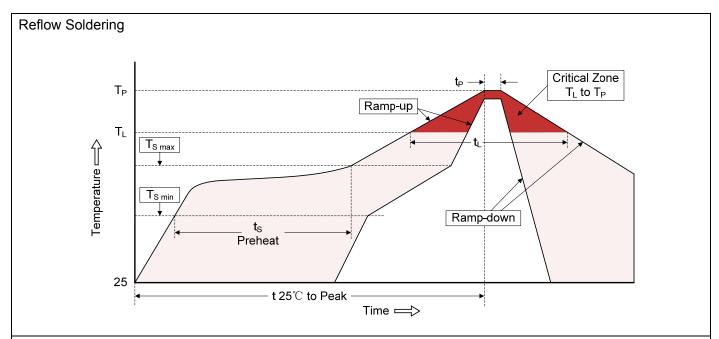


Figure 4. Normalized DC Holding Current versus
Case Temperature





## **Recommended Soldering Conditions**



### **Recommended Conditions**

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second max.
Preheat $ \begin{array}{c} \text{-Temperature Min } (T_{S \text{ min}}) \\ \text{-Temperature Max } (T_{S \text{ max}}) \\ \text{-Time (min to max) } (t_S) \end{array} $	150℃ 200℃ 60-180 seconds
T <sub>S max</sub> to T <sub>L</sub> -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature $(T_L)$ -Time $(t_L)$	217°C 60-150 seconds
Peak Temperature (T <sub>P</sub> )	260℃
Time within 5°C of actual Peak Temperature (t <sub>P</sub> )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25℃ to Peak Temperature	8 minutes max.

Max.

0.185

0.155

0.086

0.220

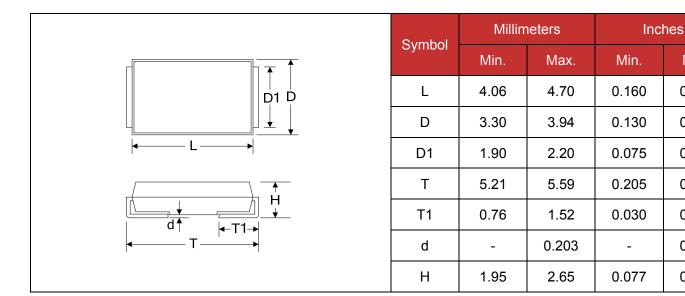
0.060

800.0

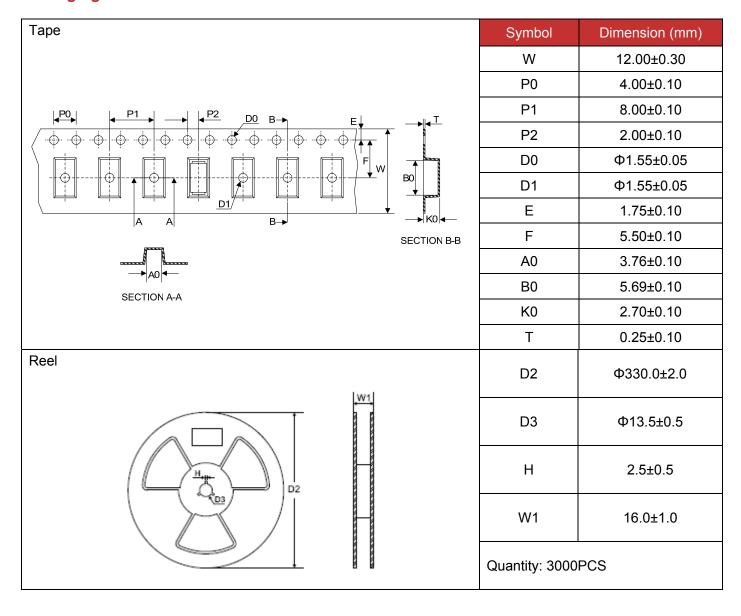
0.104



## **Dimensions (SMB/DO-214AA)**



## **Packaging**



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