### **BCT4222A**

### **High-Speed DPDT Analog Switch**

#### **Features**

♦ V<sub>CC</sub> Operating Range: 1.65V to 4.2V

♦ Rail-to-Rail Signal Range

♦ ON-Resistance Matching: 0.05 Ω (TYP)

♦ ON-Resistance Flatness: 0.08Ω (TYP)

♦ High Off Isolation: 57dB at 10MHz

♦ 54dB (10MHz) Crosstalk Rejection Reduces Signal Distortion

♦ Break-Before-Make Switching

◆ -3dB Bandwidth: 700MHz

◆ Extended Industrial Temperature Range: –40°C to 85°C

◆ Improved Direct Replacement for NLAS7222

◆ Packaging (Pb-free & Green available)

### **General Description**

The BCT4222A is a high bandwidth, fast double-pole double-throw (DPDT) analog switch. Its wide bandwidth and low bit-to-bit skew allow it to pass high-speed differential signals with good signal integrity. Each switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. Industry-leading advantages include a propagation delay of less than 250ps, resulting from its low channel resistance and low I/O capacitance. Its high channel-to-channel crosstalk rejection results in minimal noise interference.

#### **Applications**

Cell

Phones

**PDAs** 

Portable Instrumentation

Differential Signal Data Routings

USB 2.0 Signal Routing

### **Connection Diagram**

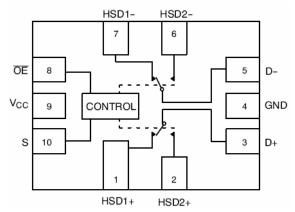


Figure 1. Pin Connections and Logic
Diagram (BCT4222A Top View)



### **Pin Description**

Pin Number	Name	Description
10	S	Select Input
4	GND	Ground
1,2	HSD1+,HSD2+	Data Ports
7,6	HSD1-,HSD2-	Data Ports
3,5	D+, D-	Data Ports
9	VCC	Positive Power Supply
8	/OE	Output Enable

### **Logic Function Table**

/OE	s	HSD1+,HSD1-	HSD2+,HSD2-
1	Х	OFF	OFF
0	0	ON	OFF
0	1	OFF	ON

### **ORDERING INFORMATION**

Ordering Code	Package Description	Temp Range	Top Marking
BCT4222AETB-TR	10-pin WQFN 1.4X1.8	-40°C to +85°C	AKX



### **MAXIMUM RATINGS**

Symbol	Pins	Parameter	Value	Unit	
V <sub>CC</sub>	V <sub>CC</sub>	Positive DC Supply Voltage	-0.5 to +4.6	V	
	HSD1+,				
	HSD1-,		0.545.V		
V <sub>IS</sub>	HSD2+,	Analog Signal Voltage	-0.5 to V <sub>CC</sub> +0.3	V	
	HSD2-				
	D+, D-		-0.5 to +4.6		
V <sub>IN</sub>	/OE	Control Input Voltage	-0.5 to +4.6	V	
Icc	V <sub>CC</sub>	Positive DC Supply Current	50	mA	
Ts		Storage Temperature	-65 to +150	°C	
I <sub>IN</sub>	/OE	Control Input Current	±20mA	mA	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability

### **ESD PROTECTION**

Symbol	Parameter	Value	Unit
ESD	Human Body Model - All Pins	2.0	kV
ESD	Human Body Model - I/O to GND	8.0	kV



#### RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Min	Max	Unit
V <sub>CC</sub>		Positive DC Supply Voltage	1.65	4.2	V
	HSD1+,				
	HSD1-,		GND	V <sub>cc</sub>	V
V <sub>IS</sub>	HSD2+,	Analog Signal Voltage			
	HSD2-				
	D+, D-		GND	4.2	
V <sub>IN</sub>	/OE	Digital Select Input Voltage	GND	Vcc	V
T <sub>A</sub>		Operating Temperature Range	-40	+85	°C

Minimum and maximum values are guaranteed through test or design across the Recommended Operating Conditions, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.



### **DC ELECTRICAL CHARACTERISTICS** (Typical: T = 25°C)

### **BCT4222A SUPPLY AND LEAKAGE CURRENT**

			neter Test Conditions	V 00	-4	0°C to +85	°C	Unit
Symbol	Pins	Parameter		V <sub>CC</sub> (V)	Min	Тур	Max	Unit
_	\/	Quiescent	$V_{IS} = V_{CC}$ or GND;	1.65 -4.2			1.0	
Icc	Vcc	Supply Current	I <sub>OUT</sub> = 0 A	1.00 -4.2	-	-	1.0	uA
		Increase in I <sub>CC</sub>					10	
I <sub>CCT</sub>	$V_{CC}$	per Control	V <sub>IN</sub> = 2.6 V	3.6	-	-	10	uA
		Voltage						
	HSD1+,	OFF State						
I <sub>OZ</sub>	HSD1-, HSD2+,	Leakage	$0 \le V_{IS} \le V_{CC}$	1.65 - 4.2	-	-	±1.0	uA
	HSD2-	Current						
loff	D+, D-	Power OFF						
		Leakage	0 ≤ V <sub>IS</sub> ≤4.5 V	0	-	-	±1.0	uA
		Current						

#### **BCT4222A DIGITAL INPUT VOLTAGE**

Symbol	Pins	Parameter Test Conditions	Test Conditions	V <sub>cc</sub> (V)	-40°C to +85°C			Unit
	riiis		VCC (V)	Min	Тур	Max	Onit	
V <sub>IH</sub>	Input High S,/OE 3.6	1.6			V			
VIH	3,/OL	Voltage		3.0	1.0	-	-	v
V <sub>IL</sub>	S,/OE	Input Low		3.6	_		0.5	\ <u>'</u>
	S,/UE	Voltage			_	-		V



#### **BCT4222A HIGH SPEED ON RESISTANCE**

Comple of	Dina	Davamatav	Took Conditions	anditions V (V)	-40°C to +85°C			Unit
Symbol	Pins	Parameter	Test Conditions	V <sub>cc</sub> (V)	Min	Тур	Max	Unit
			$V_{IS} = 0 \text{ V to } 0.4 \text{ V},$	2.7		9.0	12	
R <sub>ON</sub>		On-Resistance		3.3		8.0	10	Ω
			$I_{ON} = 8 \text{ mA}$	4.2		7.0	8.0	
	On-Resistance	V 0.V to 0.4.V	2.7		1.6			
R <sub>FLAT</sub>		$V_{IS} = 0 \text{ V to } 0.4 \text{ V},$	3.3		1.5		Ω	
		Flatness	$I_{ON} = 8 \text{ mA}$	4.2		1.4		
		On-Resistance	$V_{IS} = 0 \text{ V to } 0.4 \text{ V},$	2.7		1.6		
R <sub>ON</sub>				3.3		1.5		Ω
		Matching	I <sub>ON</sub> =8 mA	4.2		1.4		

### **BCT4222A DC ELECTRICAL CHARACTERISTICS**

(continued) FULL SPEED ON RESISTANCE (Typical: T = 25°C, V<sub>CC</sub> = 3.3 V)

Comple of	Dina	Donomoton	Test Conditions V	V 00	-40°C to +85°C			Unit
Symbol	Pins	Parameter		V <sub>CC</sub> (V)	Min	Тур	Max	Unit
Ron			V 0.V.tV	2.7		9.0	12	
		On-Resistance	$V_{IS} = 0 \text{ V to } V_{CC},$	3.3		8.5	10.5	Ω
			I <sub>ON</sub> = 8 mA	4.2		7.5	8.5	
		On-Resistance $V_{IS} = 0 \text{ V to } V_{CC}$ , Flatness $I_{ON} = 8 \text{ mA}$	V 0.V/to.V/	2.7		1.6		
R <sub>FLAT</sub>				3.3		1.5		Ω
			ION = O IIIA	4.2		1.4		
R <sub>ON</sub>		On-Resistance	$V_{IS} = 0 \text{ V to } V_{CC},$	2.7		2.20		
		Matching	$I_{ON} = 8 \text{ mA}$	3.3		2.45		Ω
		iviatoriirig	ION – O IIIA	4.2		2.65		



#### **BCT4222A AC ELECTRICAL CHARACTERISTICS**

TIMING/FREQUENCY (Typical: T = 25°C,  $V_{CC}$  = 3.3 V,  $R_L$  = 50 $\Omega$  ,  $C_L$  = 5 pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions V <sub>CC</sub> (V)		-40°C to +85°C			Unit
Symbol	rins	rarameter Test Conditions		VCC (V)	Min	Тур	Max	Onit
	Closed to	Turn-ON Time	Soo toot circuit 2	165 45		4.4	20	20
t <sub>ON</sub>	Open	Tulli-ON Tille	See test circuit 2	1.00 - 4.5		14	30	ns
t <sub>OFF</sub>	Open to	Turn-OFF Time	See test circuit 2	1.65 - 4.5		10	20	ne
LOFF	Closed	Tum-OFF Time	See lest circuit 2	1.05 - 4.5		10	20	115
4		Break-Before-Make	See test circuit 1	1.65 - 4.5	3.0	4.4	7.0	nc
t <sub>BBM</sub>		Delay	See lest circuit 1	1.03 - 4.3	3.0	4.4	30 ns 20 ns 7.0 ns	115
BW		0 10 0 1 1 111	C <sub>L</sub> = 5 pF	4.05.4.5		550		MHz
DVV		-3 dB Bandwidth	C <sub>L</sub> = 0 pF	1.65 - 4.5		700		IVII IZ

#### **BCT4222A ISOLATION**

(Typical: T = 25°C,  $V_{CC}$  = 3.3 V,  $R_L$  = 50 $\Omega$  ,  $C_L$  = 5 pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions	V <sub>cc</sub> (V)	-40°C to +85°C			Unit
	Pins				Min	Тур	Max	Oilit
OIDD	0	OFF looksing	4 050 MH-	1.65 -		22		40
OIRR	Open OFF-Isolation	OFF-Isolation	f = 250 MHz	4.5		-22		dB
VTALIC	HSD1+	Non-Adjacent	6 050 MH-	1.65 -		20		-ID
XTALK	to HSD1- Channel Crosstalk		f = 250 MHz	4.5		-30		dB



#### **BCT4222A CAPACITANCE**

(Typical: T = 25°C,  $V_{CC} = 3.3$  V,  $R_L = 50\Omega$ ,  $C_L = 5$  pF, f = 1 MHz)

Symbol	Pins	Parameter	Test Conditions	-40°C to +85°C			11
				Min	Тур	Max	Unit
C <sub>IN</sub>	OE	Control Pin Input	V 0.V	-	3.0	-	pF
		Capacitance	$V_{CC} = 0 V$				
C <sub>ON</sub>	D+ to	ON Capacitance	V <sub>CC</sub> = 3.3 V; OE = 0 V	-	8.0	-	pF
	HSD1+ or						
	HSD2+						
$C_{OFF}$	HSD2+,	OFF Capacitance	V <sub>CC</sub> = V <sub>IS</sub> = 3.3 V; OE	-	4.5	-	pF
	HSD2-		= 3.3 V				

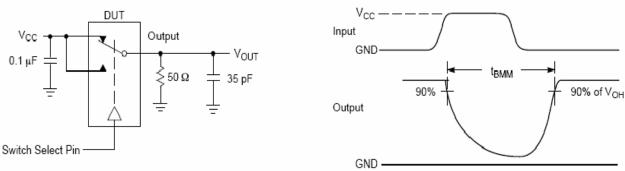


Figure 1. t<sub>BBM</sub> (Time Break-Before-Make)

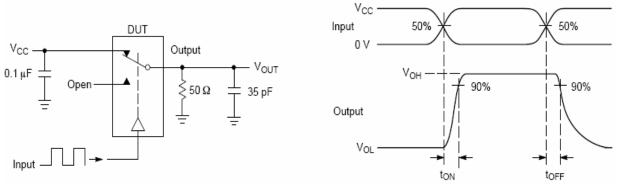


Figure 2. t<sub>ON</sub> / t<sub>OFF</sub>



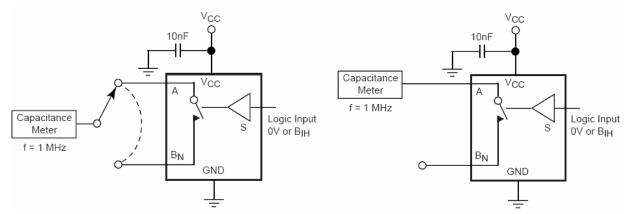


Figure 3. Channel ON/OFF Capacitance

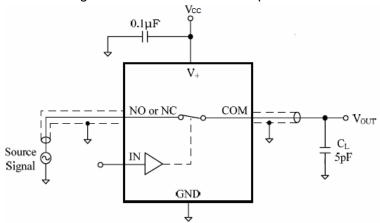


Figure 4. Bandwidth -3dB

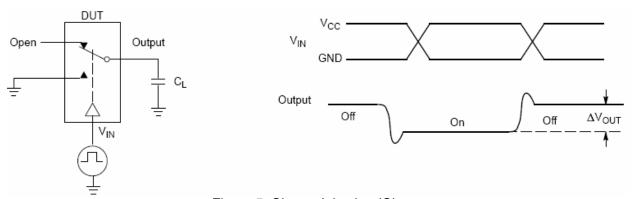


Figure 5. Charge Injecting (Q)



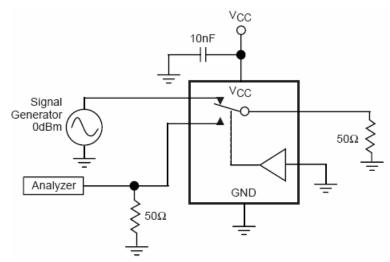


Figure 6. Crosstalk

### **Applications Information**

#### **Logic Inputs**

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V.

### **Eye Diagram Measurements**

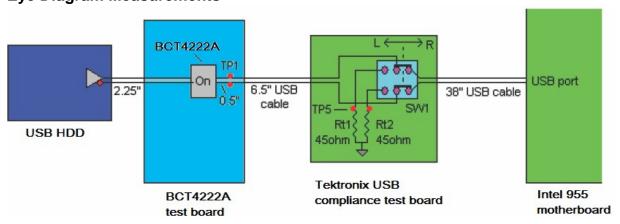


Figure 7: USB2.0 High-speed (480 Mbps) Signal Integrity Test Setup



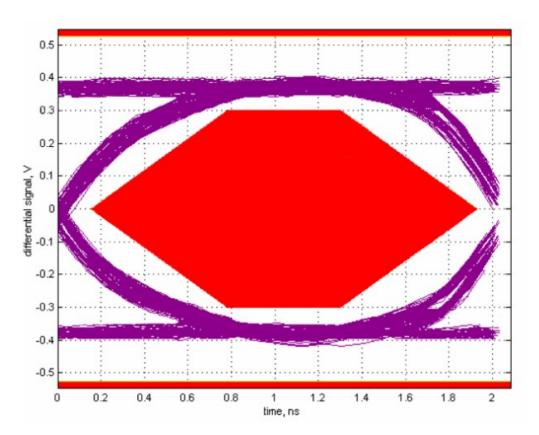
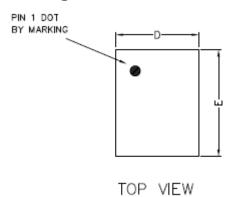
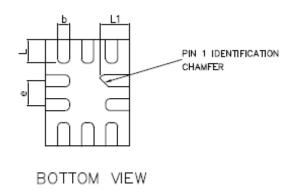


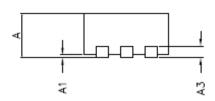
Figure 8: USB 2.0 High Speed (480Mbps) Eye Diagram Test(BCT4222A with Vcc=3.0V)



### **Package Information**







COMMON DIMENSIONS(MM)								
PKG.	UT: ULTRA THIN							
REF.	MIN.	NOM.	MAX					
Α	0.50	0.55	0.60					
A1	0.00	-	0.05					
А3	0.15 REF.							
D	1.35	1.40	1.45					
E	1.75	1.80	1.85					
Ь	0.15	0.20	0.25					
L	0.30	0.40	0.50					
L1	0.40	0.50	0.60					
е	0.40 BSC							

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