

BCT4899

$5\Omega\,$, High Speed, Low Voltage Dual DPDT Analog Switch

GENERAL DESCRIPTION

The BCT4899 is a high-speed, low-voltage, dual-independent double-pole double-throw (DPDT) COMS analog switch that is designed to operate from a single +1.8V to +5.5V power supply. It features high-bandwidth (500MHz) and low on-resistance (5 Ω TYP).

The BCT4899 is configured as a dual double-pole double-throw(DPDT) device with two logic control inputs that control two multiplexer/demultiplexer each. The configuration can also be used as a dual differential 2-to-1 multiplexer/demultiplexer.

BCT4899 is available in Green TQFN-3×3-16L and TQFN-2.5×2.5-16L and UTQFN1.8x2.6-16L

FEATURES

- Low Voltage Operation: 1.8V to 5.5V
- On-Resistance: $5\Omega(TYP)$
- -3dB Bandwidth: 500MHz
- Rail-to-Rail Input and Output Operation
- High Off-Isolation: -55dB at 10MHz
- Low Crosstalk: -60dB at 10MHz
- Low Power Consumption(<0.01uW)
- -40°C to +85°C Operating Temperature Range

APPLICATIONS

- Communication Systems
- Portable Instrumentation
- Audio and Video Switching
- PCMCIA Cards
- Computer Peripherals
- Modems
- PDAs

Order Number	Package Type	Temperature Range Marking		QTY/Reel	
		10°C to +85°C	4899	3000	
DC14099ETE-TK		-40 C 10 +65 C	XXXXX	3000	
			4899	2000	
DC14099EGE-1K	QFIN 2.5X2.5-10L	-40 C 10 +65 C	XXXXX	3000	
		10°C to 195°C	4899	2000	
DU14099EFE-IK		11.8x2.6-16L -40°C to +85°C		3000	

ORDERING INFORMATION

Mark Note:

"4899" in Marking is Product code

"XXXXX" in Marking will be appeared as the batch code.



PIN CONFIGURATION (Top View)





PIN DESCRIPTIONS

Pin	Name	Function	
1	NC1	Normally Closed Terminal Switch 1	
2	INA	Select Input, control switch 1 and switch 2	
3	NO2	Normally Open Terminal Switch 2	
4	COM2	Common Terminal Switch 2	
5	NC2	Normally Closed Terminal Switch 2	
6	GND	Ground	
7	NO3	Normally Open Terminal Switch 3	
8	COM3	Common Terminal Switch 3	
9	NC3	Normally Closed Terminal Switch 3	
10	INB	Select Input, control switch 3 and switch 4	
11	NO4	Normally Open Terminal Switch 4	
12	COM4	Common Terminal Switch 4	
13	NC4	Normally Closed Terminal Switch 4	
14	VCC	Positive Power Supply	
15	NO1	Normally Open Terminal Switch 1	
16	COM1	Common Terminal Switch 1	

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LOGIC DIAGRAM



TRUTH TABLE

INA	SWITCH STATE		
0	NC1 = COM1, NC2 = COM2		
1	NO1 = COM1, NO2 = COM2		

INB	SWITCH STATE
0	NC3 = COM3, NC4 = COM4
1	NO3 = COM3, NO4 = COM4



ABSOLUTE MAXIMUM RATINGS

VCC, INA, INB to GND	0.3V to +6.0V
All Other Pins to GND (Note 1)0.3'	V to (VCC + 0.3V)
Continuous Current (NOx, NCx, COM_)	±100mA
Operating Temperature Range	40°C to +85°C
Storage Temperature Range	-65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+260°C

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. Broadchip recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Broadchip reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact Broadchip sales office to get the latest datasheet.

RECOMMENDED OPERATING CONDTIONS

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications.

Symbol	Parameter	Min.	Max.	Unit
V _{cc}	Supply Voltage	1.5	5.5	V
V _{CTRL}	Control Input Voltage(INA,INB) ⁽²⁾	0	VCC	V
V _{SW}	Switch I/O Voltage	0	VCC	V
T _A	Operating Temperature	-40	+85	°C

Notes:

1. The input and output negative ratings maybe exceed if the input and output diode current ratings are observed.

2. The control input must be held HIGH or LOW; it must not float.



DC ELECTRICAL CHARACTERISTICS

Unless otherwise noted.	TA = +25°C.	(Note 1)
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PARAMETER	SYM	CONDITIONS	V _{cc} (V)	MIN	TYP	MAX	UNITS
Supply Voltage	V _{CC}			1.8		5.5	V
Quiescent Supply Current	I _{cc}	INA=0V or V_{CC} , INB=0V or V_{CC} , $I_{OUT}=0$	5.5			1	uA
Switch On Registeres	D	$0V \leq V_{SW} \leq V_{CC}$,	4.5		5.0	8.0	Ω
Switch Of Resistance	RON	I _{SW} =10mA,	2.7		12	22	Ω
On Resistance Matching			4.5		0.3	1.0	Ω
Between Channels		$v_{SW}=0v$, $I_{SW}=1011A$,	2.7		1.0	3.0	Ω
Flatness for On	A D	$0V \leq V_{SW} \leq V_{CC},$	4.5		2.0	3.0	Ω
Resistance		I _{SW} =10mA,	2.7		12	18	Ω
Off leakage Current of							
Open Data Paths	I _{OFF}	$0V \leq V_{SW} \leq V_{CC}$	1.8 to 5.5	-1		1	uA
(NCx and NOx Pin)							
On leakage Current of							
Close Data Paths	I _{ON}	$0V \leq V_{SW} \leq V_{CC}$	1.8 to 5.5	-1		1	uA
(NCx,NOx and COMx Pin)							
Input Voltage High	МЦ		1 9 to 5 5	1 5			V
(INA, INB)	VILL		1.0 10 5.5	1.5			V
Input Voltage Low	VII		1 9 to 5 5			0.4	V
(INA, INB)	VIL		1.0 10 5.5			0.4	V



AC ELECTRICAL CHARACTERISTICS

Unless otherwise noted. $TA = +25^{\circ}C$. (Note 1)

PARAMETER	SYM	CONDITIONS	V _{cc} (V)	MIN	TYP	MAX	UNITS
Turn-On Time	t _{ON}	R _L =300Ω, C _L =35pF, V _{SW} =0 to 1.5 V	3.0		35		ns
Turn-Off Time	t _{OFF}	R _L =300Ω, C _L =35pF, V _{SW} =0 to 1.5 V	3.0		45		ns
Break-Before-Make Time	t _{BBM}	$\label{eq:VNC} \begin{split} V_{\text{NC}} &= V_{\text{NC}} = 1.5 \text{ V} \ , \\ R_{\text{L}} &= 300 \Omega, \ C_{\text{L}} = 35 \text{pF} \end{split}$	3.0		6		ns
Charge Injection	Q	$V_G = 0V, R_G = 0\Omega, CL = 1nF$	3.0		3		
-3db Bandwidth ⁽²⁾	BW	$V_{S}=0$ dBm,R _L =50 Ω , C _L =0pF	3.0		500		MHz
Off Isolation ⁽²⁾	O _{IRR}	V_S =0dBm,f=10MHz, R _L =50Ω	3.0		-55		dB
Crosstalk ⁽²⁾	X _{TALK}	V_S =0dBm,f=10MHz, R _L =50 Ω	3.0		-60		dB
Output On Capacitance ⁽²⁾	C _{ON}	/OE=0V, f=1MHz	3.0		10		pF
Output Off Capacitance ⁽²⁾	C_{OFF}	/OE=3.3V, f=1MHz	3.0		6		pF

Note 1: Devices are 100% tested at TA = $+25^{\circ}$ C. Limits across the full temperature range are guaranteed by design and correlation.

Note 2: Guaranteed by characterization.



TEST CIRCUITS



Figure 1. Switching Time



Figure 2. Break-Before-Make Interval





 $Q = C_L \times \Delta V_{OUT}$











PACKAGE OUTLINE DIMENSIONS

TQFN 3x3-16L



Top Vlew



Side View

Construction of	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
А	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A2	0.153	0.253	0.006	0.010
D	2.900	3.100	0.114	0.122
E	2.900	3.100	0.114	0.122
D1	1.600	1.800	0.063	0.071
E1	1.600	1.800	0.063	0,071
k	0.200	0.200MIN.		BMIN.
b	0.180	0.300	0.007	0.012
е	0.500	DTYP.	0.500TYP.	
L	0.300	0.500	0.012	0.020



Bottom View



TQFN 2.5x2.5-16L





UTQFN 1.8x2.6 -16L









SIDE	VIE	W

COMMON DIMENSIONS(MM)						
PKG,	TU	ULTRA THIN	1			
REF.	MIN.	N□M.	МАХ			
Α	>0,50	0.55	0.60			
A1	0.00	_	0.05			
A3		0.15 RFF.				
D	1.75	1.80	1,85			
E	2.55	2.60	2,65			
L	0,30	0,40	0.50			
b	0.15	0,20	0,25			
e	0.40 BSC					

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