

Continental Device India Limited

An ISO/TS16949 and ISO 9001 Certified Company



NPN SILICON PLANAR SWITCHING TRANSISTORS

2N2221 2N2222

TO-18 Metal Can Package



General Purpose Transistor

ABSOLUTE MAXIMUM RATINGS

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Emitter Voltage	V _{CEO}	30	V
Collector Base Voltage	V_{CBO}	60	V
Emitter Base Voltage	V_{EBO}	5	V
Collector Current Continuous	I _C	800	mA
Power Dissipation @ T _a =25°C	P_{D}	500	mW
Derate Above 25°C		2.85	mW/ °C
Power Dissipation @ T _c =25°C	P_{D}	1.2	W
Derate Above 25°C		6.85	mW/ °C
Operating And Storage Junction Temperature Range	T_{j},T_{stg}	- 65 to +200	°C

ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

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DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Emitter Voltage	*V _{CEO}	$I_C=10$ mA, $I_B=0$	30			V
Collector Base Voltage	V_{CBO}	I_{C} =10 μ A, I_{E} =0	60			V
Emitter Base Voltage	V_{EBO}	I_{E} =10 μ A, I_{C} =0	5			V
Collector Cut Off Current	I _{CBO}	V_{CB} =50V, I_{E} =0			10	nA
		V _{CB} =50V, I _E =0, T _a =150°C			10	μΑ
Collector Cut Off Current	I _{EBO}	V_{EB} =3 V , I_{C} =0			10	nA

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DC Current Gain	h _{FE}	*I _C =0.1mA, V _{CE} =10V	>20	>35	
		I_C =1mA, V_{CE} =10V	>25	>50	
		*I _C =10mA, V _{CE} =10V	>35	>75	
		*I _C =150mA, V _{CE} =10V	40 - 120	100 - 300	
		*I _C =150mA, V _{CE} =1V	>20	>50	
		*I _C =500mA, V _{CE} =10V	>20	>30	

*Pulse Test: Pulse Width ≤ 300ms, Duty Cycle ≤ 2%



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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

SMALL SIGNAL CHARACTERISTICS

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Collector Emitter Saturation Voltage	*V _{CE (sat)}	I _C =150mA, I _B =15mA			0.4	V
		I_C =500mA, I_B =50mA			1.6	V
Base Emitter Saturation Voltage	*V _{BE (sat)}	I _C =150mA, I _B =15mA			1.3	V
		I_C =500mA, I_B =50mA			2.6	V
Transition Frequency	**f _T	I _C =20mA, V _{CE} =20V, f=100MHz	250			MHz
Output Capacitance	C _{obo}	V _{CB} =10V, I _E =0, f=100KHz			8.0	pF
Input Capacitance	C _{ibo}	V _{BE} =0.5V, I _C =0, f=100KHz			30	pF

SWITCHING TIME

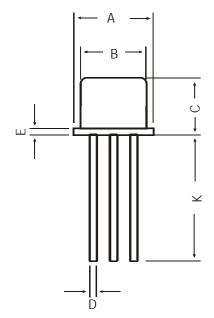
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Delay Time	t _d	I _C =150mA, I _{B1} =15mA,			10	ns
Rise Time	t _r	V_{CC} =30V, $V_{BE(off)}$ =0.5V			25	ns
Storage Time	t_s	I _C =150mA, I _{B1} =			225	ns
Fall Time	t _f	I _{B2} =15mA, V _{CC} =30V			60	ns

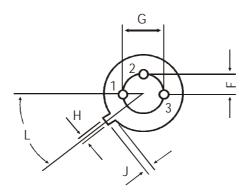
^{*}Pulse Test: Pulse Width ≤ 300ms, Duty Cycle ≤ 2%

^{**} f_T is defined as the frequency at which Ih_{fe}I extrapolates to unity

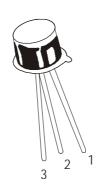
TO-18 Metal Can Package

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B 4.52	5.84 1.97 5.33				
C 4.31 5	. 33				
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D 0.40 ().53				
E — ().76				
E F — ´	1.27				
⊑ G — 2	2.97				
일 H 0.91 1	1.17				
J 0.71 °	1.21				
F — 2 G — 2 K H 0.91 J 0.71 K 12.70	_				
	45 DEG				



PIN CONFIGURATION

- 1. EMITTER
- 2. BASE3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size Oty Gr		
TO-18	1K/polybag	350 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	34 kgs

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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