



PNP SILICON EPITAXIAL PLANAR TRANSISTORS

BC556_BC560





For switching and AF amplifier application

ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless specified otherwise)

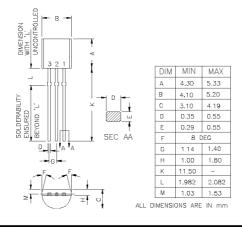
DESCRIPTION	SYMBOL	BC556	BC557	BC560	BC558	BC559	UNITS
Collector Base Voltage	V _{CBO}	80	50 30		0	V	
Collector Emitter Voltage	V _{CEO}	65	45 30		0	V	
Emitter Base Voltage	V _{EBO}	5					V
Collector Current (DC)	I _C	100					mA
Collector Current - Peak	I _{CM}	200					mA
Power Dissipation	P _{tot}	500					mW
Storage Temperature	T _{stg}	- 65 to +150					°C
Junction Temperature	Т _ј	150					°C

Characteristics at Ta = 25°C

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	MAX	UNITS
		I _C =2mA, V _{CE} =5V	75	800	
DC Current Gain	h _{FE}	Α	110	220	-
		В	200	450	-
		ι _c =10mA, Ι _B =0.5mA	420	800 0.30	- V
Collector Emitter Saturation Voltage	V _{CE(Sat)}	0	-		
		$I_{\rm C}$ =100mA, $I_{\rm B}$ =5mA	-	0.65	V
Base Emitter on Voltage	$V_{\text{BE(on)}}$	$I_{C}=2mA, V_{CE}=5V$	0.55	0.75	V
	BE(01)	I _C =10mA, V _{CE} =5V	-	0.82	V
Collector Base Cut off Current	I _{CBO}	V_{CB} =30V, I _E =0	-	15	nA
Emitter Base Cut off Current	I _{EBO}	V _{EB} =5V	-	100	nA
Collector Base Breakdown Voltage					
BC556	V _{(BR)CBO}	1 1001	80	-	
BC557 , BC560		I _C =100μΑ	50	-	V
BC558 , BC559			30	-	
Collector Emitter Breakdown Voltage					
BC556	V _{(BR)CEO}	I _C =2mA	65	-	
BC557 , BC560			45	-	V
BC558 , BC559			30	-	
Emitter Base Breakdown Voltage	V _{(BR)EBO}	Ι _Ε =100μΑ	5	-	V
Transition Frequency	f _T		100	-	MHz
Collector Base Capacitance	C _{cb}	V _{CB} =10V, f=1MHz	-	6.0	pF

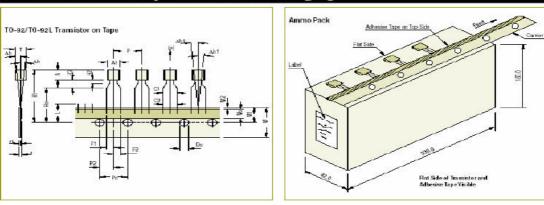
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BC556_BC560 **TO-92 Plastic Package**



Packaging Specifications									
T & A: Tape and Ammo Pack;	T & R: Tape and Reel; Bulk: I	Loose in Poly Bags; Tube:	Tube and Carto	n; K: 1,000					
Package / Case Type	Packaging Type	Std. Packing	Inner Carbon			Outer Carton			
		Qty	Qty	\$ize L x W x H	Gross Weight	Qty	Size L x W x H	Gross Weight	
				(cm)	(Kg)		(cm)	(Kg)	
10-92	Bulk	1,000	5K	19 x 19 x 8	1.1	80K	43 x 40 x 35	20.0	
	T&A	2,000	2K	32 x 4.5 x 20	0.7	40K	43 x 40 x 35	15.2	

TO-92 and TO-92L Tape and Ammo Packaging



Min 4.7

7.8

3.7

Nom

12.7 12.7

6.35

0 0 18.0

6.0 9.0

16.0

4.0

0.2

Max 5.1

8.2

4.1

1.0

29.0 11.0

2.0

1.45

4.0

0.22

Tol

±0.3

±0.2

±0.3

±1.0 ±1.0 +1.0 -0.5

+0.5

±0.5

±0.5

±0.2

±0.5

Tape Specifications

		T0-92
Item description	Symbol	Min
Body width	A1	4.45
Body height	A	4.32
Body thickness	T	3.18
Pitch of component ^{CP}	P	
Feed hole pitch ⁵¹	Po	§ §
Feed hole center to component centre ⁵²	Pz	
Comp. alignment, Side view ⁴³	Dh	
Comp. alignment, Front view ⁶³	Dhrt	- C - C
Tape width ^{or}	W	6 S
Hold down tape width ^{or}	Wo	
Hole position	Wi	
Hold-down tape position	W2	0.0
Lead wire olinah height	Ho	16 S
Componentheight	H1	
Length of snipped leads	L	
Feed hole diameter ^{cr}	Do	ni s
Total tape thickness ⁵⁴	t	
Lead-to-lead distance ^{Cr}	F1, F2	2.4
Stand off	H2	0.45
Clinch height	H3	i (
Lead parallelismCr	C1-Cz	
Pull-out force	(p)	6N



Taping Specification

- Maximum alignment deviation between leads not to be greater than 0.20 mm. Maximum non-cumulative variation
- between type feed holes shal not ex 1 mm in 20 pitches.
- Hold down tape not to exceed beyond the edge(s) carrier tape and there shall be no
- exposure of adhesive. No more than 3 consecutive missing
- components is permitted. A tape trailer, having at least three . feed holes is required after the last
- compenent. Splices shall not interfere with the
- sprocket feed holes.

§1 Cumulative pitch error 1.0 mm/20 pitch.

§2 To be measured at bottom of clinch.
§3 At top of body.

§4 ti = 0.3 – 0.6 mm Cr Critical Dimension.

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2.2

0.45

6N

TO-92 Plastic Package

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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