#### L, S-band Medium Power SPDT Switch



#### **Features**

Control voltage :

$$VC(H) = 1.8 \text{ to } 5.0 \text{ V } (3.0 \text{V TYP.})$$
  
 $VC(L) = -0.2 \text{ to } 0.2 \text{ V } (0 \text{V TYP.})$ 

• Low insertion loss:

$$L_{ins}1 = 0.39$$
 dB TYP. @ f = 1.0 GHz  $L_{ins}2 = 0.44$  dB TYP. @ f = 2.0 GHz  $L_{ins}3 = 0.45$  dB TYP. @ f = 3.0 GHz

• High isolation:

ISL1 = 32 dB TYP. @ 
$$f = 1.0$$
 GHz  
ISL2 = 26 dB TYP. @  $f = 2.0$  GHz  
ISL2 = 22 dB TYP. @  $f = 3.0$  GHz

• Handling power:

$$P_{in(0.1dB)} = +30.0 \text{ dBm TYP.} @ f = 3.0 \text{ GHz},$$
  
 $VC(H) = 3.0 \text{ V, } VC(L) = 0 \text{ V}$ 

## **Applications**

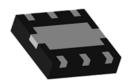
• Wireless LAN (IEEE802.11 b/g)

#### **Package**

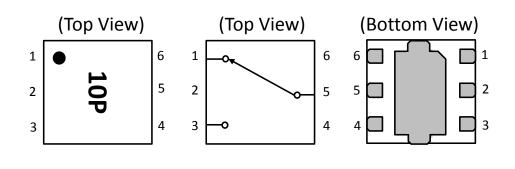
6-pin Thin SON Package(XS03) (1.5mm x 1.5mm x 0.37mm)

#### **Description**

 The CKRF2406XS03 is a GaAs MMIC SPDT(Single Pole Double Throw) switch which was developed for wireless LAN



# Pin Configuration and Internal Block Diagram



Pin No.	Pin Name
1	RF1
2	GND
3	RF2
4	VC2
5	RFC
6	VC1

Remark Exposed pad : GND

# **Ordering Information**

Part Number	Order Number	Package	Marking	Supplying Form
CKRF2406XS03-C2	CKRF2406XS03-C2	6-pin TSON	10P	•Embossed tape 8 mm wide
		(Pb-Free)		•Pin 1, 6 face the perforation
				side of the tape
				·Qty 10 kpcs/reel

CDS-0034-04 Page 1 of 7

## L, S-band Medium Power SPDT Switch



## **Absolute Maximum Ratings**

(TA =  $+25^{\circ}$ C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Control Voltage	VC	6.0 <sup>Note 1</sup>	V
Input Power	Pin	+33.0 <sup>Note 2</sup>	dBm
Operating Ambient Temperature	T <sub>A</sub>	-45~+85	$^{\circ}$
Storage Temperature	T <sub>stg</sub>	-55~+150	$^{\circ}$

Note

1. |VC1 - VC2|≦6.0V

2. 3.0V≦|VC1 - VC2|≦5.0V

## **Recommended Operating Range**

(TA = +25°C, unless otherwise specified)

<u> </u>						
Parameter	Symbol	MIN.	TYP.	MAX.	Unit	
Operating Frequency	f	0.05	-	3.0	GHz	
Switch Control Voltage (H)	VC(H)	+1.8	+3.0	+5.0	V	
Switch Control Voltage (L)	VC(L)	-0.2	0	+0.2	V	

## **Truth Table**

VC1	VC2	RFC-RF1	RFC-RF2
High	Low	ON	OFF
Low	High	OFF	ON

CDS-0034-04 Page 2 of 7

### L, S-band Medium Power SPDT Switch



#### · Electrical Characteristics

 $(T_A=+25\,^{\circ}\text{C},\ VC(H)=3.0V,\ VC(L)=0V,\ Zo=50\,^{\circ}\Omega,\ DC\ Block\ Capacitance=56pF,\ unless\ otherwise$  specified)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	Lins1	f = 0.05 to 0.5 GHz Note 1	-	0.35	0.55	dB
	Lins2	f = 0.5 to 1.0 GHz	-	0.39	0.59	dB
	Lins3	f = 1.0 to 2.0 GHz	-	0.44	0.64	dB
	Lins4	f = 2.0 to 2.5 GHz	-	0.45	0.65	dB
	Lins5	f = 2.5 to 3.0 GHz	-	0.45	0.65	dB
Isolation	ISL1	f = 0.05 to 0.5 GHz Note 1	35	38	-	dB
	ISL2	f = 0.5 to 1.0 GHz	29	32	_	dB
	ISL3	f = 1.0 to 2.0 GHz	23	26	-	dB
	ISL4	f = 2.0 to 2.5 GHz	21	24	-	dB
	ISL5	f = 2.5 to 3.0 GHz	19	22	_	dB
Input Return Loss	RL <sub>in</sub> 1	f = 0.05 to 0.5 GHz Note 1	15	20	-	dB
	RL <sub>in</sub> 2	f = 0.5 to 3.0 GHz	15	20	-	dB
Output Return Loss	RL <sub>out</sub> 1	f = 0.05 to 0.5 GHz Note 1	15	20	-	dB
	RL <sub>out</sub> 2	f = 0.5 to 3.0 GHz	15	20	-	dB
0.1 dB Loss Compression	P <sub>in(0.1dB)</sub>	f = 3.0 GHz	-	+23	-	dBm
Input Power Note 2		VC(H)=1.8V, VC(L)=0V				
		f = 3.0 GHz		+30	-	dBm
		VC(H)=3.0V, VC(L)=0V				
0.5 dB Loss Compression	P <sub>in(0.5dB)</sub>	f = 3.0 GHz	_	+26	-	dBm
Input Power Note 3		VC(H)=1.8V, VC(L)=0V	_			
		f = 3.0 GHz	_	+32	-	dBm
		VC(H)=3.0V, VC(L)=0V				
2nd Harmonics	2f0	$f = 3.0 \text{ GHz}, P_{in} = +20 \text{dBm}$	-	-85	-	dBc
3rd Harmonics	3f0	$f = 3.0 \text{ GHz}, P_{in} = +20 \text{dBm}$	-	-85	-	dBc
Input 3rd Order	IIP3	f = 2.5GHz	-	+58	-	dBm
Intercept Point	111-2	2-tone 1MHz Spacing				
Error Vector Magnitude	EVM	802.11g, 64QAM, 54Mbps		2.5		%
		Pin≦+25dBm				
Switch Control Speed	tsw	50% CTL to 90/10% RF	-	50	-	ns
Switch Control Current	Icont	Non RF	-	1	-	μ <b>A</b>

Note 1 DC block capacitance = 1,000pF at f=0.05 to 0.5 GHz

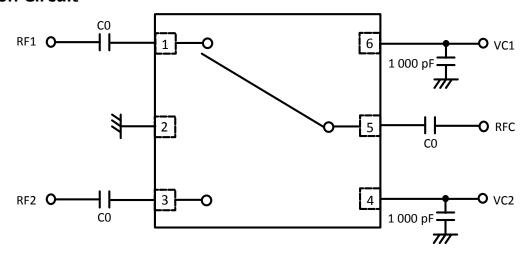
Note 2  $P_{in(0.1dB)}$  is the measured input power level when the insertion loss increases 0.1dB more than that of the linear range.

Note 3 P<sub>in(0.5dB)</sub> is the measured input power level when the insertion loss increases 0.5dB more than that of the linear range.

### L, S-band Medium Power SPDT Switch



#### **Evaluation Circuit**

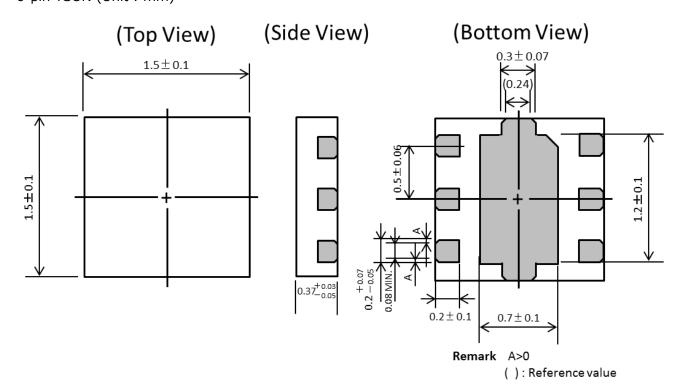


Note C0: 0.05 to 0.5 GHz 1,000pF : 0.4 to 3.0 GHz 56pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins. This device is used it is necessary to use DC Block Capacitance.

## **Package Dimensions**

6-pin TSON (Unit: mm)



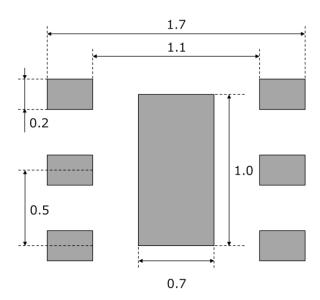
CDS-0034-04 Page 4 of 7

# L, S-band Medium Power SPDT Switch



# **PCB Layout Footprint**

6-pin TSON (Unit: mm)



The PCB Layout Footprint in this document is for reference only.

#### L, S-band Medium Power SPDT Switch



#### [CAUTION]

- All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice.
- You should not alter, modify, copy, or otherwise misappropriate any CDK product, whether in whole or in part.
- CDK does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of CDK products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of CDK or others.
- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. CDK assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- CDK has used reasonable care in preparing the information included in this document, but CDK does not warrant that such information is error free. CDK assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- Although CDK endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions.
  - Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a CDK product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures

    Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
- Please use CDK products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive.
   CDK assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of CDK.

CDS-0034-04 Page 6 of 7

#### L, S-band Medium Power SPDT Switch



[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

#### [CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

CHUO DENSHI KOGYO CO., LTD

3400 Kooyama, Matsubase, Uki-City,

Kumamoto 869-0512, Japan Tel: +81-964-32-2730 Fax: +81-964-32-3549

URL : http://www.en.cdk.co.jp/

Contact info for inquiries

Electronic Devices Division Sales and Planning Department

TEL : +81-964-32-2750 E-mail : info@cdk.co.jp FAX : +81-964-32-3549

CDS-0034-04 Page 7 of 7

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Analogue Switch ICs category:

Click to view products by CDK manufacturer:

Other Similar products are found below:

FSA3051TMX NLVAS4599DTT1G MAX4992EVB+T MAX4684ETB+T BCM6522IPBG BCM65300IFSBG MAX14764ETA+T
TMUX1113RSVR TMUX1112RSVR ADG1436TRUZ-EP BL4684C PE423422A PE42359SCAA PE42540F RS550YUCM12

ADGS1414DBCCZ ADG658YRUZ-REEL7 RS2117YUTQK10 RS2118YUTQK10 RS2227XUTQK10 SP2526A-1EN-L/TR FSA4157P6X
BA7603F-E2 MAX4702EUE+ MAX4617CUE+ MAX4599EUT+T MAX4066ESD+ MAX4052ACSE+ MAX396CAI+ MAX391CPE+
MAX4730EXT+T MAX314CPE+ MAX4051AEEE+ BU4066BCFV-E2 MAX313CPE+ BU4S66G2-TR TS3A4751PWR

NCN1154MUTAG DG444DY-E3 NLAS4157DFT2G NLAS4599DFT2G NLAS7242MUTBG NLASB3157DFT2G NLAST4599DFT2G

NLAST4599DTT1G DG403DY-T1-E3 MAX4714EXTT MAX392CPE BGSX22G2A10E6327XTSA1 ADG1611BRUZ-REEL7