

Specification Sheet for Approved

Customer Name:	
Customer Part No.:	
Ceaiya Part No:	MAI4012A 系列
Spec No:	L414

【For Customer Approval Only】

If you Approval, Please Stamp

【RoHS Compliant Parts】

Approved By	Checked By	Prepared By
李庆辉	查凯	劳水花

Shenzhen Ceaiya Electronics Co., Ltd.

地址 1: 深圳市龙华区观澜街道银星智界一期综合楼 716 号

地址 2: 广东省东莞清溪镇青滨东路 105 号力合紫荆智能制造中心 10 栋

Http://www.ceaiya.com

Tel: 0769-89135516

Fax: 0769-89135519

Specification Sheet for SMD Power Inductor

5. Electrical Characteristics

Part Number	Inductance	DC Resistance		Saturation Current		Heat Rating Current	
	1MHz/1V	Max.	Typ.	Max.	Typ.	Max.	Typ.
Units	uH	Ω	Ω	A	A	A	A
Symbol	L	DCR		Isat		Irms	
MAI4012A-R47MT	0.47 \pm 20%	0.041	0.033	7.20	8.20	3.80	4.30
MAI4012A-R56MT	0.56 \pm 20%	0.050	0.040	6.00	7.00	3.20	3.80
MAI4012A-R68MT	0.68 \pm 20%	0.055	0.042	5.20	6.20	3.25	3.80
MAI4012A-1R0MT	1.0 \pm 20%	0.059	0.049	3.80	4.60	3.00	3.50
MAI4012A-1R5MT	1.5 \pm 20%	0.075	0.060	3.80	4.50	2.80	3.20
MAI4012A-2R2MT	2.2 \pm 20%	0.090	0.075	2.80	3.30	2.50	3.00
MAI4012A-3R3MT	3.3 \pm 20%	0.130	0.106	2.80	3.30	2.00	2.50
MAI4012A-4R7MT	4.7 \pm 20%	0.175	0.145	2.30	2.60	1.80	2.10
MAI4012A-6R8MT	6.8 \pm 20%	0.230	0.190	1.60	2.20	1.50	1.75
MAI4012A-8R2MT	8.2 \pm 20%	0.273	0.210	1.58	1.95	1.46	1.68
MAI4012A-100MT	10 \pm 20%	0.360	0.300	1.55	1.85	0.85	1.00
MAI4012A-220MT	22 \pm 20%	0.800	0.650	1.00	1.25	0.80	0.90

Note:※1: Rated current: Isat(max.)or Irms(max.),whichever is smaller;

※2: Saturation Current: Max. Value, DC current at which the inductance drops less than 30% from its value without current; Typ. Value, DC current at which the inductance drops 30% from its value without current;

※3: Irms: DC current that causes the temperature rise (ΔT) from 20 $^{\circ}$ C ambient.

For Max. Value, $\Delta T < 40^{\circ}$ C; for Typ. Value, ΔT is approximate 40 $^{\circ}$ C.

The part temperature (ambient + temp. rise) should not exceed 125 $^{\circ}$ C under worst case operating conditions.

Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

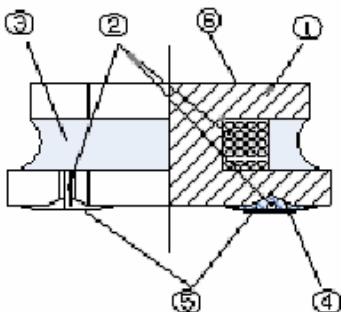
※4: Absolute maximum voltage: DC 40V

Typical Electrical Characteristics:

Please refer to appendix

6. Structure

The structure of MAI4012A product.

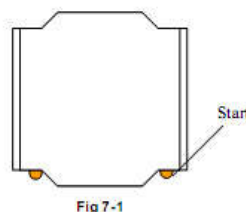


NO.	Components	Material
①	Core	Soft magnetic Metal
②	Wire	Polyurethane system enameled copper wire
③	Magnetic Glue	Epoxy resin and magnetic powder
④	Substrate	FeNiCu/Ag
⑤	Top Electrodes	Sn alloy
⑥	Marking	Nitrocellulose

7. Product Marking

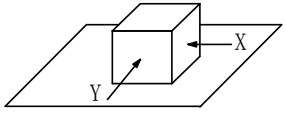
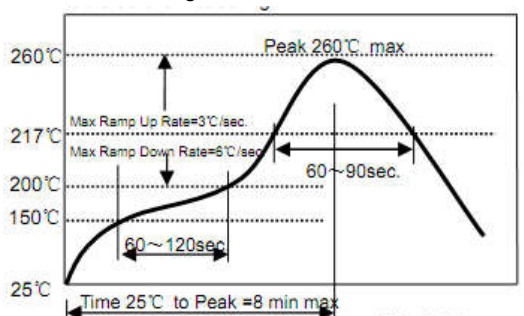
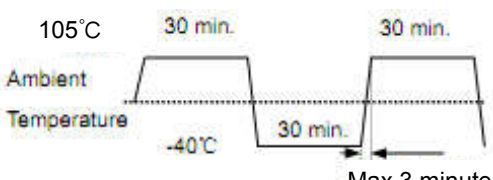
Please refer to Fig.7-1

The content of marking please refers to Item 12.



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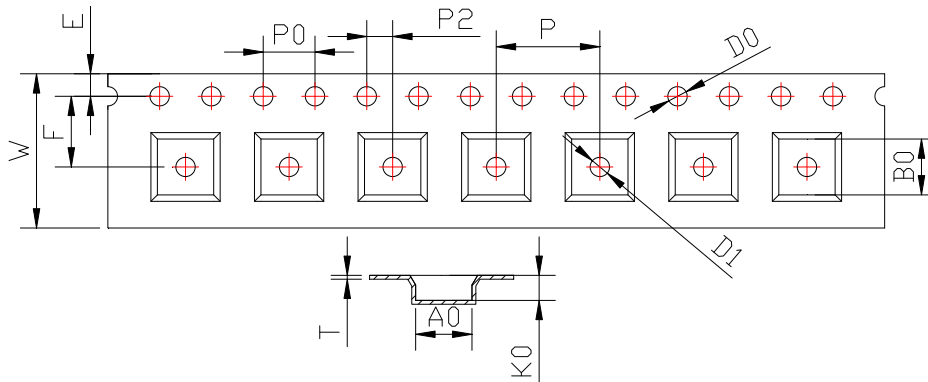
8. Reliability Test

Items	Requirements	Test Methods and Remarks
8.1 Terminal Strength	No removal or split of the termination or other defects shall occur.  Fig.7.1-1	1) Solder the inductor to the testing jig (glass epoxy board shown in Fig.7.1-1) using eutectic solder. Then apply a force in the direction of the arrow. 2) 10N force. 3) Keep time: 5±2s
8.2 High Temperature	1. No visible mechanical damage. 2. Inductance change: Within ±10%	1) Storage Temperature :125+/-5°C 2) Duration : 96 ±4 Hours 3) Recovery : then measured at room ambient temperature after placing 24 hours.
8.3 Low Temperature	1. No visible mechanical damage 2. Inductance change: Within ±10%	1) Temperature and time: -40±5°C 2) Duration: 96±4 hours 3) TRecovery : then measured at room ambient temperature after placing 24 hours.
8.4 Vibration test	1. No visible mechanical damage. 2. Inductance change: Within ±10%	1) Frequency range:10HZ~55HZ~10HZ 2) Amplitude:1.5mm p-p 3) Direction:X,Y,Z 4) Time:1 minute/cycle,2hours per axis
8.5 High Temperature Storage Tested	1. No visible mechanical damage. 2. Inductance change: Within ±10%	1) Storage Temperature :60+/-2°C 2) Relative Humidity :90-95% RH 3) Duration : 96 ±4 Hours 4) Recovery : then measured at room ambient temperature after placing 24 hours.
8.6 Resistance to Soldering Heat	1. No visible mechanical damage. 2. Inductance change: Within ±10%  Fig.7.6-1	1) Re-flowing Profile: Please refer to Fig.7.6-1 2) Test board thickness: 1.0mm 3) Test board material: glass epoxy resin 4) The chip shall be stabilized at normal condition for 1~2 hours before measuring
8.7 Thermal Shock	1. No visible mechanical damage. 2. Inductance change: Within ±10%  Fig.7.7-1	1) Temperature and time: -40±3°C for 30±3 min→105°C for 30±3min, please refer to Fig.7.7-1. 2) Transforming interval: Max, 3 minute 3) Tested cycle: 100 cycles 4) The chip shall be stabilized at normal condition for 1~2 hours before measuring

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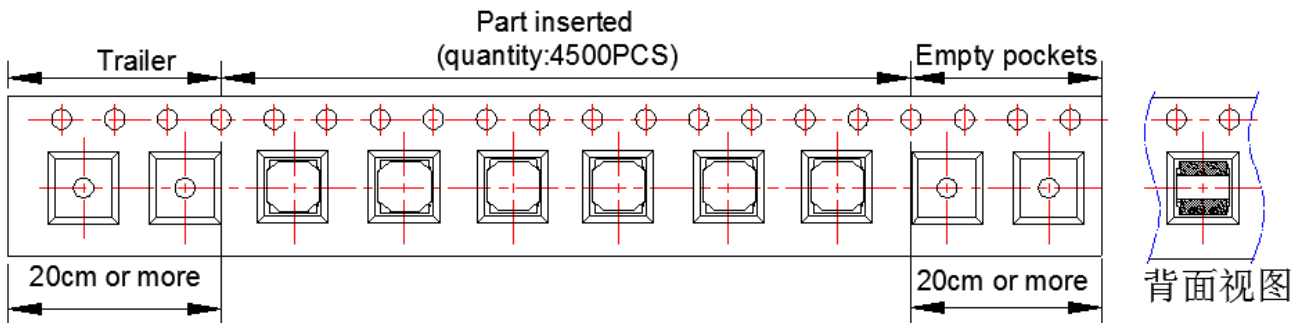
9. Packaging and Marking:

9-1. Carrier Tape Dimensions:

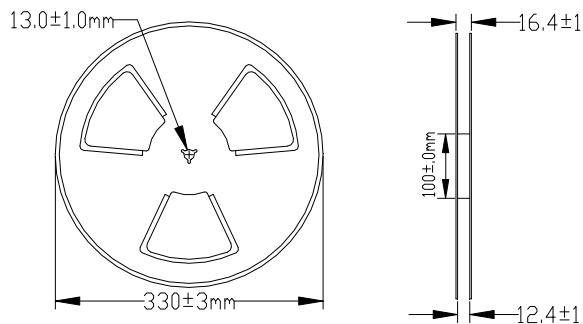


ITEM	W	A0	B0	K0	P	F	E	D0	D1	P0	P2	T
DIM	12.00	4.3	4.3	1.6	8.00	5.50	1.75	1.50	1.50	4.00	2.00	0.30
TOLE	+0.30 -0.10	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	+0.1	+0.1	±0.1	±0.1	±0.05

9-2. Taping Dimensions:



9-3. Reel Dimensions:

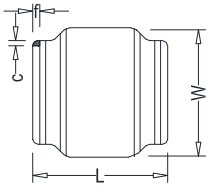
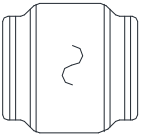
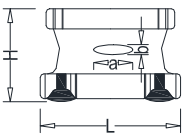
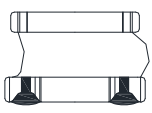
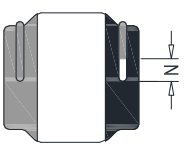
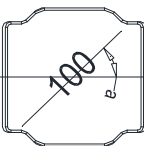


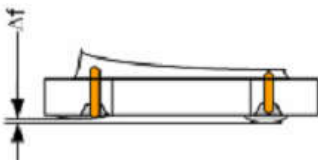
9-4. Packaging Quantity:

4.5KPCS/ Reel 13.5KPCS/ Inner Box 40.5KPCS/ Outer Box

Specification Sheet for SMD Power Inductor

Visual Inspection Standard of Product

No.	Defect Item	Figure	Rejection Identification	Acceptance
1	Core Defect		The defect length(c or f) more than L/6 or W/6 , NG	AQL=0.65
2	Core Crack		Visual cracks , NG	AQL=0.65
3	Starvation		(1)Resin starved length a more than L/2, NG (2)When L>2mm,b>H/2, NG (3)When L≤2mm, b don't control	AQL=0.65
4	Excessive glue		The length, width or height of product beyond specified value, NG	AQL=0.65
5	Cold Solder		(1)For CR2520** Series , cold solder N>0.5mm,NG (2)For other series, cold solder N>1mm,NG	AQL=0.65
6	Marking Defect		The marking angle a>45° , NG	AQL=0.65



Δf: Clearance between terminal and the surface of plate must be 0.15mm max when coil is placed on a flat plate.

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