## 2N4030

# PNP SILICON TRANSISTOR



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# DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N4030 is a silicon PNP transistor, mounted in a hermetically sealed package, designed for general purpose amplifier applications.

MARKING: FULL PART NUMBER



MAXIMUM	RATINGS: (T <sub>A</sub> =25°C unless otherwis	se noted)		
		SYMBOL		UNITS
Collector-Ba	ase Voltage	$V_{CBO}$	60	V
Collector-Emitter Voltage		$V_{CEO}$	60	V
Emitter-Bas	e Voltage	$V_{EBO}$	5.0	V
Continuous	Collector Current	IC	1.0	Α
Power Dissi	pation	$P_{D}$	1.25	W
Power Dissi	pation (T <sub>C</sub> =25°C)	$P_{D}$	7.0	W
Operating and Storage Junction Temperature		T <sub>J</sub> , T <sub>stg</sub>	-65 to +200	°C
Thermal Resistance		⊚¹C	20	°C/W
Thermal Resistance		$\Theta_{\sf JA}$	140	°C/W
ELECTRICA	AL CHARACTERISTICS: (T <sub>A</sub> =25°C ι	unless otherwise n	oted)	
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V <sub>CB</sub> =50V		50	nA
ICBO	V <sub>CB</sub> =50V, T <sub>A</sub> =150°C		50	μΑ
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V		10	μΑ
B\/ono	Ια=10μΛ	60		\/

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =50V		50	nA
I <sub>CBO</sub>	V <sub>CB</sub> =50V, T <sub>A</sub> =150°C		50	μΑ
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V		10	μΑ
<b>BVCBO</b>	I <sub>C</sub> =10μA	60		V
<b>BV</b> CEO	I <sub>C</sub> =10mA	60		V
BVEBO	I <sub>E</sub> =10μA	5.0		V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.15	V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		0.5	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =1.0A, I <sub>B</sub> =100mA		1.0	V
V <sub>BE</sub> (SAT)	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.9	V
V <sub>BE(ON)</sub>	V <sub>CE</sub> =1.0V, I <sub>C</sub> =1.0A		1.2	V
V <sub>BE(ON)</sub>	$V_{CE}$ =0.5V, $I_{C}$ =500mA		1.1	V
hFE	$V_{CE}$ =5.0V, $I_{C}$ =0.1mA	30		
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =100mA	40	120	
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =100mA, $T_{A}$ =-55°C	15		
hFE	$V_{CE}$ =5.0V, $I_{C}$ =500mA	25		
h <sub>FE</sub>	$V_{CE}$ =5.0V, $I_{C}$ =1.0A	15		
				D4 /0 I

R1 (2-June 2017)

## 2N4030

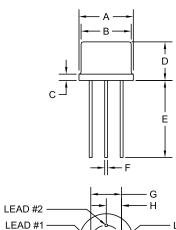




**ELECTRICAL CHARACTERISTICS - Continued:** (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
h <sub>fe</sub>	$V_{CE}$ =10V, $I_{C}$ =50mA, f=100MHz	1.0	4.0	
C <sub>ob</sub>	$V_{CB}$ =10V, $I_E$ =0, f=1.0MHz		20	pF
C <sub>ib</sub>	$V_{EB}$ =0.5V, $I_{C}$ =0, f=1.0MHz		110	pF
ton	$V_{CC}$ =30V, $I_{C}$ =500mA, $I_{B1}$ =50mA		100	ns
t <sub>s</sub>	$V_{CC}$ =30V, $I_{C}$ =500mA, $I_{B1}$ = $I_{B2}$ =50mA		350	ns
$t_f$	$V_{CC}$ =30V, $I_{C}$ =500mA, $I_{B1}$ = $I_{B2}$ =50mA		50	ns

# **TO-39 CASE - MECHANICAL OUTLINE**



LEAD #2	G H LEAD #3
<b>//</b>	R1

DIMENSIONS				
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
A (DIA)	0.335	0.370	8.51	9.40
B (DIA)	0.315	0.335	8.00	8.51
С	-	0.040	_	1.02
D	0.240	0.260	6.10	6.60
Е	0.500	-	12.70	-
F (DIA)	0.016	0.021	0.41	0.53
G (DIA)	0.200		5.08	
Н	0.100		2.54	
	0.028	0.034	0.71	0.86
J	0.029	0.045	0.74	1.14

TO-39 (REV: R1)

# LEAD CODE:

- 1) Emitter 2) Base
- 3) Collector

MARKING: FULL PART NUMBER



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#### **PRODUCT SUPPORT**

Central's operations team provides the highest level of support to insure product is delivered on-time.

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- · Consolidated shipping options

- Custom bar coding for shipments
- · Custom product packing

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- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- · PbSn plating options
- · Package details
- · Application notes
- · Application and design sample kits
- Custom product and package development

#### REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

# **CONTACT US**

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