

CMHSH5-2L

**SURFACE MOUNT SILICON
LOW FORWARD VOLTAGE
SCHOTTKY RECTIFIER
500mA, 20 VOLT**



www.centrasemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMHSH5-2L type is a silicon Schottky rectifier, epoxy molded in a surface mount package, designed for high current applications requiring a low forward voltage drop.

**LOW
V_F**



SOD-123 CASE

MARKING CODE: C2L

MAXIMUM RATINGS: (T_A=25°C)

Peak Repetitive Reverse Voltage	V _{RRM}	20	V
Peak Working Reverse Voltage	V _{RWM}	20	V
DC Blocking Voltage	V _R	20	V
Average Rectified Current	I _O	500	mA
Peak Forward Surge Current	I _{FSM}	5.5	A
(@ rated load, halfwave, single phase, 60Hz)			
Power Dissipation	P _D	400	mW
Operating Junction Temperature	T _J	-65 to +125	°C
Storage Temperature	T _{stg}	-65 to +150	°C
Thermal Resistance	θ _{JL}	150	°C/W
Thermal Resistance	θ _{JA}	250	°C/W

SYMBOL

V _{RRM}	20	V
V _{RWM}	20	V
V _R	20	V
I _O	500	mA
I _{FSM}	5.5	A
P _D	400	mW
T _J	-65 to +125	°C
T _{stg}	-65 to +150	°C
θ _{JL}	150	°C/W
θ _{JA}	250	°C/W

UNITS**ELECTRICAL CHARACTERISTICS:** (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I _R	V _R =10V		43	75	μA
I _R	V _R =10V, T _A =100°C		4.77	5.0	mA
I _R	V _R =20V		70.7	250	μA
I _R	V _R =20V, T _A =100°C		7.16	8.0	mA
BV _R	I _R =250μA	20			V
V _F	I _F =100mA		250	300	mV
V _F	I _F =100mA, T _A =100°C		158	220	mV
V _F	I _F =500mA		355	385	mV
V _F	I _F =500mA, T _A =100°C		301	330	mV
C _J	V _R =0, f=1.0MHz		94		pF
C _J	V _R =4.0V, f=1.0MHz		31		pF

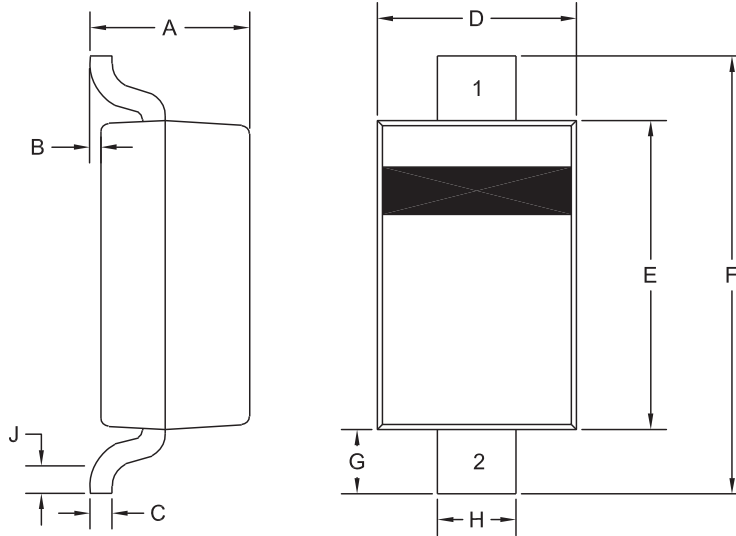
R7 (13-March 2018)

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SOD-123 CASE - MECHANICAL OUTLINE



R5

LEAD CODE

- 1) Cathode
- 2) Anode

MARKING CODE: C2L

DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.037	0.053	0.95	1.35
B	0.000	0.005	0.00	0.12
C	-	0.008	-	0.20
D	0.055	0.071	1.40	1.80
E	0.098	0.110	2.50	2.80
F	0.142	0.154	3.60	3.90
G	0.016	-	0.40	-
H	0.020	0.028	0.50	0.70
J	0.010	-	0.25	-

SOD-123 (REV:R5)

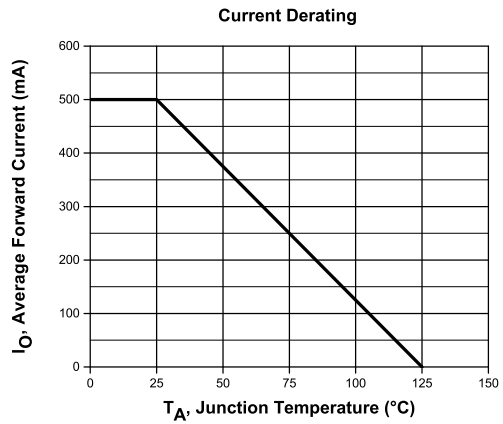
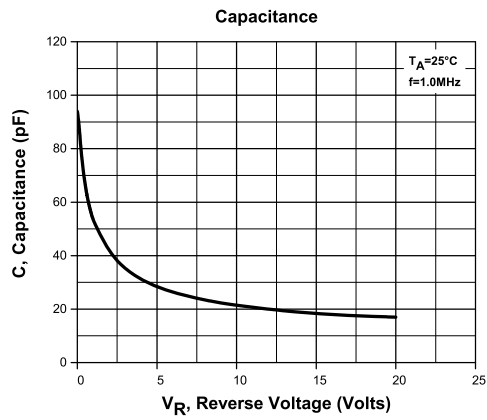
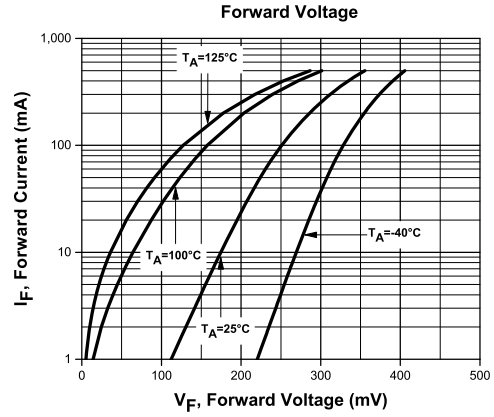
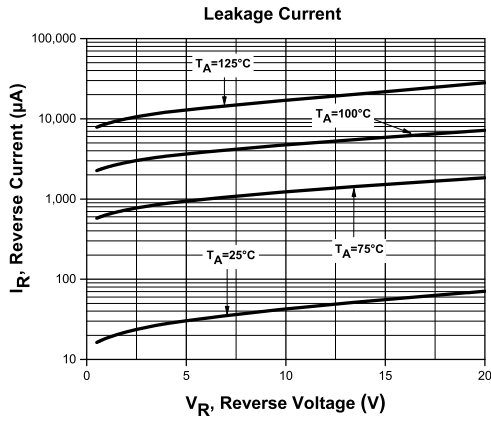
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TYPICAL ELECTRICAL CHARACTERISTICS



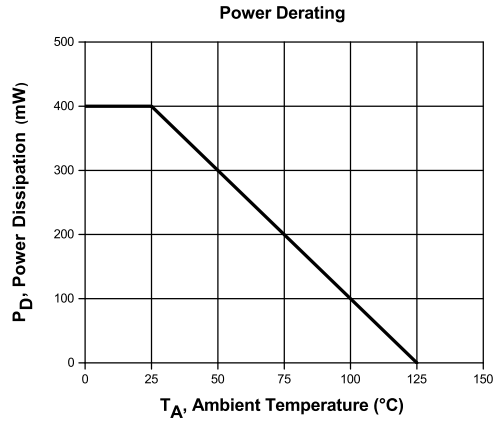
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TYPICAL ELECTRICAL CHARACTERISTICS



R7 (13-March 2018)



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Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
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- Customer specific screening
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- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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