CMLT8099

SURFACE MOUNT DUAL NPN SMALL SIGNAL SILICON TRANSISTORS



APPLICATIONS:

· Small signal general purpose amplifiers



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT8099 consists of two individual, isolated 8099 NPN silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. This PICOmini™ device has been designed for small signal general purpose amplifier applications.

MARKING CODE: C89

FEATURES:

- Device is Halogen Free by design
- Current I_C=500mA
- Voltage V_{CEO}=80V

MAXIMUM RATINGS: (TA=25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	80	V
Collector-Emitter Voltage	V_{CEO}	80	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Continous Collector Current	$I_{\mathbb{C}}$	500	mA
Power Dissipation	P_{D}	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted)

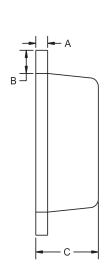
SYMBOL	TEST CONDITIONS	MÎN	MAX	UNITS
I _{CBO}	V _{CB} =80V		0.1	μΑ
I _{EBO}	V _{BE} =6.0V		0.1	μΑ
BV_{CBO}	I _C =100μA	80		V
BV _{CEO}	I _C =10mA	80		V
BV_{EBO}	I _E =10μA	6.0		V
V _{CE(SAT)}	I _C =100mA, I _B =5.0mA		0.4	V
V _{CE} (SAT)	I _C =100mA, I _B =10mA		0.3	V
V _{BE(ON)}	V _{CE} =5.0V, I _C =10mA	0.6	8.0	V
h _{FE}	V _{CE} =5.0V, I _C =1.0mA	100	300	
h _{FE}	V _{CE} =5.0V, I _C =10mA	100		
h _{FE}	V _{CE} =5.0V, I _C =100mA	75		
f_{T}	V _{CE} =5.0V, I _C =10mA, f=100MHz	150		MHz
C_{ob}	V _{CB} =10V, I _E =0, f=1.0MHz		6.0	pF
C _{ib}	V_{BE} =0.5V, I_{C} =0, f=1.0MHz		25	pF

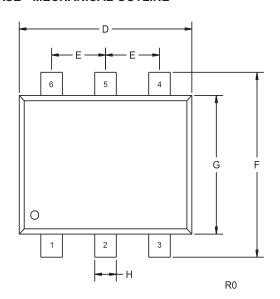
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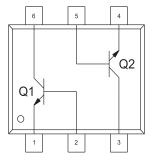


SOT-563 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.004	0.007	0.10	0.18			
В	0.008		0.20				
С	0.022	0.024	0.56	0.60			
D	0.059	0.067	1.50	1.70			
E	0.020		0.50				
F	0.061	0.067	1.55	1.70			
G	0.047		1.20				
Н	0.006	0.012	0.15	0.30			

SOT-563 (REV: R0)

LEAD CODE: 1) Emitter Q1

- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

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R1 (20-January 2010)

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