

CMPDM7002AHC
SURFACE MOUNT
N-CHANNEL
ENHANCEMENT-MODE
SILICON MOSFET



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SOT-23 CASE

- Device is *Halogen Free* by design

APPLICATIONS:

- Load/Power Switches
- Power Supply Converter Circuits
- Battery Powered Portable Equipment

MAXIMUM RATINGS: ($T_A=25^\circ\text{C}$)

Drain-Source Voltage	V_{DS}	60	V
Drain-Gate Voltage	V_{DG}	60	V
Gate-Source Voltage	V_{GS}	20	V
Continuous Drain Current (Steady State)	I_D	1.0	A
Maximum Pulsed Drain Current ($t_p=10\mu\text{s}$)	I_{DM}	5.0	A
Power Dissipation	P_D	350	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$
Thermal Resistance	Θ_{JA}	357	$^\circ\text{C}/\text{W}$

FEATURES:

- ESD Protection up to 2kV
- 350mW Power Dissipation
- Low $r_{DS(\text{ON})}$: 0.22 Ω MAX @ $V_{GS}=10\text{V}$
- Industry Standard SOT-23 Surface Mount Package

SYMBOL		UNITS
V_{DS}	60	V
V_{DG}	60	V
V_{GS}	20	V
I_D	1.0	A
I_{DM}	5.0	A
P_D	350	mW
T_J, T_{stg}	-65 to +150	$^\circ\text{C}$
Θ_{JA}	357	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ\text{C}$ unless otherwise noted)

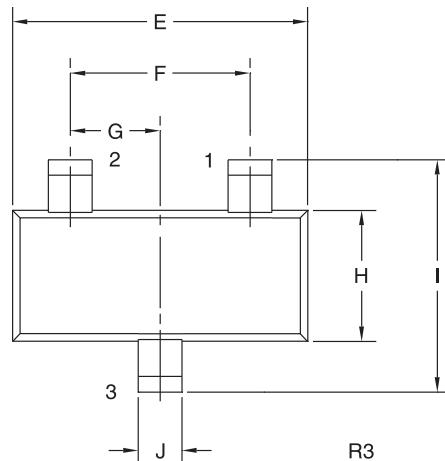
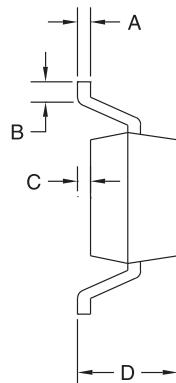
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I_{GSSF}, I_{GSSR}	$V_{GS}=20\text{V}, V_{DS}=0$			10	μA
I_{DSS}	$V_{DS}=60\text{V}, V_{GS}=0$			500	nA
BV_{DSS}	$V_{GS}=0, I_D=100\mu\text{A}$	63			V
$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.2		2.3	V
V_{SD}	$V_{GS}=0, I_S=500\text{mA}$			0.9	V
$r_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=500\text{mA}$		0.15	0.22	Ω
$r_{DS(\text{ON})}$	$V_{GS}=5.0\text{V}, I_D=500\text{mA}$		0.20	0.30	Ω
$Q_g(\text{tot})$	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=1.0\text{A}$		2.3		nC
Q_{gs}	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=1.0\text{A}$		1.0		nC
Q_{gd}	$V_{DS}=10\text{V}, V_{GS}=4.5\text{V}, I_D=1.0\text{A}$		0.7		nC
C_{rss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$			25	pF
C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$			240	pF
C_{oss}	$V_{DS}=25\text{V}, V_{GS}=0, f=1.0\text{MHz}$			50	pF
t_{on}	$V_{DD}=30\text{V}, V_{GS}=4.5\text{V}, I_D=1.0\text{A}$ $R_G=6.0\Omega, R_L=30\Omega$			35	ns
t_{off}	$V_{DD}=30\text{V}, V_{GS}=4.5\text{V}, I_D=1.0\text{A}$ $R_G=6.0\Omega, R_L=30\Omega$			50	ns

R2 (2-August 2011)

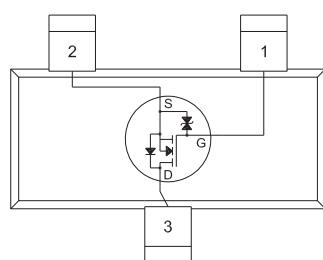
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SOT-23 CASE - MECHANICAL OUTLINE



PIN CONFIGURATION



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.003	0.007	0.08	0.18
B	0.006	-	0.15	-
C	-	0.005	-	0.13
D	0.035	0.043	0.89	1.09
E	0.110	0.120	2.80	3.05
F	0.075		1.90	
G	0.037		0.95	
H	0.047	0.055	1.19	1.40
I	0.083	0.098	2.10	2.49
J	0.014	0.020	0.35	0.50

SOT-23 (REV: R3)

LEAD CODE:

- 1) Gate
- 2) Source
- 3) Drain

MARKING CODE: 702H

R2 (2-August 2011)

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