

#### SURFACE MOUNT PNP SILICON TRANSISTOR



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## DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPT4033 type is a PNP silicon transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for very high current, general purpose amplifier applications.

### MARKING CODE: C4A

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V <sub>CBO</sub>	80	V
Collector-Emitter Voltage	V <sub>CEO</sub>	80	V
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	V
Continuous Collector Current	۱ <sub>C</sub>	500	mA
Peak Collector Current	ICM	1.0	А
Power Dissipation	PD	350	mW
Operating and Storage Junction Temperature	TJ, T <sub>stg</sub>	-65 to +150	°C
Thermal Resistance	$\Theta_{JA}$	357	°C/W

ELECTRICAL CHARACTERISTICS: (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V <sub>CB</sub> =60V		50	nA
IEBO	V <sub>EB</sub> =5.0V		10	nA
BVCBO	Ι <sub>C</sub> =10μΑ	80		V
BVCEO	I <sub>C</sub> =10mA	80		V
BVEBO	I <sub>E</sub> =10µA	5.0		V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.15	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		0.50	V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =150mA, I <sub>B</sub> =15mA		0.90	V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		1.10	V
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =0.1mA	75		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =100mA	100	300	
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =500mA	70		
fT	V <sub>CE</sub> =10V, I <sub>C</sub> =50mA, f=1.0MHz	100		MHz
Cob	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1.0MHz		20	pF
C <sub>ib</sub>	V <sub>EB</sub> =0.5V, I <sub>C</sub> =0, f=1.0MHz		110	pF



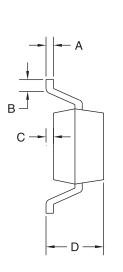
R5 (1-February 2010)

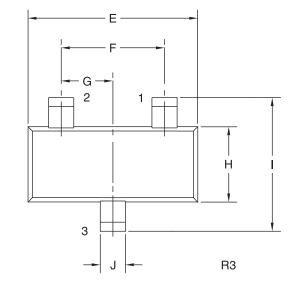


#### CMPT4033

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#### SOT-23 CASE - MECHANICAL OUTLINE





LEAD CODE:
1) Base
2) Emitter

2) Emitter 3) Collector

MARKING CODE: C4A

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
А	0.003	0.007	0.08	0.18		
В	0.006	-	0.15	-		
С	-	0.005	-	0.13		
D	0.035	0.043	0.89	1.09		
E	0.110	0.120	2.80	3.05		
F	0.075		1.90			
G	0.037		0.95			
Н	0.047	0.055	1.19	1.40		
	0.083	0.098	2.10	2.49		
J	0.014	0.020	0.35	0.50		

SOT-23 (REV: R3)

R5 (1-February 2010)

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- Inventory bonding
- Consolidated shipping options

## DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

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Special wafer diffusions

· Custom product packing

- PbSn plating options
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- Application notes
- Application and design sample kits

· Custom bar coding for shipments

Custom product and package development

## REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

## CONTACT US

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