

CMPT5551

SURFACE MOUNT
NPN SILICON TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPT5551 type is an NPN silicon transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for high voltage amplifier applications.

MARKING CODE: 1FF



SOT-23 CASE

MAXIMUM RATINGS: ($T_A=25^\circ C$)

	SYMBOL	UNITS
Collector-Base Voltage	V_{CBO}	V
Collector-Emitter Voltage	V_{CEO}	V
Emitter-Base Voltage	V_{EBO}	V
Continuous Collector Current	I_C	mA
Power Dissipation	P_D	mW
Operating and Storage Junction Temperature	T_J, T_{stg}	${}^\circ C$
Thermal Resistance	Θ_{JA}	${}^\circ C/W$

ELECTRICAL CHARACTERISTICS: ($T_A=25^\circ C$ unless otherwise noted)

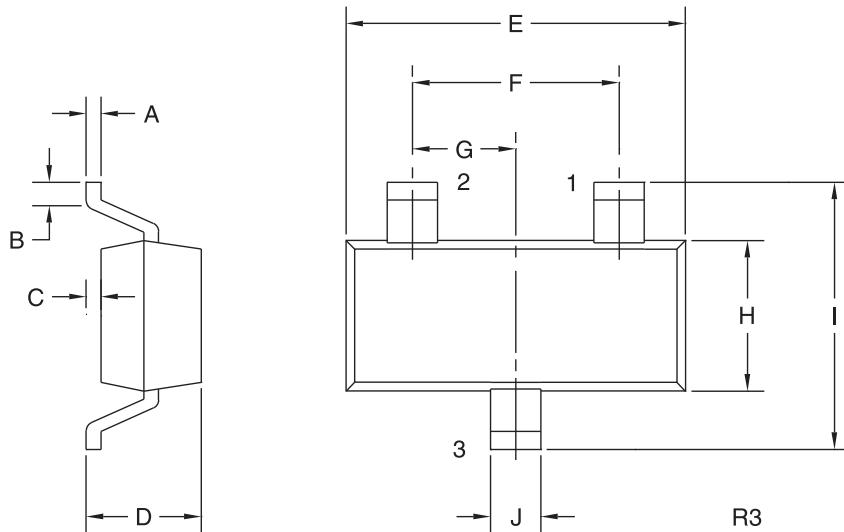
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{CBO}	$V_{CB}=120V$		50	nA
I_{CBO}	$V_{CB}=120V, T_A=100^\circ C$		50	μA
BV_{CBO}	$I_C=100\mu A$	180		V
BV_{CEO}	$I_C=1.0mA$	160		V
BV_{EBO}	$I_E=10\mu A$	6.0		V
$V_{CE(SAT)}$	$I_C=10mA, I_B=1.0mA$		0.15	V
$V_{CE(SAT)}$	$I_C=50mA, I_B=5.0mA$		0.20	V
$V_{BE(SAT)}$	$I_C=10mA, I_B=1.0mA$		1.00	V
$V_{BE(SAT)}$	$I_C=50mA, I_B=5.0mA$		1.00	V
h_{FE}	$V_{CE}=5.0V, I_C=1.0mA$	80		
h_{FE}	$V_{CE}=5.0V, I_C=10mA$	80	250	
h_{FE}	$V_{CE}=5.0V, I_C=50mA$	30		
f_T	$V_{CE}=10V, I_C=10mA, f=100MHz$	100	300	MHz
C_{ob}	$V_{CB}=10V, I_E=0, f=1.0MHz$		6.0	pF
h_{fe}	$V_{CE}=10V, I_C=1.0mA, f=1.0kHz$	50	200	
NF	$V_{CE}=5.0V, I_C=200\mu A, R_S=10\Omega, f=10Hz \text{ to } 15.7kHz$		8.0	dB

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SOT-23 CASE - MECHANICAL OUTLINE



LEAD CODE:

- 1) Base
- 2) Emitter
- 3) Collector

MARKING CODE: 1FF

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.003	0.007	0.08	0.18
B	0.006	-	0.15	-
C	-	0.005	-	0.13
D	0.035	0.043	0.89	1.09
E	0.110	0.120	2.80	3.05
F	0.075		1.90	
G	0.037		0.95	
H	0.047	0.055	1.19	1.40
I	0.083	0.098	2.10	2.49
J	0.014	0.020	0.35	0.50

SOT-23 (REV: R3)

R5 (1-February 2010)

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