

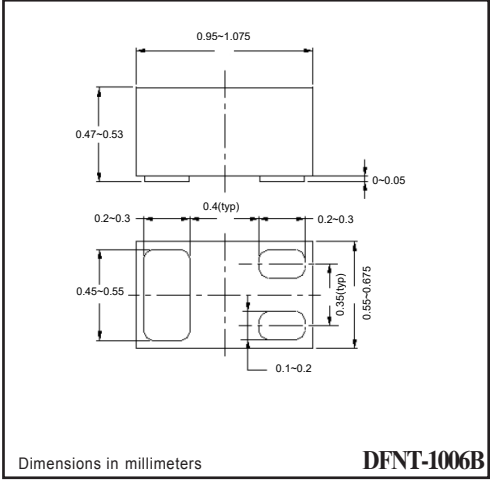
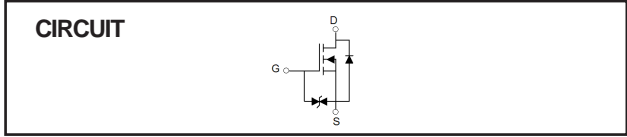
Halogens free devices

**SURFACE MOUNT**  
**N-Channel Enhancement Mode Field Effect Transistor**  
 VOLTAGE 20 Volts CURRENT 0.8 Ampere

CHMNB00HESG7GP-S

**FEATURE**

- \* Small surface mounting type. (DFNT-1006B)
- \* Extremely low threshold voltage.
- \* ESD Protected.



**MAXIMUM RATING @ Ta=25°C unless otherwise specified (Note 1)**

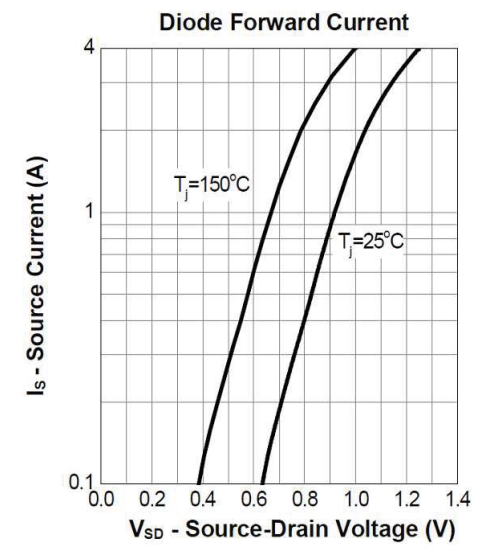
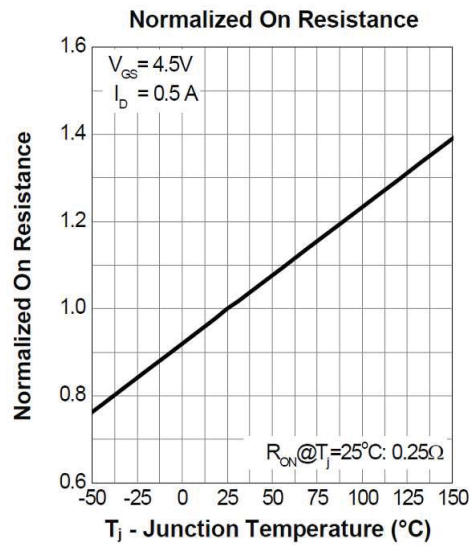
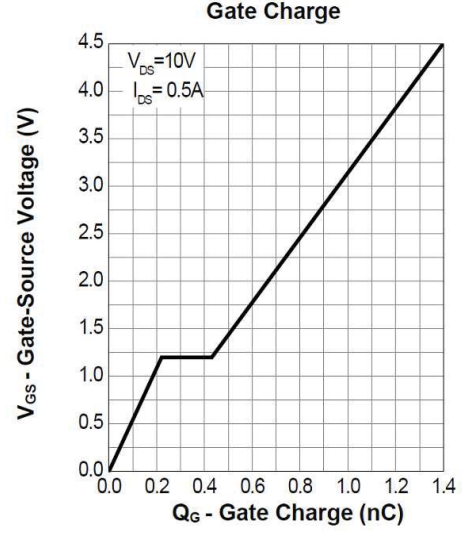
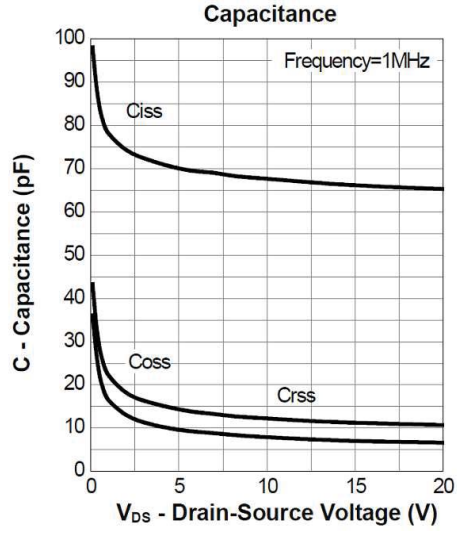
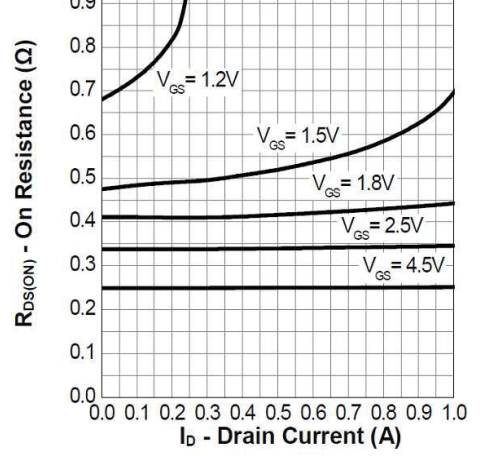
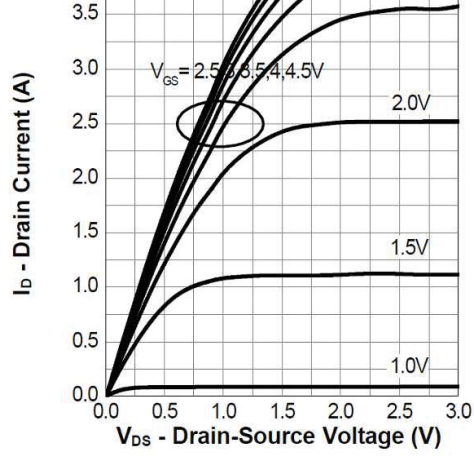
Symbol	Parameter	Value	Units
V <sub>DSS</sub>	Drain-Source voltage	20	V
V <sub>GSS</sub>	Gate -Source voltage	± 10	V
I <sub>D</sub>	Drain current (Continuous)	800	mA
I <sub>DM</sub>	Drain Current (Pulsed) <sup>(Note 1)</sup>	1000	mA
P <sub>D</sub>	Maximum Drain Current-Continue	450	mW
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	279	°C/W
T <sub>J</sub>	Operating Junction Temperature	+150	°C
T <sub>STG</sub>	Storage Temperature Range	-55 to +150	°C

ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	20	-	-	V
Gate-body Leakage	$I_{GSS}$	Forward $V_{DS}=0V, V_{GS}=8V$	-	-	10	$\mu A$
Reverse		$V_{DS}=0V, V_{GS}=-8V$	-	-	-10	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=16V, V_{GS}=0V$	-	-	1	$\mu A$
<b>ON CHARACTERISTICS</b> (Note 2)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.3	0.65	1.0	V
Static drain-Source on-resistance	$R_{DS(ON)}$	$V_{GS}=4.5V, I_D=0.5A$	-	280	400	m $\Omega$
		$V_{GS}=2.5V, I_D=0.2A$	-	350	650	
		$V_{GS}=1.8V, I_D=0.1A$	-	400	800	
		$V_{GS}=1.5V, I_D=0.05A$	-	500	1200	
		$V_{GS}=1.2V, I_D=0.02A$	-	700	3000	
<b>DYNAMIC CHARACTERISTICS</b>						
Total Gate Charge	Qg	$V_{DS} = 10V$ $I_D = 0.5A$ $V_{GS} = 4.5V,$	-	1.4	-	nC
Gate-Source Charge	Qgs		-	0.22	-	nC
Gate-Drain Charge	Qgd		-	0.21	-	nC
Input capacitance	$C_{ISS}$	$V_{DS}=10V, V_{GS}=0V, f=1.0MHz$	-	67	-	pF
Output capacitance	$C_{OSS}$		-	19	-	
Reverse transfer capacitance	$C_{RSS}$		-	6	-	
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD} = 10V,$ $I_D = 150mA$ $V_{GEN} = 4V$ $R_{GEN} = 10\Omega$	-	2.8	-	ns
Turn-On Rise Time	$t_r$		-	20	-	
Turn-Off Delay Time	$t_{D(OFF)}$		-	23	-	
Turn-Off Fall Time	$t_f$		-	23	-	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Drain-Source diode forward voltage	$V_{SD}$	$V_{GS}=0V, I_S=500mA$	-	0.87	1.3	V

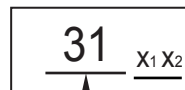
Notes:

1. Pulse width limited by maximum junction temperature.
2. Pulse test: Pulse width  $\leq 300\mu S$ , Duty cycle  $\leq 2\%$



# The Marking and Part No. for CHMNB00HESG7GP-S (DFNT-1006B)

Marking:



Marking Code

Date Code Standard

X1


X2

Date Code (會變更)

Line code (會變更)

Date Code	N	P	Q	R	S	T	U	V	W	X	Y	Z
2018Year	JAN.	FEB.	MAR.	APR	MAY	JUNE	JULY	AUG.	SEP.	OCT	NOV.	DEC.

Date Code	A	B	C	D	E	F	G	H	J	K	L	M
2019Year	JAN.	FEB.	MAR.	APR	MAY	JUNE	JULY	AUG.	SEP.	OCT	NOV.	DEC.

CUSTOMER :	APPROVED :	 <b>CHENMKO LTD.</b>
APPROVED :	CHECKED :	
CHECKED :	DRAWN :	

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