

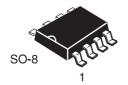
## **CEM9435A**

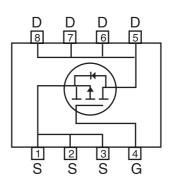
March 1998

### **P-Channel Enhancement Mode MOSFET**

#### **FEATURES**

- -30V , -5.3A ,  $R_{DS(ON)}$ =50m $\Omega$  @Vgs=-10V. RDS(ON)=90m $\Omega$  @Vgs=-4.5V.
- Super high dense cell design for extremely low RDS(ON).
- High power and current handing capability.
- Surface Mount Package.





#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	VGS	±20	V
Drain Current-Continuous <sup>®</sup> @TJ=125℃ -Pulsed <sup>b</sup>	D	±5.3	А
	ldм	±20	A
Drain-Source Diode Forward Current <sup>a</sup>	ls	-1.9	A
Maximum Power Dissipation <sup>a</sup>	PD	2.5	W
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 to 150	°C

#### **THERMAL CHARACTERISTICS**

Thermal Resistance, Junction-to-Ambient <sup>a</sup>	R∂JA	50	°C/W
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# **CEM9435A**

### ELECTRICAL CHARACTERISTICS (TA= $25^{\circ}$ C unless otherwise noted)

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Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit
OFF CHARACTERISTICS			1	1	I	
Drain-Source Breakdown Voltage	BVdss	Vgs=0V, ID=-250µA	-30			V
Zero Gate Voltage Drain Current	DSS	Vds=-24V, Vgs=0V			-1	μA
Gate-Body Leakage	lgss	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
ON CHARACTERISTICS <sup>b</sup>						
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID = -250μA -1			-3	V
Drain-Source On-State Resistance		Vgs=-10V, Id =-5.3A		40	50	mΩ
	Rds(on)	$V_{GS} = -4.5V, I_{D} = -4.2A$		67	90	mΩ
On-State Drain Current	D(ON)	Vds = -5V, Vgs = -10V	-20			A
Forward Transconductance	9 <sub>FS</sub>	Vds = -15V, Id = - 5.3A	4	8.3		S
DYNAMIC CHARACTERISTICS <sup>©</sup>						
Input Capacitance	Ciss			860	1120	РF
Output Capacitance	Coss	VDS =-15V, VGS = 0V f =1.0MHz		458	600	РF
Reverse Transfer Capacitance	Crss			140	190	РF
SWITCHING CHARACTERISTICS	S°					
Turn-On Delay Time	tD(ON)	Vd = -15V,		9	30	ns
Rise Time	tr	Id = -1A, Vgen = - 10V,		16	60	ns
Turn-Off Delay Time	tD(OFF)	$R_{\text{GEN}} = 6\Omega$		75	120	ns
Fall Time	tr			40	100	ns
Total Gate Charge	Qg			29	40	nC
Gate-Source Charge	Qgs	VDS =-15V, ID = -5.3A, VGS =-10V		3		nC
Gate-Drain Charge	Qgd			9		nC

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### **ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ <sup>c</sup>	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS <sup>b</sup>						5	
Diode Forward Voltage	Vsd	Vgs = 0V, Is =-5.3A		-0.84	-1.3	۷	

0.18

0.15

0.12

0.09

0.06

0.03

0.00

0

Vgs=-10V

2.5

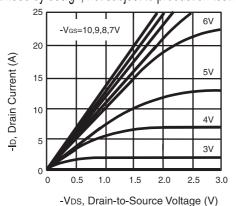
RDS (ON), On-Resistance (Ohms)

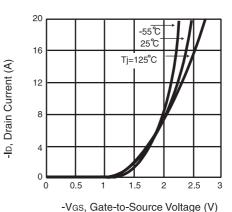
Notes

a.Surface Mounted on FR4 Board, t  $\leq$ 10sec.

b.Pulse Test:Pulse Width  $\leq$ 300  $\mu$  s, Duty Cycle  $\leq$  2%.

c.Guaranteed by design, not subject to production testing.





**Figure 1. Output Characteristics** 

Figure 2. Transfer Characteristics

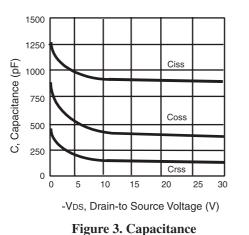
Tj=125°C

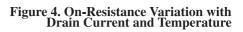
25°C

-55°C

7.5

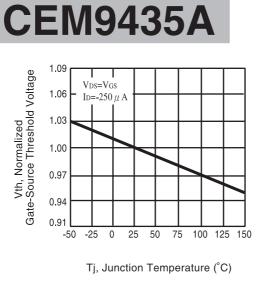
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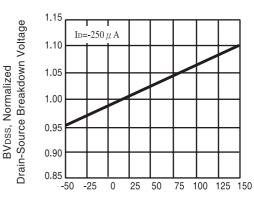




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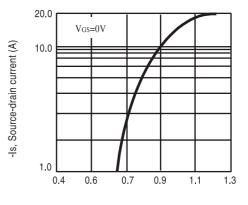
-ID, Drain Current(A)





Tj, Junction Temperature (°C)

Figure 6. Breakdown Voltage Variation with Temperature



-Vsp, Body Diode Forward Voltage (V)

Figure 8. Body Diode Forward Voltage Variation with Source Current

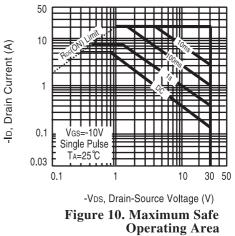
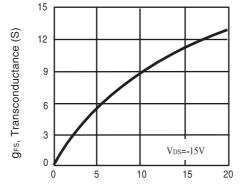
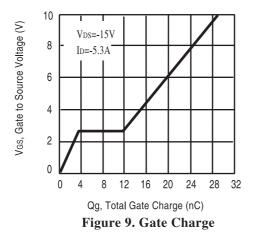


Figure 5. Gate Threshold Variation with Temperature



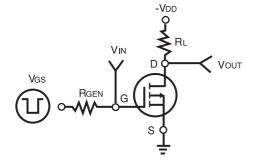
-IDS, Drain-Source Current (A) **Figure 7. Transconductance Variation** with Drain Current



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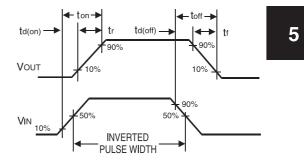


Figure 11. Switching Test Circuit

Figure 12. Switching Waveforms

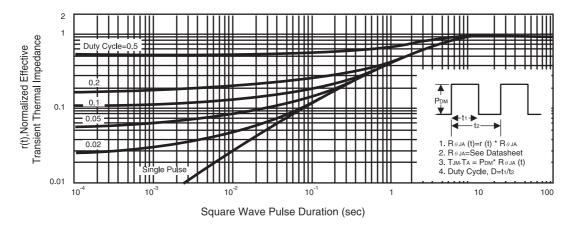


Figure 13. Normalized Thermal Transient Impedance Curve

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