

CHIPLINK N-Channel Enhancement Mode Power MOSFET

Description

The LX3400L combines advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltage as low as 2.5V. This device is suitable for use as a load switch or PWM applications.

Features

V_{DS}=30V, I_D=5.1A

 $R_{DS(ON)}$ <33m Ω @VDS=10V

 $R_{DS(ON)}$ <39m Ω @VDS=4.5V

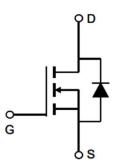
 $R_{DS(ON)}$ <55mQ@VDS=2.5V

- Low gate charge
- High power and current handing capability
- Termination is Lead-free and RoHS Compliant

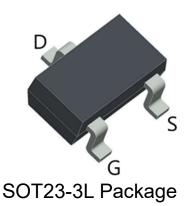


Applications

- PWM applications
- Load switch
- Power Management



schematic diagram



Maximum Ratings(T_A =25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±12	V
Continuous Drain Current	I _D	5.1	Α
Pulsed Drain Current ^B	I _{DM}	20	Α
Maximum Power Dissipation ^A	P _D	1.3	W
Junction and Storage Temperature Range	T _J , T _{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction to Ambient	R _{QJA}	96	°C/W
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Electrical Characteristics (T_A=25 °C unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30			V
Gate-Threshold Voltage	$V_{th(GS)}$	V_{DS} = V_{GS} , I_{D} =250 uA	0.7	0.9	1.2	V
Gate-body Leakage	IGSS	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V			1	uA
	R _{DS(ON)}	V _{GS} =10V, I _D =5A		24	33	mΩ
Drain-Source On-Resistance		V _{GS} =4.5V, I _D =4A		26	39	mΩ
		V_{GS} =2.5V, I_{D} =3A		33	55	mΩ
Forward Transconductance	g FS	V_{DS} =5 V , I_D =5 A	10			S
Dynamic Characteristics						
Input Capacitance	C _{iss}	\/ - 45\/ \/ -0\/		595		
Output Capacitance	Coss	V_{DS} = 15V, V_{GS} =0V, F=1MHz		39		pF
Reverse Transfer Capacitance	C _{rss}	1 - 11011 12		36		
Switching Capacitance						
Turn-on Delay Time	t _{d(on)}			3.0		nS
Turn-on Rise Time	t _r	V_{DD} = 15V, R_L =3 Ω		4.5		nS
Turn-off Delay Time	t _{d(off)}	V_{GS} = 10V, R_{GEN} =3 Ω		25		nS
Turn-off Fall Time	t _f			3.8		nS
Total Gate Charge	Qg	$V_{DS} = 15V, I_{D} = 5A,$		9.3		nC
Gate-Source Charge	Q _{gs}	V _{GS} =4.5V		1.6		nC
Gate-Drain Charge	Q_{gd}			2.1		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _D =5A			1.2	V
Diode Forward Current	ls				5.1	Α

Notes:

- A. The Power dissipation P_D is based on $T_{J(MAX)}$ =150 $^{\circ}\mathbb{C}$, using≤10s junction-to ambient thermal resistance.
- B. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C.Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- C. The Static characteristics in Figures are obtained using \leq 300 μ s pulses, duty cycle 2% max.



Typical Electrical and Thermal Characteristics

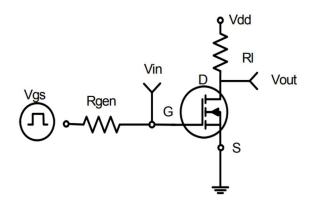


Figure 1:Switching Test Circuit

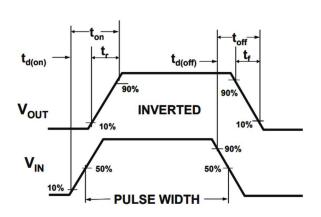


Figure 2:Switching Waveforms

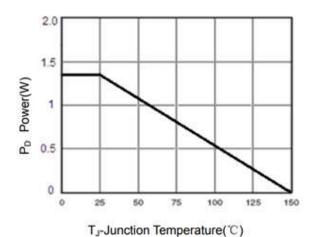


Figure 3 Power Dissipation

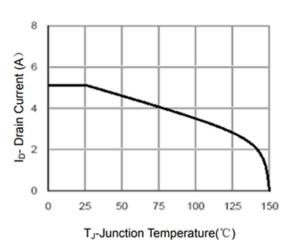
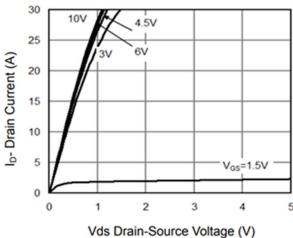
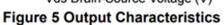


Figure 4 Drain Current





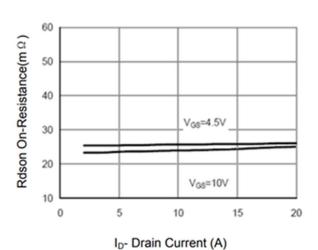
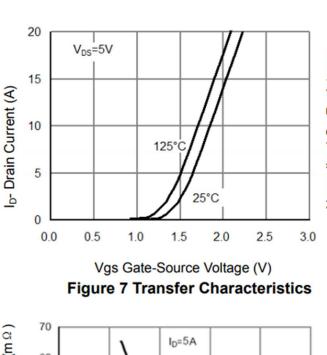
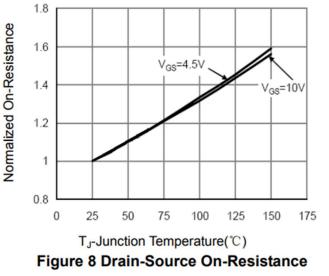
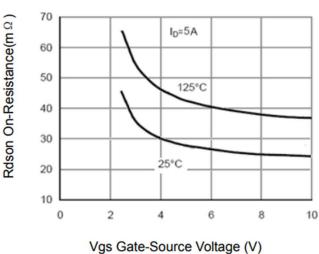


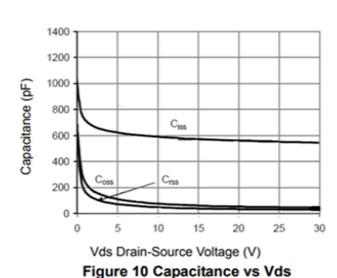
Figure 6 Drain-Source On-Resistance











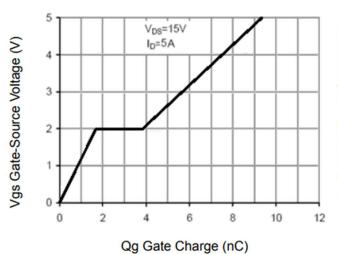
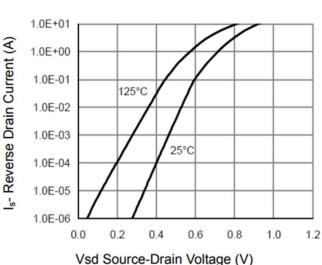


Figure 11 Gate Charge

Figure 9 Rdson vs Vgs



Vsd Source-Drain Voltage (V)
Figure 12 Source- Drain Diode Forward



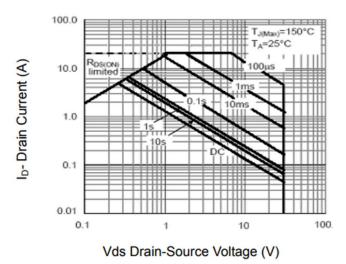


Figure 13 Safe Operation Area

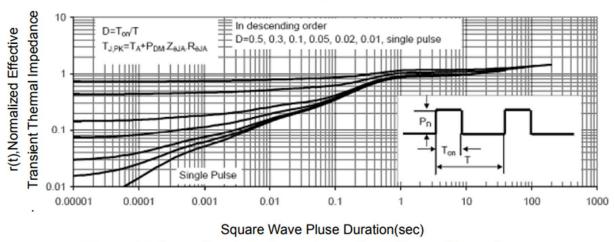
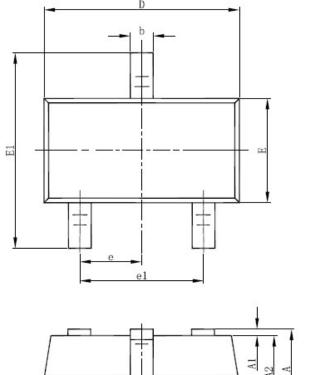
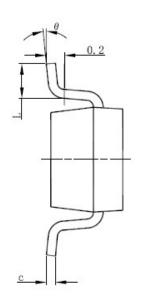


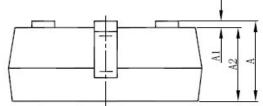
Figure 14 Normalized Maximum Transient Thermal Impedance



SOT23-3L Package Information







Cl 1	Dimensions In	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
Α	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°



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