



JIANGSU CHANGJING ELECTRONICS TECHNOLOGY CO., LTD

TO-220BK Plastic-Encapsulate Thyristors

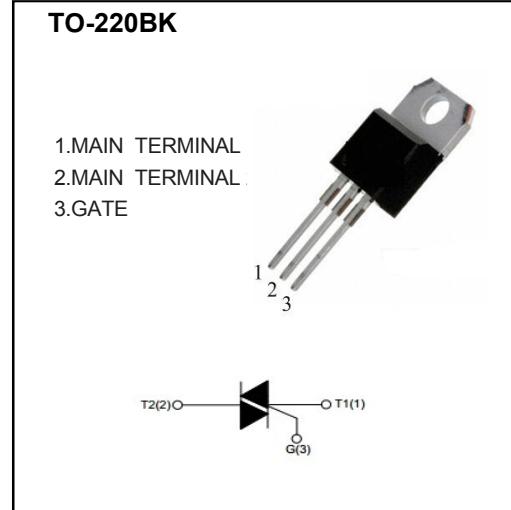
CT408B 4Q TRIACs

MAIN CHARACTERISTICS

$I_{T(RMS)}$		8A
V_{DRM}/V_{RRM}	CT408B-600S/C	600V
	CT408B-800S/C	800V
V_{TM}		1.55V

FEATURES

- NPNPN 5-layer Structure TRIACs
- Mesa Glass Passivated Technology
- Multi Layers Metal Electrodes
- High Junction Temperature
- Good Commutation Performance



APPLICATIONS

- Heater Control
- Motor Speed Controller
- Mixer

MARKING

CT408B:Series Code
600S:Depends on V_{DRM} and I_{GT}

XXX:Internal Code

ABSOLUTE RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test condition		Value		Unit	
V_{DRM}/ V_{RRM}	Repetitive peak off-state voltage	$T_j=25^\circ\text{C}$	CT408B-600S/C	600		V	
			CT408B-800S/C	800		V	
$I_{T(RMS)}$	RMS on-state current	TO-220BK($T_c \leq 110^\circ\text{C}$), Fig. 1,2		8		A	
I_{TSM}	Non repetitive surge peak on-state current	Full sine wave , $T_j(\text{init})=25^\circ\text{C}$, $tp=20\text{ms}$; Fig. 3,5		80		A	
I^2t	I^2t value	$tp=10\text{ms}$		36		A^2s	
dI_T/dt	Critical rate of rise of on-state current	$I_G=2*I_{GT}$, $tr \leq 10\text{ns}$, $F=120\text{Hz}$, $T_j=125^\circ\text{C}$	$I - II - III$	50	$\text{A}/\mu\text{s}$		
			IV	10			
I_{GM}	Peak gate current	$tp=20\mu\text{s}$, $T_j=125^\circ\text{C}$		2		A	
$P_{G(AV)}$	Average gate power	$T_j=125^\circ\text{C}$		0.5		W	
T_{STG}	Storage temperature			$-40 \sim +150$		$^\circ\text{C}$	
T_j	Operating junction temperature			$-40 \sim +125$			

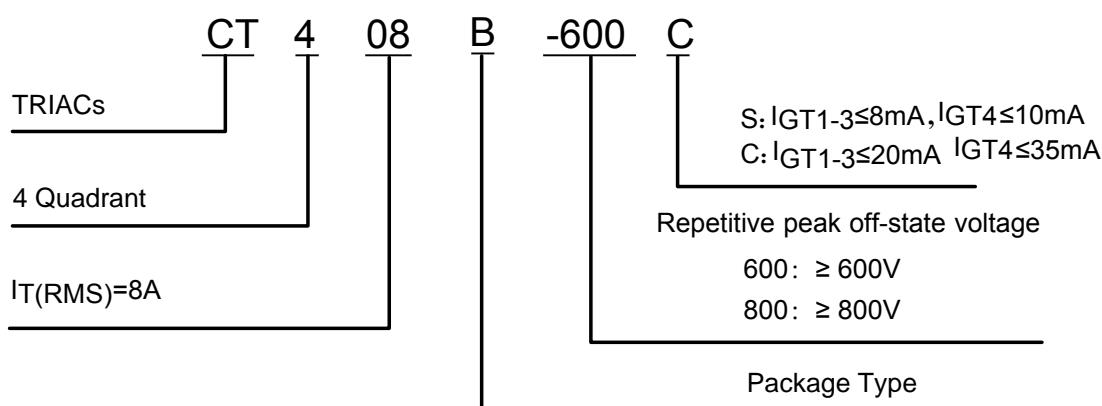
ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test condition	Value		Unit
			S	C	
I_{GT}	Gate trigger current	$V_D=12\text{V}$, $I_T = 1\text{A}$, $T_j=25^\circ\text{C}$, Fig. 6	≤ 8	≤ 20	mA
			≤ 10	≤ 35	
V_{GT}	Gate trigger voltage	I - II - III - IV	≤ 1.3		V
V_{GD}	Non-triggering gate voltage	$V_D=V_{DRM}$, $T_j=125^\circ\text{C}$	≥ 0.2		V
I_H	Holding current	$V_D=12\text{V}$, $I_{GT}=0.1\text{A}$, $T_j=25^\circ\text{C}$, Fig. 6	≤ 10	≤ 20	mA
I_L	Latching current		≤ 15	≤ 25	mA
			≤ 20	≤ 35	mA
dV_D/dt	Critical rate of rise of off-state	$V_D=67\%V_{DRM}$, Gate Open $T_j=125^\circ\text{C}$	≥ 10	≥ 20	V/ μs
V_{TM}	On-state Voltage	$I_{TM}=10\text{A}$, $tp=380\mu\text{s}$, Fig. 4	≤ 1.55		V
I_{DRM} / I_{RRM}	Repetitive peak off-state current	$V_D=V_{DRM}/V_{RRM}$, $T_j=25^\circ\text{C}$	≤ 5	≤ 5	μA
		$V_D=V_{DRM}/V_{RRM}$, $T_j=125^\circ\text{C}$	≤ 1	≤ 1	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th} (j-c)$	Junction to case (AC)	1.6	$^\circ\text{C/W}$
$R_{th} (j-a)$	Junction to ambient	60	$^\circ\text{C/W}$

PART NUMBER



CHARACTERISTICS CURVES

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

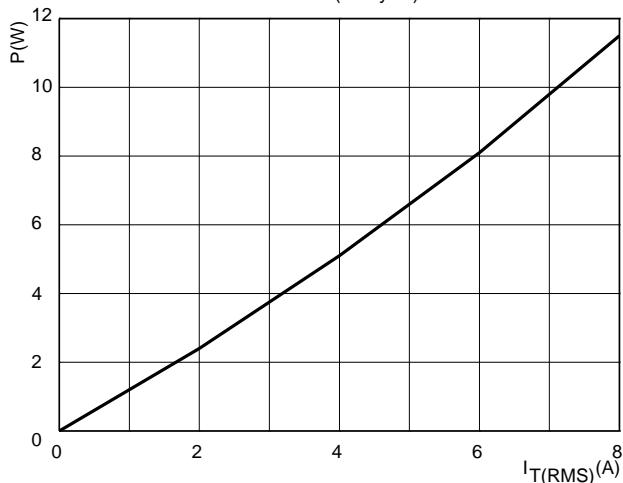


FIG.2: RMS on-state current versus case temperature (full cycle)

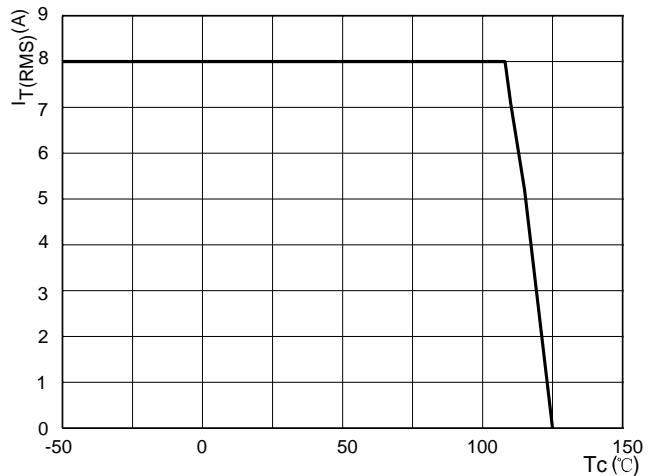


FIG.3: Surge peak on-state current versus number of cycles

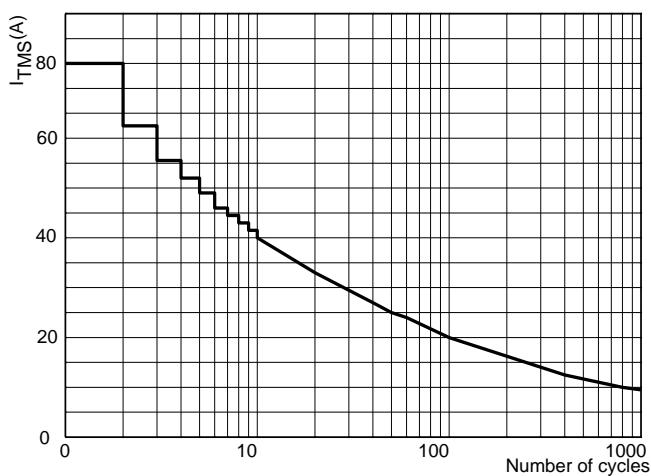


FIG.4: On-state characteristics (maximum values)

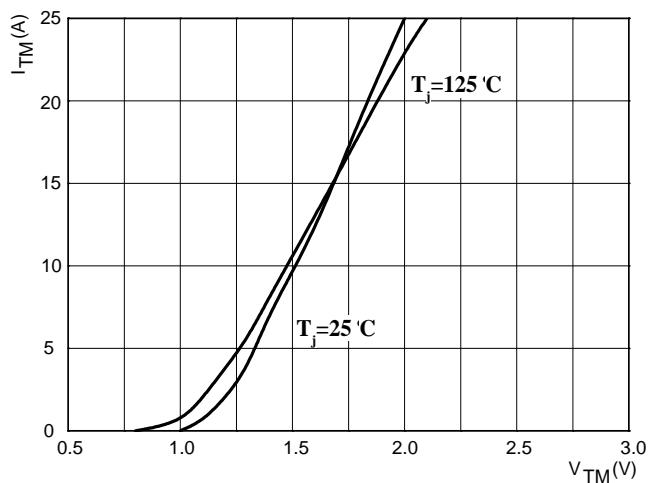


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$

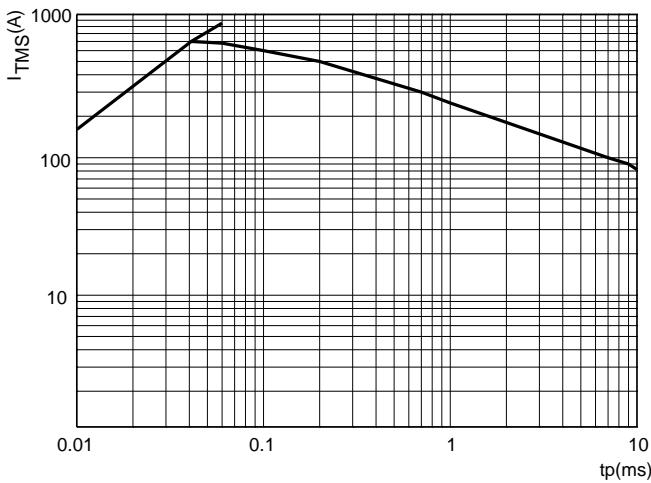
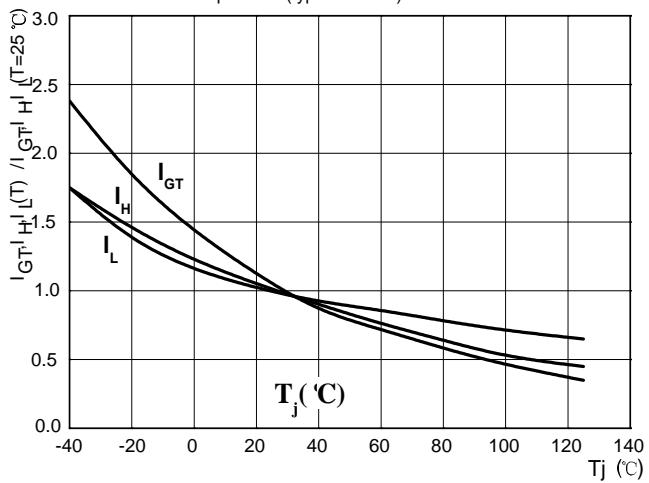
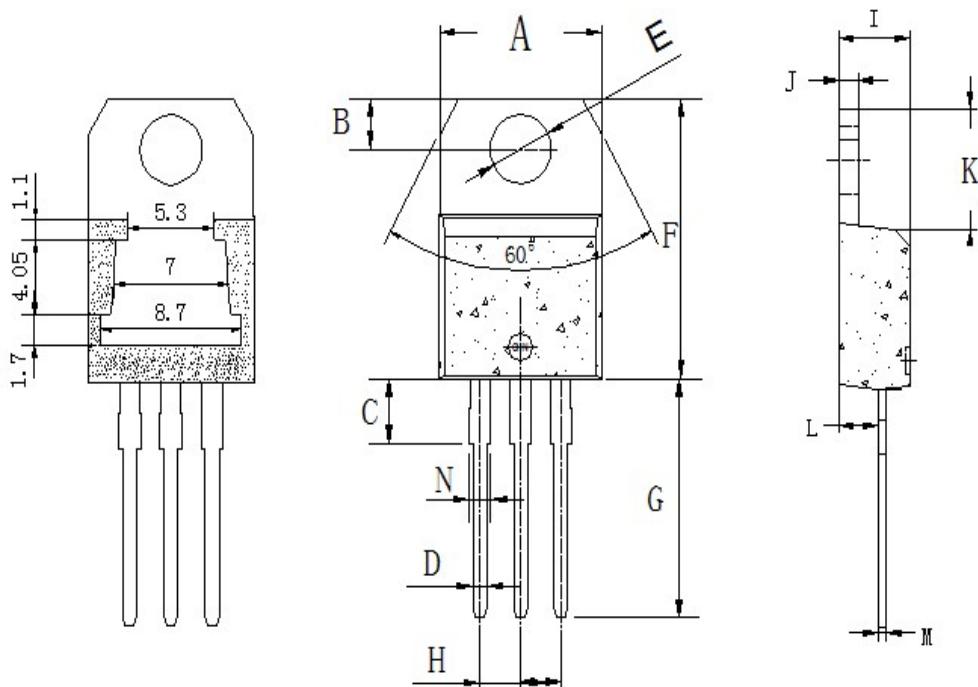


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



TO-220BK PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.8	10.4	0.385	0.409
B	2.65	3.1	0.104	0.122
C	2.8	4.2	0.110	0.165
D	0.7	0.92	0.027	0.036
E	3.75	3.95	0.147	0.155
F	14.8	16.1	0.582	0.633
G	13.05	13.6	0.513	0.535
H	2.4	2.7	0.094	0.106
I	4.38	4.61	0.172	0.181
J	1.15	1.36	0.045	0.053
K	5.85	6.82	0.230	0.268
L	2.35	2.75	0.092	0.108
M	0.35	0.65	0.013	0.025
N	1.18	1.42	0.046	0.055

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