



CPC1230N 350V Normally-Open Single-Pole 4-Pin SOP OptoMOS[®] Relay

Parameter	Rating	Units
Blocking Voltage	350	V _P
Load Current	120	mA _{rms} / mA _{DC}
On-Resistance (max)	30	Ω

Features

- 0.4mm Distance Through Insulation (Supplementary Isolation Requirement of EN60950)
- 1500V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- · High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 4-Pin SOP Package
- Wave Solderable
- Tape & Reel Version Available

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - · Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - · Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment
- Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

The CPC1230N is a miniature normally-open (1-Form-A), single-pole solid state relay in a 4-pin SOP package that employs optically coupled MOSFET technology to provide 1500V_{rms} of input to output isolation. The optically coupled outputs, that use patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

The device is compliant with supplementary isolation in accordance with EN/IEC 60950-1.

The CPC1230N uses IXYS Integrated Circuits Division's state of the art double molded vertical construction packaging to produce one of the world's smallest 4-pin relays. It offers board space savings approaching 20% over the competitor's larger 4-pin SOP relay.

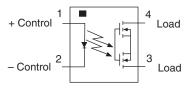
Approvals

- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1172007
- EN/IEC 60950-1 Certified Component: Certificate B 13 12 82667 003

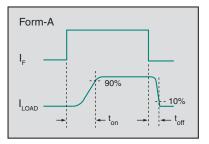
Ordering Information

Part #	Description
CPC1230N	4-Pin SOP (100/tube)
CPC1230NTR	4-Pin SOP (2000/reel)

Pin Configuration



Switching Characteristics of Normally-Open Devices







Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage (Peak)	350	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	А
Input Power Dissipation	70	mW
Total Power Dissipation ¹	400	mW
Capacitance, Input to Output	1	pF
Isolation Voltage, Input to Output	1500	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

 $^1\,$ Derate linearly 3.33 mW / $^o\!C$

Electrical Characteristics @ 25°C

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics					1	1
Load Current						
Continuous ¹	-	I _L			120	mA _{rms} / mA _{DC}
Peak	t=10ms	I _{LPK}	-	-	±350	mA _P
On-Resistance ²	I _L =120mA	R _{ON}	-	25	30	Ω
Off-State Leakage Current	V _L =350V _P	ILEAK	-	-	1	μΑ
Switching Speeds						
Turn-On	L - EmA \/ -10\/	t _{on}	-	-	2	
Turn-Off	I _F =5mA, V _L =10V	t _{off}	-	-	1	ms
Output Capacitance	I _F =0mA, V _L =50V, f=1MHz	C _{OUT}	-	9	-	pF
Input Characteristics						
Input Control Current to Activate ³	I _L =120mA	I _F	-	0.8	2	mA
Input Control Current to Deactivate	-	I _F	0.3	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _B =5V	I _B	-	-	10	μΑ

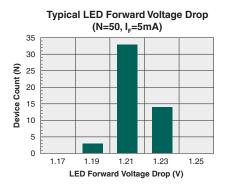
Load current derates linearly from 120mA @ 25°C to 80mA @ 85°C. Measurement taken within 1 second of on-time. 1

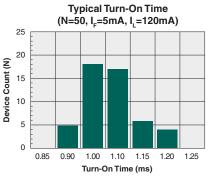
3 For applications requiring high temperature operation (greater than 60°C) a minimum LED drive current of 4mA is recomended.

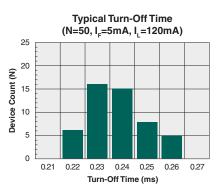


CPC1230N

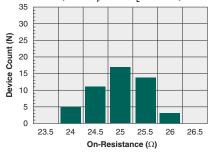
PERFORMANCE DATA @ 25°C (Unless Otherwise Noted)*

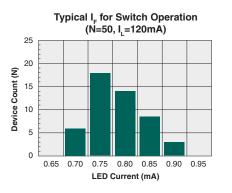




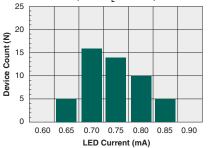


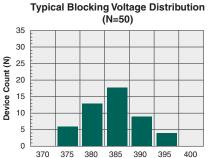
Typical On-Resistance Distribution (N=50, I_z=2mA, I₁=120mA)



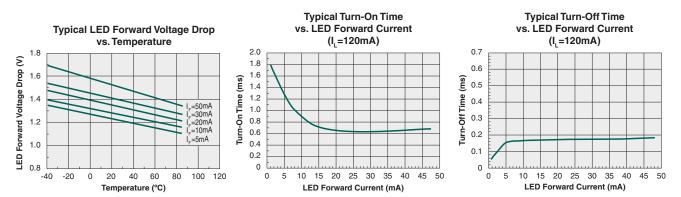


Typical I_F for Switch Dropout (N=50, I_L=120mA)





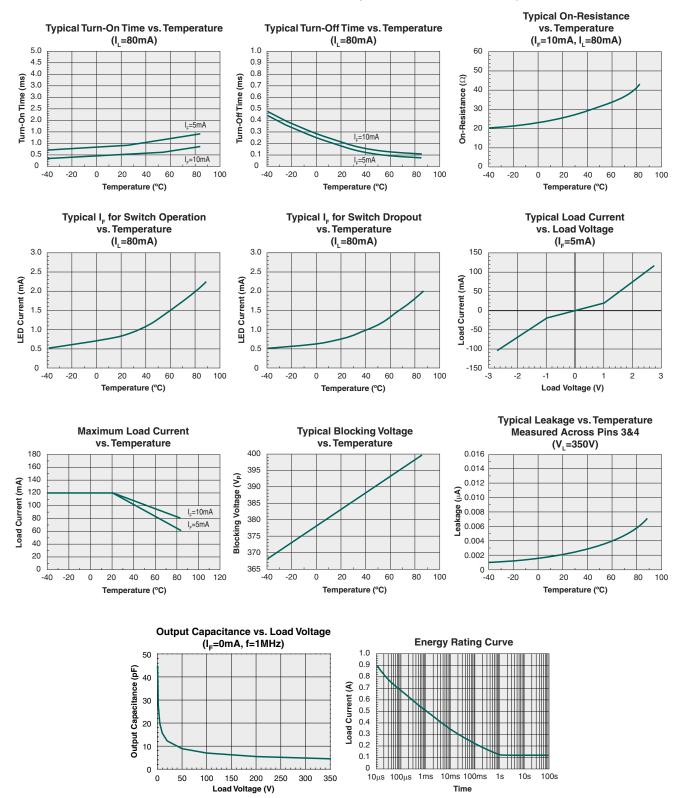
Blocking Voltage (V_p)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



PERFORMANCE DATA @ 25°C (Unless Otherwise Noted)*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating	
CPC1230N	MSL 3	

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Soldering Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time	Maximum Reflow Cycles
CPC1230N	260°C for 30 seconds	3

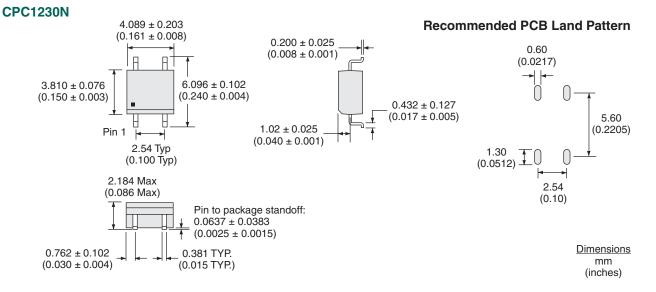
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

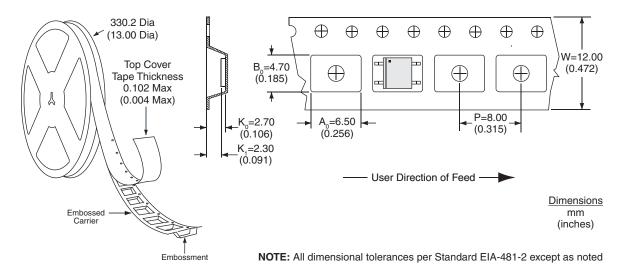




MECHANICAL DIMENSIONS



CPC1230NTR Tape & Reel



For additional information please visit our website at: www.ixysic.com

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