

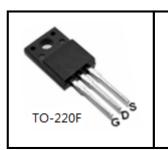
650V N-Channel MOSFET

FEATURES

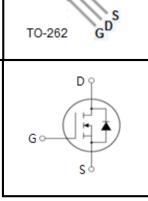
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



TO-220 GDS



Device Marking and Package Information			
Device	Package	Marking	
CS10N65F	TO-220F	CS10N65F	
CS10N65P	TO-220	CS10N65P	
CS10N65K	TO-262	CS10N65K	

Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Value			Unit	
		TO-220F	TO-220	TO-262	Onit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}		650		V	
Continuous Drain Current	I _D		10		А	
Pulsed Drain Current (note1)	I _{DM}	40			А	
Gate-Source Voltage	V _{GSS}		±30		V	
Single Pulse Avalanche Energy (note2)	E _{AS}	321		mJ		
Avalanche Current (note1)	I _{AS}	8		А		
Repetitive Avalanche Energy (note1)	E _{AR}	192		mJ		
Power Dissipation ($T_c = 25^{\circ}C$)	P _D	32	6	65	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		٥C		

Thermal Resistance					
Parameter	Symbol	Value			Unit
		TO-220F	TO-220	TO-262	
Thermal Resistance, Junction-to-Case	R_{thJC}	1.92	0.89		K/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		r\/ v v



CS10N65F,CS10N65P,CS10N65K

Specifications $T_J = 25^{\circ}C$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Value			11		
		Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250 \mu A$	650			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA		
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V _{GS} = 10V, I _D = 5.0A		0.65	0.8	Ω		
Dynamic								
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz		1377		pF		
Output Capacitance	C _{oss}			138				
Reverse Transfer Capacitance	C _{rss}			18				
Total Gate Charge	Q_{g}	$V_{DD} = 520V, I_{D} = 10A,$ $V_{GS} = 10V$		48		nC		
Gate-Source Charge	Q_{gs}			7				
Gate-Drain Charge	Q_{gd}			23				
Turn-on Delay Time	t _{d(on)}			45				
Turn-on Rise Time	t _r	V _{DD} = 325V, I _D =10A,		29		20		
Turn-off Delay Time	t _{d(off)}	$R_{\rm G} = 25 \Omega$		201		ns		
Turn-off Fall Time	t _f			69				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	۱ _s	T _C = 25 °C			10	Λ		
Pulsed Diode Forward Current	I _{SM}				40	A		
Body Diode Voltage	V_{SD}	$T_{J} = 25^{\circ}C, I_{SD} = 5A, V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 10A, di _F /dt =100A /µs		524		ns		
Reverse Recovery Charge	Q _{rr}			2.7		μC		

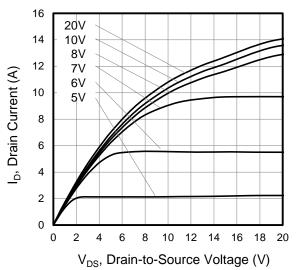
Notes

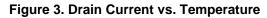
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%

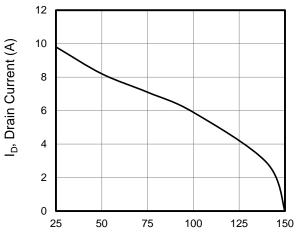


Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 1. Output Characteristics (T_J = 25°C)

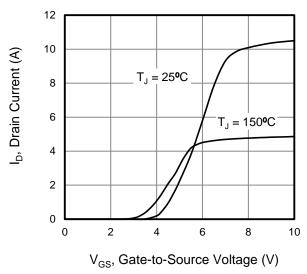






T_C, Case Temperature (A)





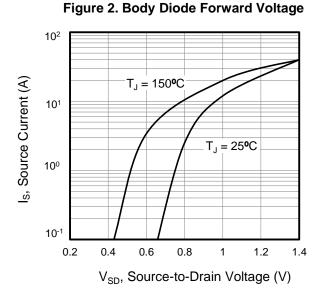


Figure 4. BV_{DSS} Variation vs. Temperature

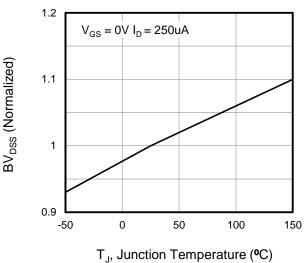
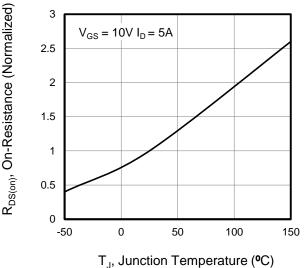
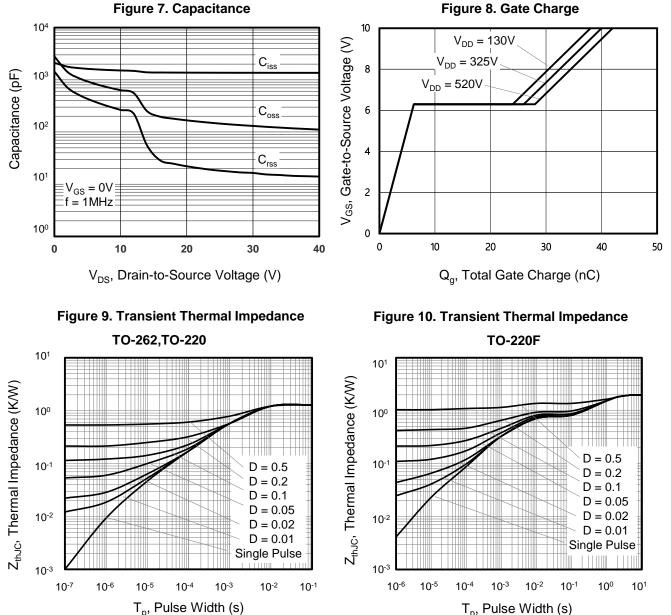


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



T_p, Pulse Width (s)





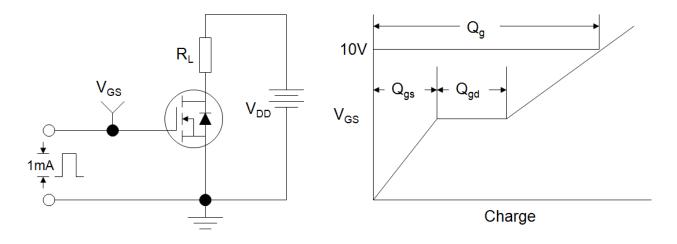


Figure B: Resistive Switching Test Circuit and Waveform

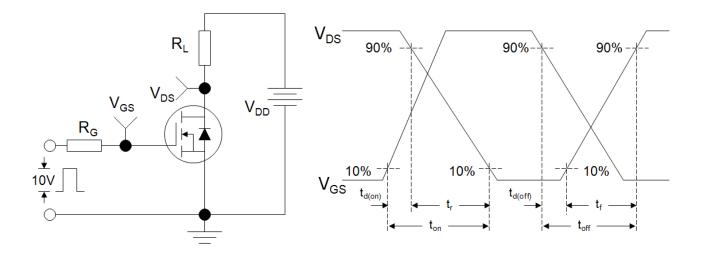
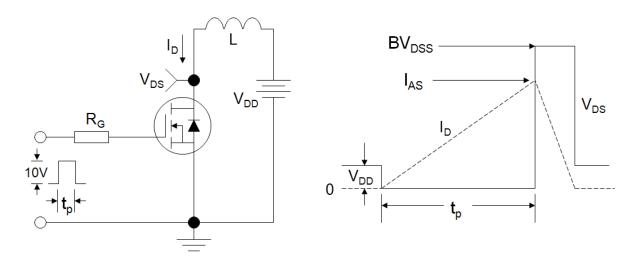
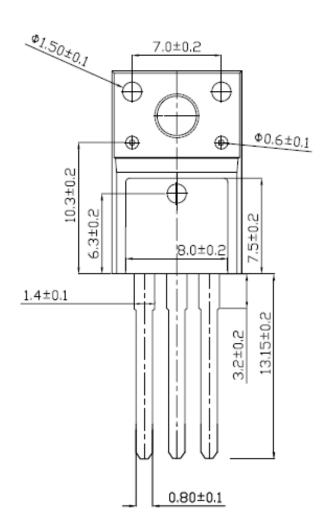


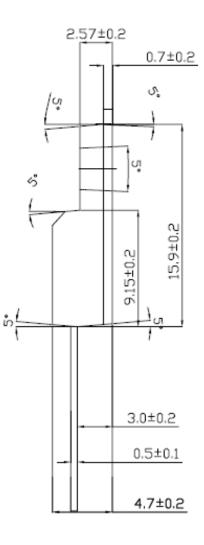
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F

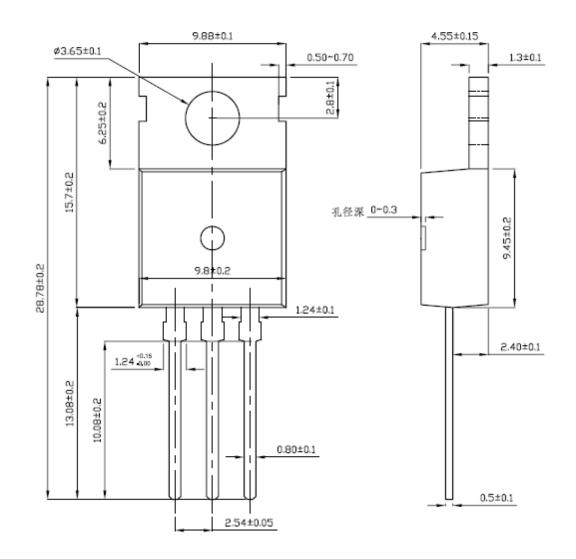






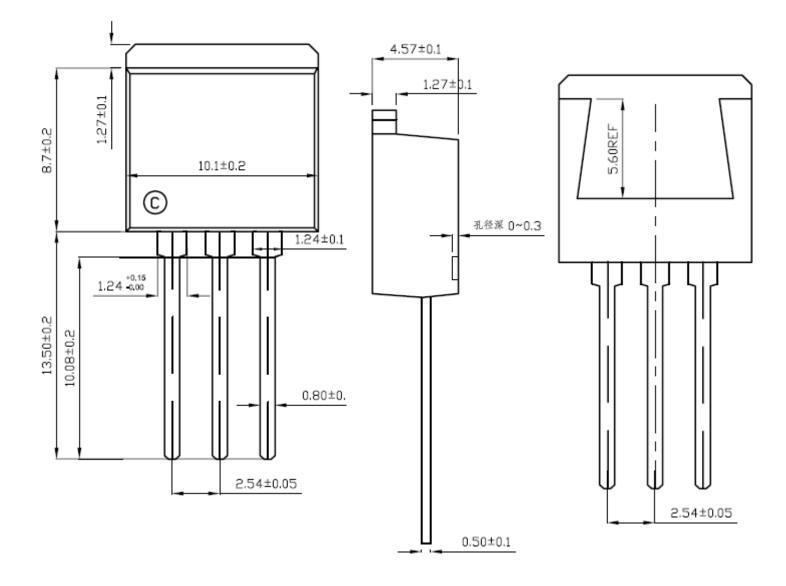


TO-220





TO-262





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