

500V N-Channel MOSFET

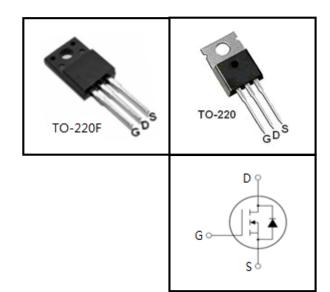
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package	Marking		
CS15N50F	TO-220F	CS15N50F		
CS15N50P	TO-220	CS15N50P		



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Dovementor	Symbol	Val	l lmit		
Parameter		TO-220F	TO-220	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	500		V	
Continuous Drain Current	I _D	15		А	
Pulsed Drain Current (note1)	I _{DM}	60		А	
Gate-Source Voltage	V_{GSS}	±30		\ \	
Single Pulse Avalanche Energy (note2)	E _{AS}	352.8		mJ	
Avalanche Current (note1)	I _{AS}	8.4		Α	
Repetitive Avalanche Energy (note1)	E _{AR}	229.3		mJ	
Power Dissipation (T _C = 25°C)	P _D	32	65	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~-	⊦ 150	°C	

Thermal Resistance					
Barrandari	Symbol	Va	11		
Parameter		TO-220F	TO-220	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	1.92	0.89	12/\\	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60	K/W	



Specifications $T_J = 25^{\circ}C$, unless otherwise noted							
Parameter	Symbol	To at Oom distance	Value			1121	
		Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	500			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 500V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA	
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V	
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 7.5A$		0.35	0.42	Ω	
Dynamic							
Input Capacitance	C _{iss}	$V_{GS} = 0V$, $V_{DS} = 25V$,		1752		pF	
Output Capacitance	C _{oss}			185			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		13.5			
Total Gate Charge	Q_g	$V_{DD} = 400V, I_{D} = 15A,$ $V_{GS} = 10V$		44		nC	
Gate-Source Charge	Q_{gs}			7.5			
Gate-Drain Charge	Q_{gd}	93		19			
Turn-on Delay Time	t _{d(on)}			45.5		ns	
Turn-on Rise Time	t _r	V _{DD} = 250V, I _D =15A,		27			
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		193			
Turn-off Fall Time	t _f			45			
Drain-Source Body Diode Character	istics						
Continuous Body Diode Current	Is				15	^	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			60	A	
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 7.5\text{A}, V_{GS} = 0\text{V}$			1.4	V	
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 15A,$		472		ns	
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		3.24		μC	

2

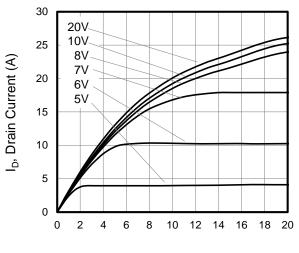
Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 ^{o}C
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)



V_{DS}, Drain-to-Source Voltage (V)

Figure 2. Body Diode Forward Voltage

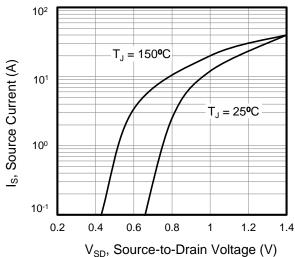


Figure 3. Drain Current vs. Temperature

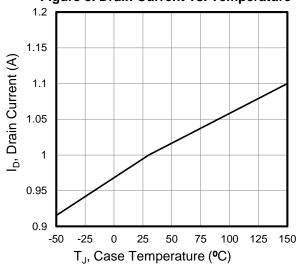


Figure 5. Transfer Characteristics

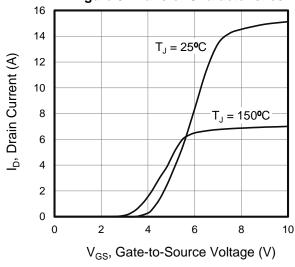


Figure 4. BV_{DSS} Variation vs. Temperature

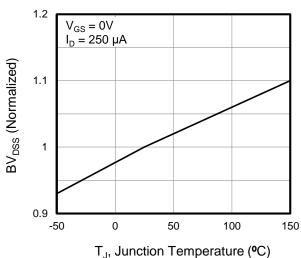
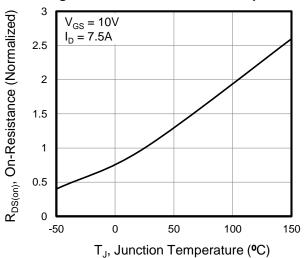


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

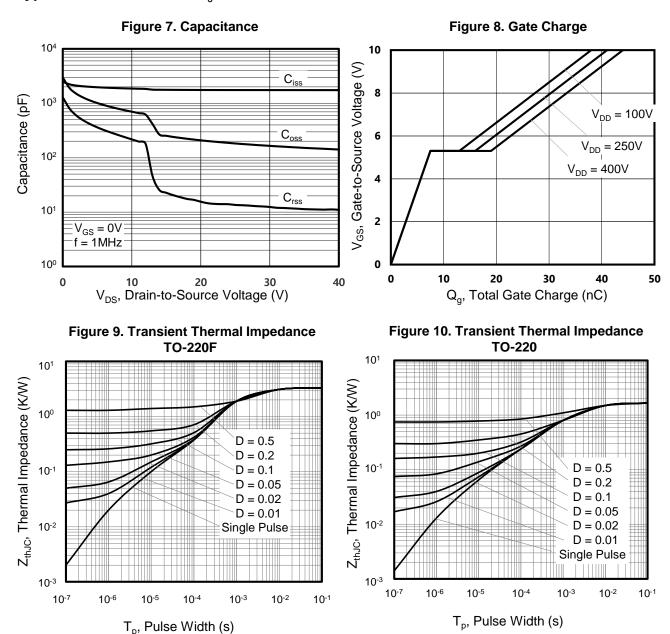




Figure A: Gate Charge Test Circuit and Waveform

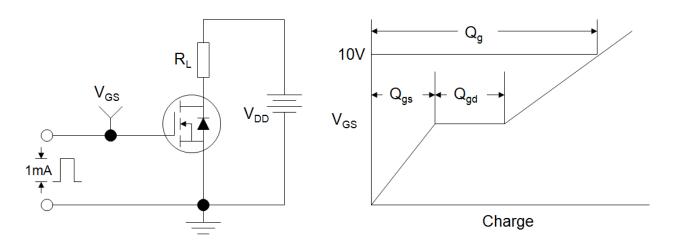


Figure B: Resistive Switching Test Circuit and Waveform

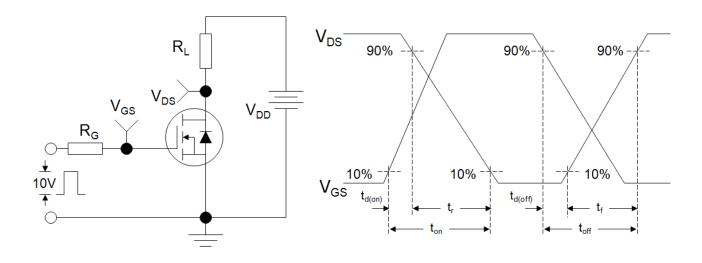
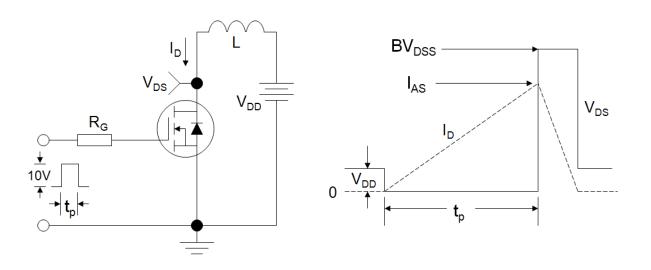
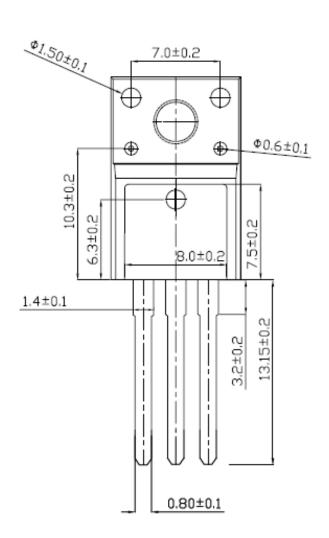


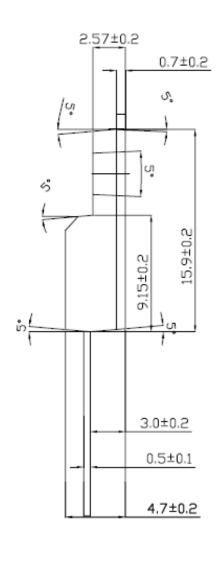
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





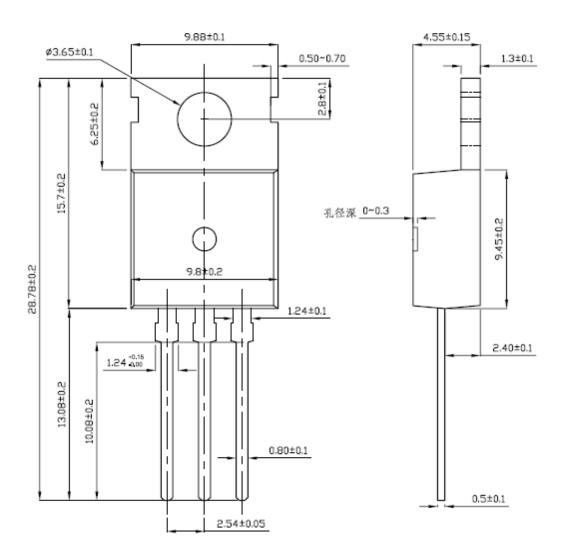
TO-220F







TO-220





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