

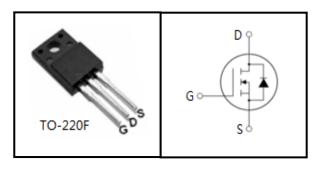
700V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information				
Device Package		Marking		
CS15N70F	TO-220F	CS15N70F		

Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted							
Parameter	Symbol	Value	Unit				
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	700	V				
Continuous Drain Current	I _D	15	А				
Pulsed Drain Current (note1)	I _{DM}	60	А				
Gate-Source Voltage	V _{GSS}	±30	V				
Single Pulse Avalanche Energy (note2)	E _{AS}	520	mJ				
Avalanche Current (note1)	I _{AS}	10.2	А				
Repetitive Avalanche Energy (note1)	E _{AR}	312	mJ				
Power Dissipation ($T_c = 25^{\circ}C$)	P _D	70	W				
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C				

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	1.78	°C/W	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5		



CS15N70F

Specifications $T_J = 25^{\circ}C$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Value			Unit		
	Symbol	rest conditions	Min.	Тур.	Max.			
Static				-				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250 \mu A$	700			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 700V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	uA		
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30 V$			±100	nA		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 7.5A		0.56	0.66	Ω		
Dynamic								
Input Capacitance	C _{iss}	V _{GS} = 0V,		1990				
Output Capacitance	C _{oss}	$V_{DS} = 25V,$		197		pF		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		25				
Total Gate Charge	Q_g	V _{DD} = 560V, I _D = 15A, V _{GS} = 10V		65		nC		
Gate-Source Charge	Q_{gs}			9.5				
Gate-Drain Charge	Q_{gd}			34				
Turn-on Delay Time	t _{d(on)}			48		ns		
Turn-on Rise Time	t _r	$V_{DD} = 350$ V, I _D =15A, R _G = 25 Ω		39.5				
Turn-off Delay Time	t _{d(off)}	R _G = 25 Ω		260				
Turn-off Fall Time	t _f			68				
Drain-Source Body Diode Character	istics	-						
Continuous Body Diode Current	۱ _s	T _c = 25 ⁰C			15	A		
Pulsed Diode Forward Current	I _{SM}	$T_{\rm C} = 25.0$			60			
Body Diode Voltage	V_{SD}	$T_{J} = 25^{\circ}C, I_{SD} = 7.5A, V_{GS} = 0V$			1.2	V		
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 15A,		777		ns		
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		4		μC		

Notes

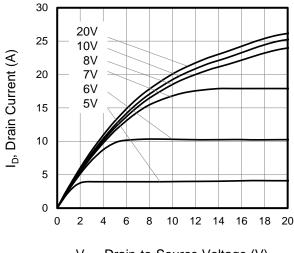
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_G = 25 \Omega, Starting T_J = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



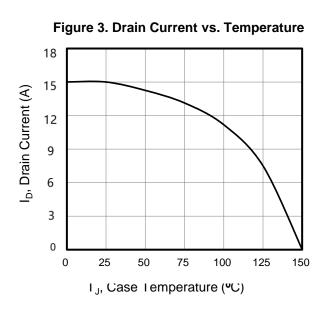
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

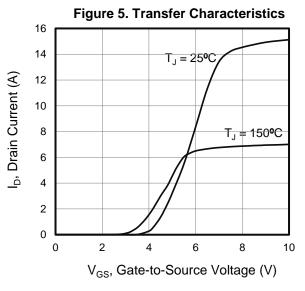
Figure 1. Output Characteristics (T_J = 25°C)





 V_{DS} , Drain-to-Source Voltage (V)





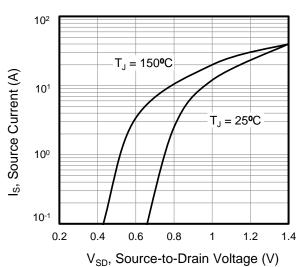


Figure 4. BV_{DSS} Variation vs. Temperature

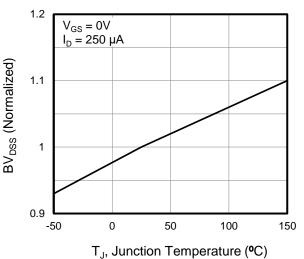
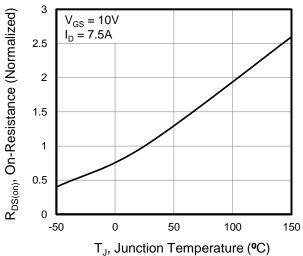
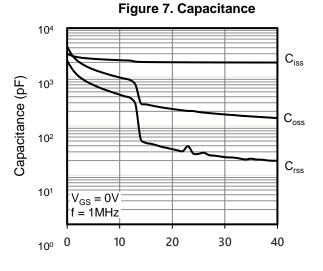


Figure 6. On-Resistance vs. Temperature

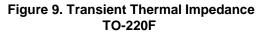


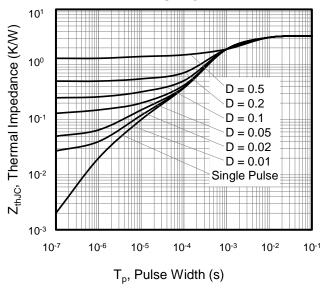


Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted



V_{DS}, Drain-to-Source Voltage (V)





V_{GS}, Gate-to-Source Voltage (V)

Figure 8. Gate Charge

Q_g, Total Gate Charge (nC)





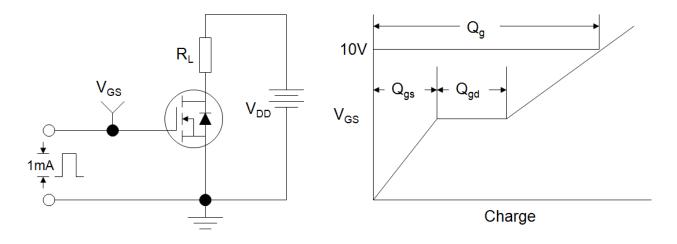


Figure B: Resistive Switching Test Circuit and Waveform

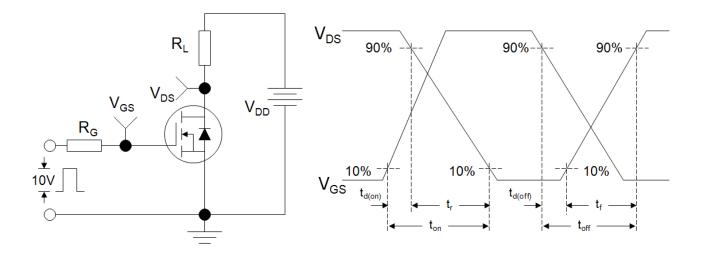
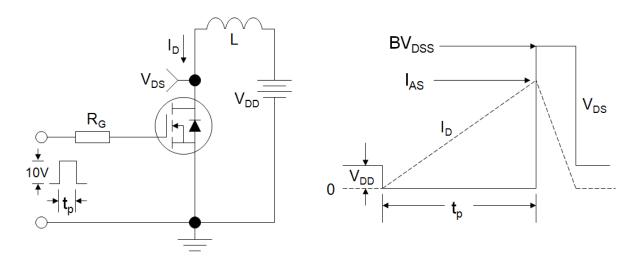
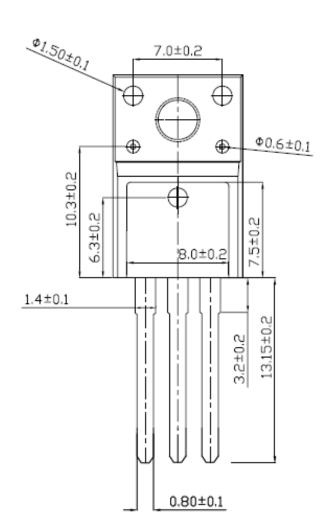


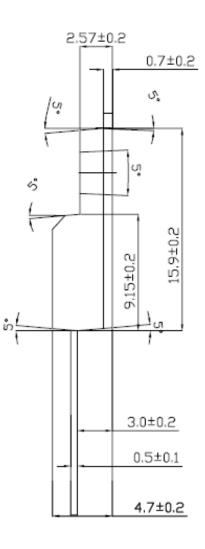
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F







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