

# **650V N-Channel MOSFET**

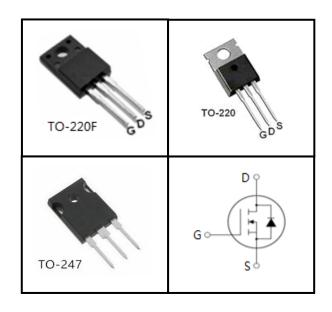
### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information						
Device	Package	Marking				
CS16N65F	TO-220F	CS16N65F				
CS16N65P	TO-220	CS16N65P				
CS16N65W	TO-247	CS16N65W				



Absolute Maximum Ratings T <sub>C</sub> = 25°C, unless otherwise noted						
Davamatan	Symbol		1114			
Parameter		TO-220F	TO-220	TO-247	Unit	
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	650		-	V	
Continuous Drain Current	I <sub>D</sub>	16			А	
Pulsed Drain Current (note1)	I <sub>DM</sub>	64			А	
Gate-Source Voltage	V <sub>GSS</sub>	±30		V		
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	460		mJ		
Avalanche Current (note1)	I <sub>AS</sub>	9.6		А		
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	276			mJ	
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	98 196		W		
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150			°C	

Thermal Resistance						
Bassastas	Symbol	Value			11.7	
Parameter		TO-220F	TO-220	TO-247	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	1.27	0.635		°C/W	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	60		] 30/00	

# CS16N65F,CS16N65P,CS16N65W

Davamatav		Took Complications	Value						
Parameter	Symbol Test Conditions -		Min.	Тур.	Max.	Unit			
Static									
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650			V			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ			
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V			
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_{D} = 8A$		0.45	0.55	Ω			
Dynamic									
Input Capacitance	C <sub>iss</sub>	V 0V		2063		pF			
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		204					
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		29					
Total Gate Charge	$Q_g$			74					
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 520V, I_{D} = 16A,$ $V_{GS} = 10V$		10		nC			
Gate-Drain Charge	$Q_{gd}$	65		40					
Turn-on Delay Time	t <sub>d(on)</sub>			54					
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 325V, I_{D} = 16A,$		40					
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		312		ns			
Turn-off Fall Time	t <sub>f</sub>			66					
Drain-Source Body Diode Character	istics								
Continuous Body Diode Current	I <sub>S</sub>	T 25.00			16	Δ.			
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			64	Α			
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 8\text{A}, V_{GS} = 0\text{V}$			1.4	V			
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 16A,$		682		ns			
Reverse Recovery Charge	$Q_{rr}$	di <sub>F</sub> /dt =100A /μs		4.5		μC			

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Figure 2. Body Diode Forward Voltage



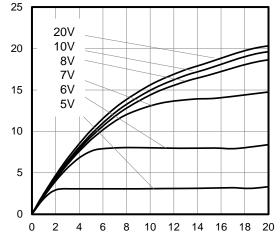
I<sub>D</sub>, Drain Current (A)

Drain Current (A)

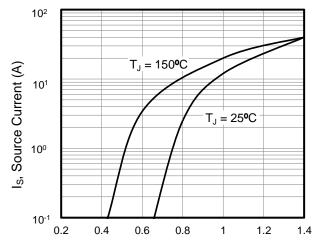
l<sub>D</sub>, Drain Current (A)

## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted



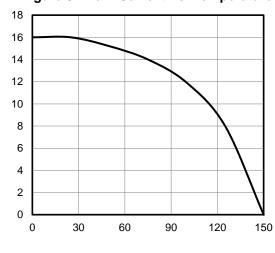


V<sub>DS</sub>, Drain-to-Source Voltage (V)



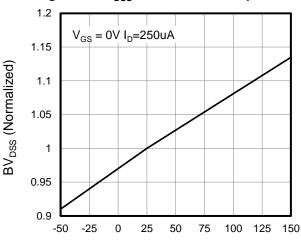
V<sub>SD</sub>, Source-to-Drain Voltage (V)

Figure 3. Drain Current vs. Temperature



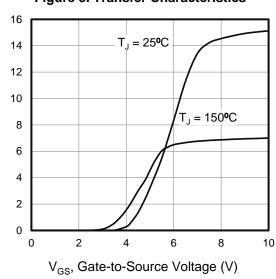
T<sub>C</sub>, Case Temperature (A)

Figure 4. BV<sub>DSS</sub> Variation vs. Temperature



T<sub>C</sub>, Case Temperature (°C)

Figure 5. Transfer Characteristics



R<sub>DS(on)</sub>, On-Resistance (Normalized)

Figure 6. On-Resistance vs. Temperature

V<sub>GS</sub> = 10V I<sub>D</sub>= 8A

2.5

1.5

1
0.5
0
-75
-25
25
75
125

T<sub>J</sub>, Junction Temperature (°C)

## CS16N65F,CS16N65P,CS16N65W

## **Typical Characteristics** $T_J = 25$ °C, unless otherwise noted

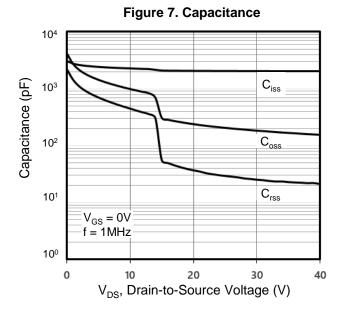


Figure 9. Transient Thermal Impedance
TO-220F

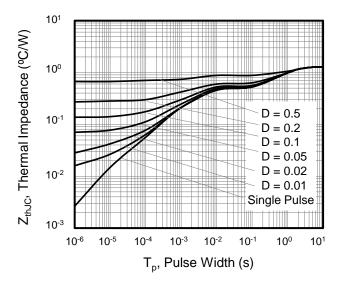


Figure 8. Gate Charge

10

V<sub>DD</sub> = 130V

V<sub>DD</sub> = 325V

V<sub>DD</sub> = 520V

V<sub>DD</sub> = 520V

0

0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

Q<sub>n</sub>, Total Gate Charge (nC)

Figure 10. Transient Thermal Impedance TO-220

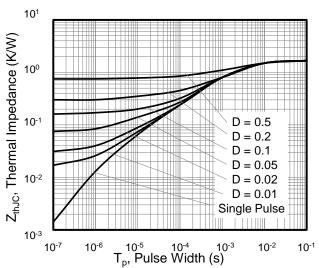




Figure A: Gate Charge Test Circuit and Waveform

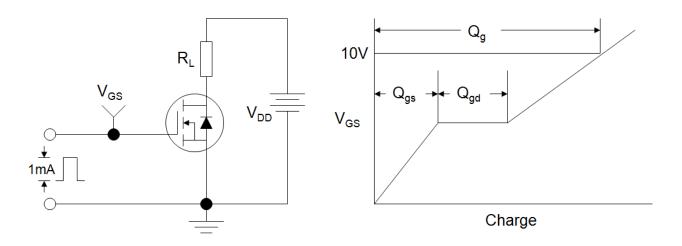


Figure B: Resistive Switching Test Circuit and Waveform

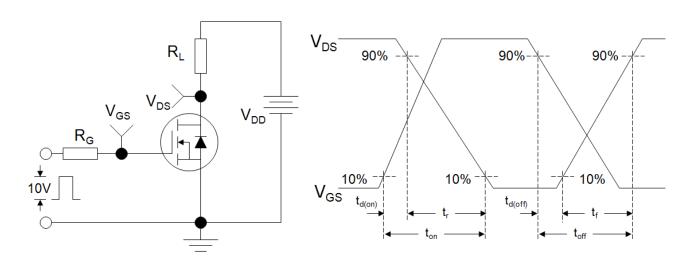
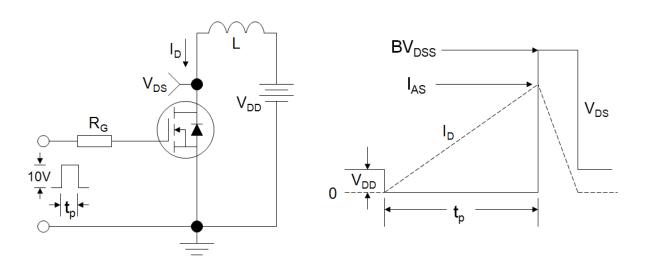
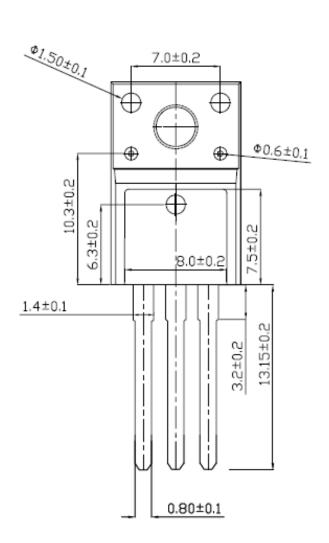


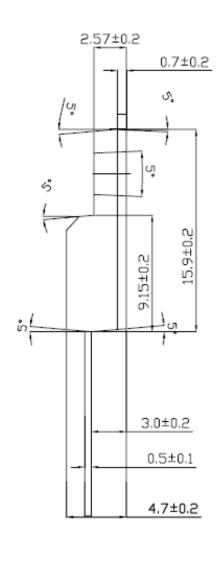
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





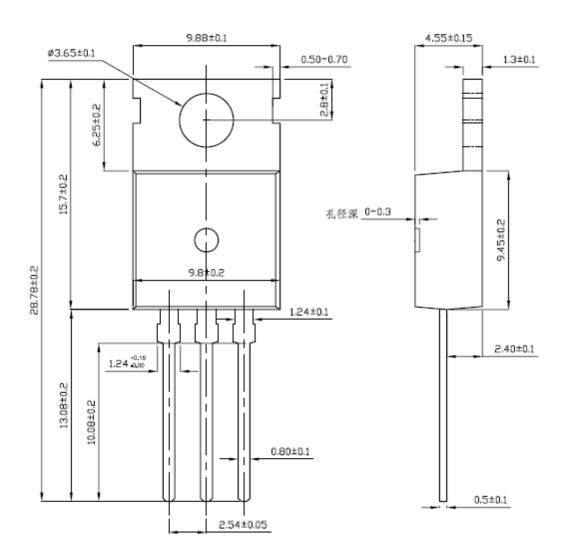
## **TO-220F**







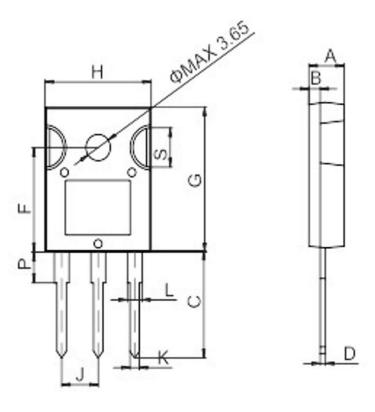
## **TO-220**



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**TO-247** 



	Dimensions							
Ref.	1	MIIImeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.		
Α	4.9		5.4	0.193		0.213		
В	1.6		2.0	0.063		0.079		
С	14.35		15.4	0.565		0.606		
D	0.5		0.8	0.020		0.031		
F	14.4		15.1	0.567		0.594		
G	19.7		20.6	0.775		0.811		
Н	15.4		16.2	0.606		0.638		
J	5.3		5.6	0.209		0.220		
K	1.3		1.5	0.051		0.059		
L	2.8		3.3	0.110		0.130		
Р	3.7		4.2	0.146		0.165		
S	5.35		5.65	0.211		0.222		

### CS16N65F,CS16N65P,CS16N65W

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