

# **500V N-Channel MOSFET**

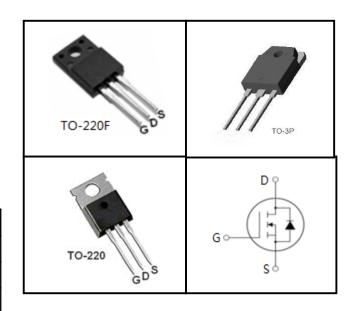
#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package	Marking		
CS18N50F	TO-220F	CS18N50F		
CS18N50P	TO-220	CS18N50P		
CS18N50V	TO-3P	CS18N50V		



<b>Absolute Maximum Ratings</b> T <sub>C</sub> = 25°C, unless otherwise noted						
Parameter	Symbol	Value			Unit	
raianietei		TO-220F	TO-3P	TO-220	Onit	
Drain-Source Voltage ( $V_{GS} = 0V$ )	V <sub>DSS</sub>	500		V		
Continuous Drain Current	I <sub>D</sub>	18		Α		
Pulsed Drain Current (note1)	I <sub>DM</sub>	72		Α		
Gate-Source Voltage	V <sub>GSS</sub>	±30		V		
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	980		mJ		
Avalanche Current (note1)	I <sub>AS</sub>	14		Α		
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	588		mJ		
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	98	160	)	W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150		°C		

Thermal Resistance						
Parameter	Symbol	Value			I Incid	
Parameter		TO-220F	TO-3P	TO-220	Unit	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.27	0.6		K/W	
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	40		r/ vv	

## CS18N50F,CS18N50P,CS18N50V

<b>Specifications</b> $T_J = 25^{\circ}$ C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
raiametei	Зуппоп		Min.	Тур.	Max.	Onit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	500			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 500V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_{D} = 9A$		0.28	0.34	Ω
Dynamic						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V,		2367		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 25V$ ,		228		
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		15		
Total Gate Charge	$Q_g$			53.4		nC
Gate-Source Charge	$Q_gs$	$V_{DD} = 400 \text{V}, I_D = 18 \text{A}, $ $V_{GS} = 10 \text{V}$		10		
Gate-Drain Charge	$Q_{gd}$			20		
Turn-on Delay Time	t <sub>d(on)</sub>			51.3		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 250V, I_{D} = 18A,$		36.5		ns
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		232		
Turn-off Fall Time	t <sub>f</sub>			61		
Drain-Source Body Diode Character	ristics					
Continuous Body Diode Current	Is	T 05.00			18	A
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			72	
Body Diode Voltage	$V_{SD}$	$T_J = 25^{\circ}C$ , $I_{SD} = 9A$ , $V_{GS} = 0V$			1.4	V
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 18A,$		497		ns
Reverse Recovery Charge	$Q_{rr}$	di <sub>F</sub> /dt =100A /μs		4		μC

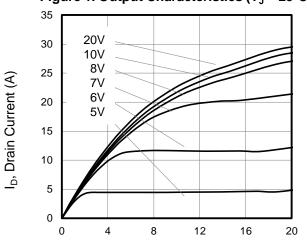
#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



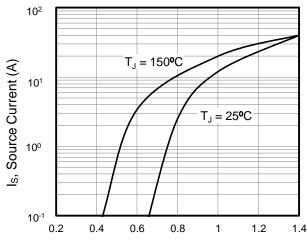
### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics (T<sub>J</sub> = 25°C)



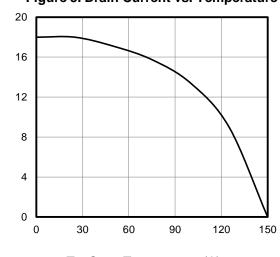
 $V_{DS}$ , Drain-to-Source Voltage (V)

Figure 2. Body Diode Forward Voltage



V<sub>SD</sub>, Source-to-Drain Voltage (V)

Figure 3. Drain Current vs. Temperature

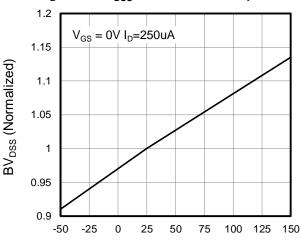


Drain Current (A)

I<sub>D</sub>, Drain Current (A)

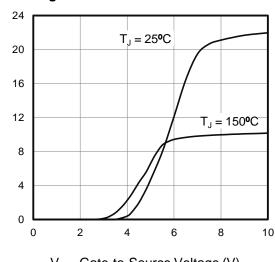
T<sub>C</sub>, Case Temperature (A)

Figure 4. BV<sub>DSS</sub> Variation vs. Temperature



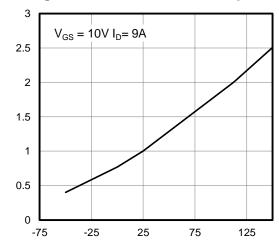
T<sub>C</sub>, Case Temperature (°C)

Figure 5. Transfer Characteristics



 $V_{GS}$ , Gate-to-Source Voltage (V)

Figure 6. On-Resistance vs. Temperature



T<sub>J</sub>, Junction Temperature (°C)

RDS(on), On-Resistance (Normalized)

## CS18N50F,CS18N50P,CS18N50V

### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

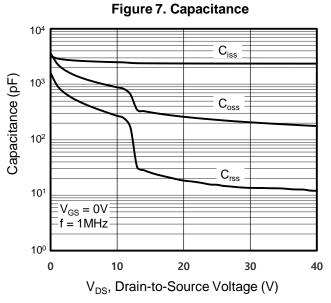


Figure 9. Transient Thermal Impedance TO-220F

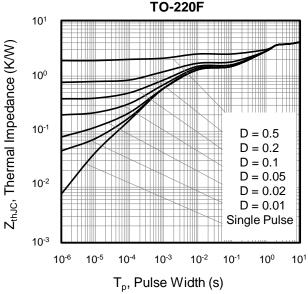


Figure 10. Transient Thermal Impedance TO-220. TO-3P

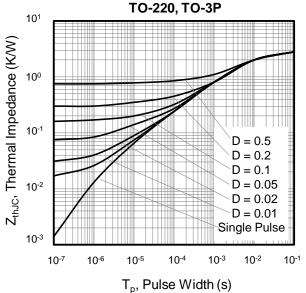




Figure A: Gate Charge Test Circuit and Waveform

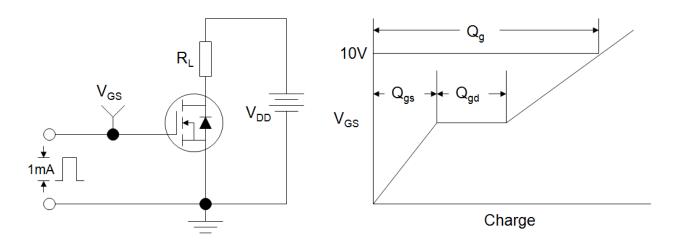


Figure B: Resistive Switching Test Circuit and Waveform

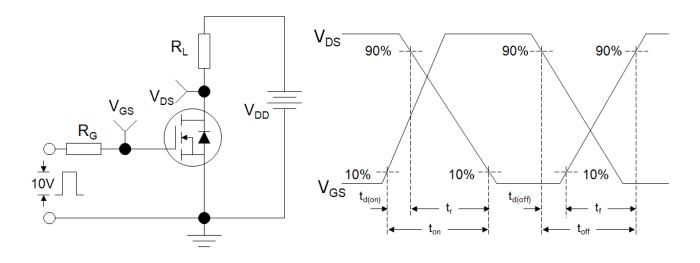
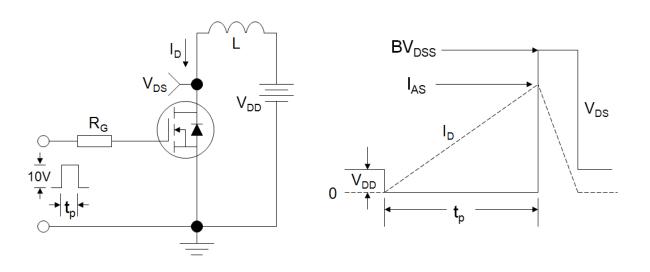
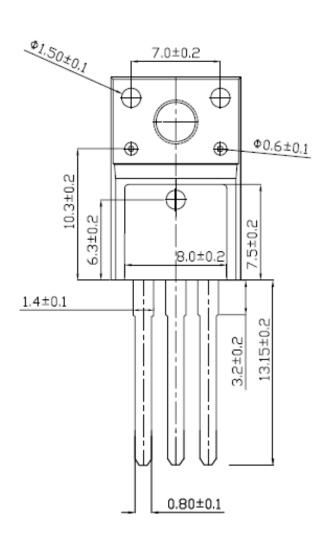


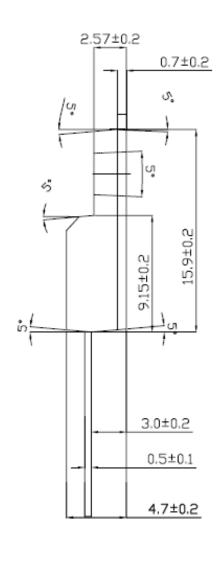
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





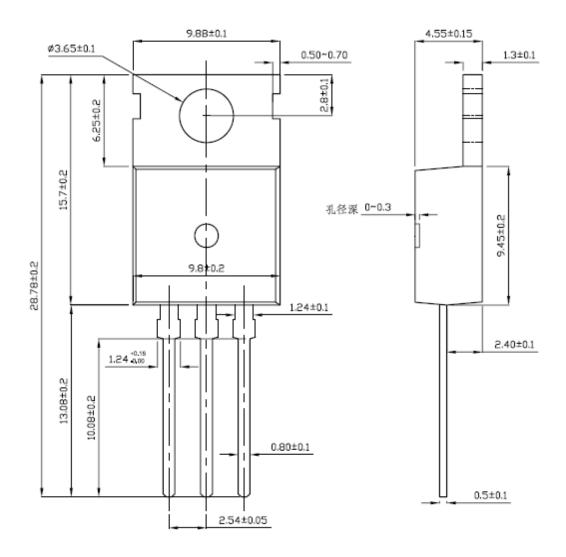
# **TO-220F**







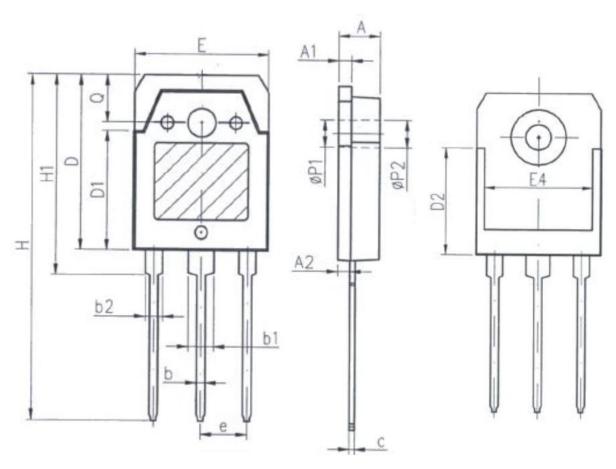
## **TO-220**



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**TO-3P** 



Unit:mm				
Symbol	Min.	Max.		
Α	4. 6	5		
A1	1. 4	1. 65		
A2	1. 18	1. 58		
b	0.8	1. 2		
b1	2. 8	3. 2		
b2	1.8	2. 2		
C	0.5	0. 75		
D	19. 6	20. 2		
D1	13. 55	14. 25		
D2	12. 9REF			
E	15. 35	15. 85		
E4	12. 6	_		
е	5. 45TYP			
Н	40. 1	40. 9		
H1	23. 15	23. 65		
P1	3. 2REF			
P2	3. 5REF			

### CS18N50F,CS18N50P,CS18N50V

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