

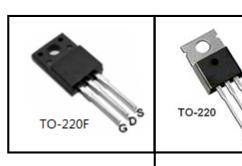
600V N-Channel MOSFET

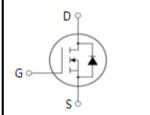
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)





Device Marking and Package Information			
Device	Package	Marking	
CS20N60F	TO-220F	CS20N60F	
CS20N60P	TO-220	CS20N60P	

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted							
Borometor	Symbol	Va	11				
Parameter		TO-220F	TO-220	Unit			
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	600		V			
Continuous Drain Current	I _D	20		Α			
Pulsed Drain Current (note1)	I _{DM}	80		Α			
Gate-Source Voltage	V_{GSS}	±30 566 11		V			
Single Pulse Avalanche Energy (note2)	E _{AS}			mJ			
Avalanche Current (note1)	I _{AS}			Α			
Repetitive Avalanche Energy (note1)	E _{AR}	339		mJ			
Power Dissipation (T _C = 25°C)	P_{D}	120	140	W			
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C			

Thermal Resistance				
Parameter	Symbol	Va	l lmit	
		TO-220F	TO-220	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	1.9	1.2	00.00
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60	°C/W



Specifications $T_J = 25^{\circ}$ C, unless otherwise noted								
Doromotor	Cumhal	Test Conditions	Value			l lmit		
Parameter	Symbol	rest Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			٧		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 600 \text{V}, V_{GS} = 0 \text{V}, T_{J} = 25^{\circ}\text{C}$			1	μA		
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		0.32	0.40	Ω		
Dynamic								
Input Capacitance	C _{iss}	V 0V		2521		pF		
Output Capacitance	C _{oss}	$V_{GS} = 0V$, $V_{DS} = 25V$, f = 1.0MHz		264				
Reverse Transfer Capacitance	C _{rss}			35				
Total Gate Charge	Q_g	$V_{DD} = 480V, I_{D} = 20A,$ $V_{GS} = 10V$		76		nC		
Gate-Source Charge	Q_{gs}			13				
Gate-Drain Charge	Q_{gd}			32				
Turn-on Delay Time	t _{d(on)}			53				
Turn-on Rise Time	t _r	$V_{DD} = 300V, I_{D} = 20A,$		44		ns		
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		321				
Turn-off Fall Time	t _f			80				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	I _s	T _C = 25 °C			20	А		
Pulsed Diode Forward Current	I _{SM}				80			
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 10A$, $V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 20A,$ $di_{F}/dt = 100A / \mu s$		733		ns		
Reverse Recovery Charge	Q _{rr}			6.5		μC		

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

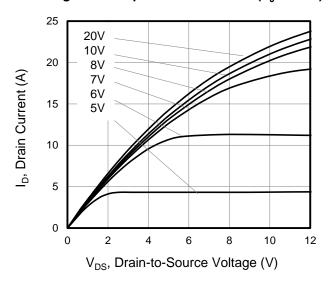


Figure 3. Drain Current vs. Temperature

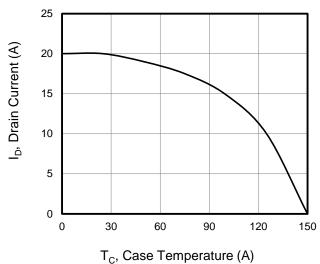


Figure 5. Transfer Characteristics

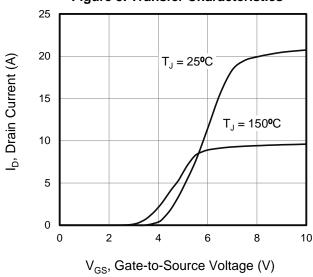


Figure 2. Body Diode Forward Voltage

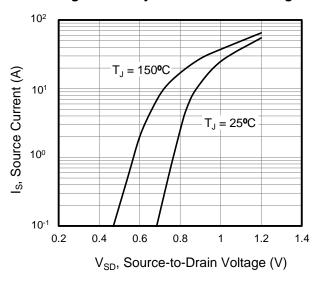


Figure 4. BV_{DSS} Variation vs. Temperature

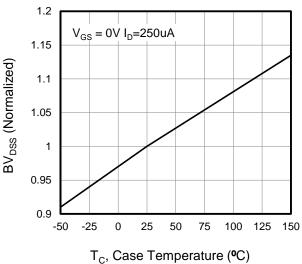
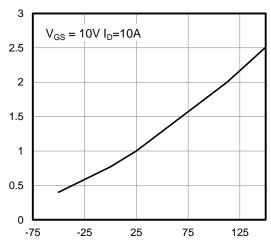


Figure 6. On-Resistance vs. Temperature



T_J, Junction Temperature (°C)

R_{DS(on)}, On-Resistance (Normalized)



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

V_{GS}, Gate-to-Source Voltage (V)

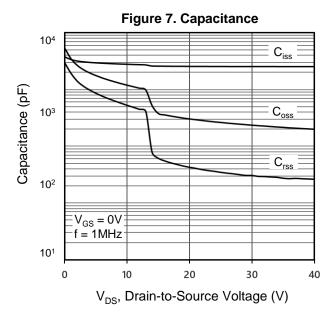
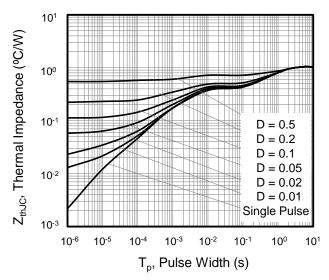


Figure 9. Transient Thermal Impedance
TO-220



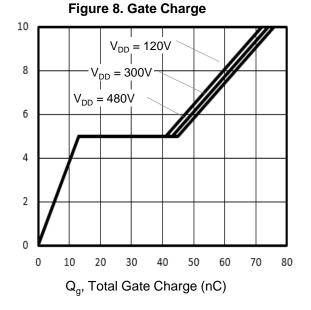


Figure 9. Transient Thermal Impedance TO-220F

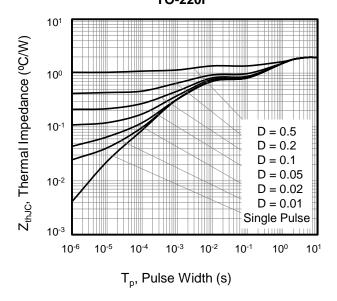




Figure A: Gate Charge Test Circuit and Waveform

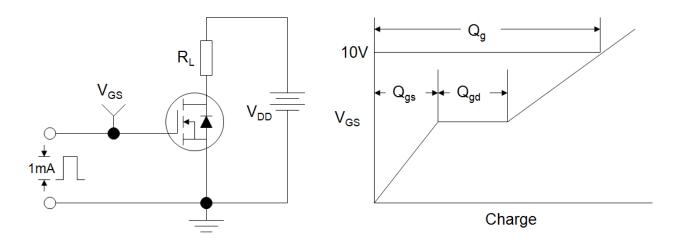


Figure B: Resistive Switching Test Circuit and Waveform

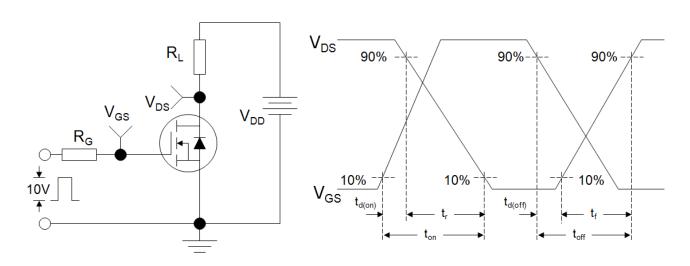
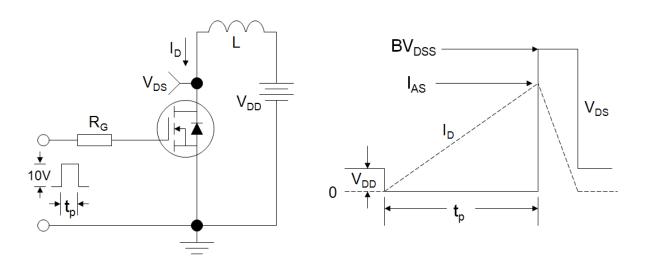
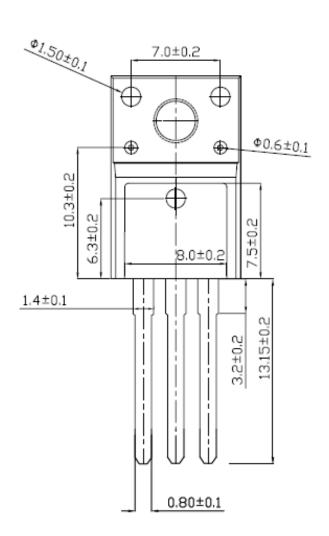


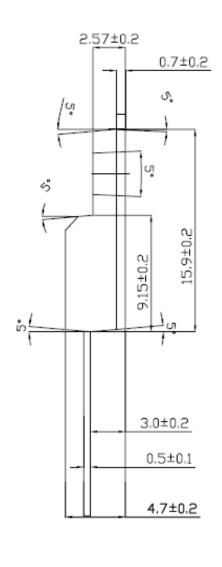
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





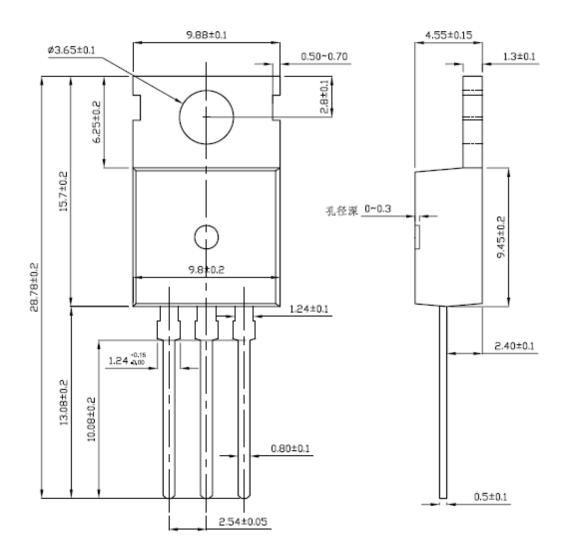
TO-220F







TO-220





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