

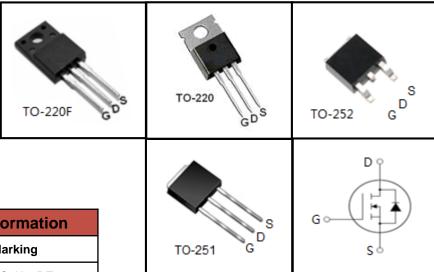
## **500V N-Channel MOSFET**

#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



| <b>Device Marking and Package Information</b> |         |          |  |  |  |
|---|---------|----------|--|--|--|
| Device  | Package | Marking  |  |  |  |
| CS2N50DF                                      | TO-220F | CS2N50DF |  |  |  |
| CS2N50DP                                      | TO-220  | CS2N50DP |  |  |  |
| CS2N50DU                                      | TO-251  | CS2N50DU |  |  |  |
| CS2N50DD                                      | TO-252  | CS2N50DD |  |  |  |

| <b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted |                  |          |        |        |        |      |  |
|--|------------------|----------|--------|--------|--------|------|--|
| Parameter  | Symbol           |          | Unit   |        |        |      |  |
| raiametei  |                  | TO-220F  | TO-220 | TO-251 | TO-252 | Unit |  |
| Drain-Source Voltage (V <sub>GS</sub> = 0V)                                  | V <sub>DSS</sub> | 500      |        |        | ٧      |      |  |
| Continuous Drain Current   | I <sub>D</sub>   | 2        |        |        | Α      |      |  |
| Pulsed Drain Current (note1)   | I <sub>DM</sub>  | 8        |        |        | А      |      |  |
| Gate-Source Voltage  | V <sub>GSS</sub> | ±30      |        |        | V      |      |  |
| Single Pulse Avalanche Energy (note2)  | E <sub>AS</sub>  | 11.2     |        |        | mJ     |      |  |
| Avalanche Current (note1)  | I <sub>AS</sub>  | 1.5      |        |        | А      |      |  |
| Repetitive Avalanche Energy (note1)  | E <sub>AR</sub>  | 6.7      |        |        | mJ     |      |  |
| Power Dissipation (T <sub>C</sub> = 25°C)                                    | $P_{D}$          | 25       |        | 19     |        | W    |  |
| Operating Junction and Storage Temperature Range                             | $T_J,T_stg$      | -55~+150 |        |        |        | °C   |  |

| Thermal Resistance                      |                   |         |        |        |        |      |  |
|---|-------------------|---------|--------|--------|--------|------|--|
| Barrantan                               | Symbol            | Value   |        |        |        | 11   |  |
| Parameter                               |                   | TO-220F | TO-220 | TO-251 | TO-252 | Unit |  |
| Thermal Resistance, Junction-to-Case    | R <sub>thJC</sub> | 11.9    | 6.6    |        | IZ AAI |      |  |
| Thermal Resistance, Junction-to-Ambient | $R_{thJA}$        | 62.5    | 60     |        | K/W    |      |  |

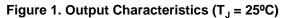
| <b>Specifications</b> $T_J = 25^{\circ}C$ , unless otherwise noted |                     |   |       |      |      |         |  |  |  |
|--|---------------------|---|-------|------|------|---------|--|--|--|
| Parameter  | Comple of           | Test Conditions                                     | Value |      |      | l la it |  |  |  |
|  | Symbol              | Test Conditions                                     | Min.  | Тур. | Max. | Unit    |  |  |  |
| Static   |                     |   |       |      |      |         |  |  |  |
| Drain-Source Breakdown Voltage                                     | $V_{(BR)DSS}$       | $V_{GS} = 0V, I_{D} = 250\mu A$                     | 500   |      |      | ٧       |  |  |  |
| Zero Gate Voltage Drain Current                                    | I <sub>DSS</sub>    | $V_{DS} = 500V, V_{GS} = 0V, T_{J} = 25^{\circ}C$   |       |      | 1    | μΑ      |  |  |  |
| Gate-Source Leakage  | I <sub>GSS</sub>    | $V_{GS} = \pm 30V$                                  |       |      | ±100 | nA      |  |  |  |
| Gate-Source Threshold Voltage                                      | V <sub>GS(th)</sub> | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$                | 3.0   |      | 4.0  | V       |  |  |  |
| Drain-Source On-Resistance (Note3)                                 | R <sub>DS(on)</sub> | V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A          |       | 5.4  | 6.5  | Ω       |  |  |  |
| Dynamic  |                     |   |       |      |      |         |  |  |  |
| Input Capacitance  | C <sub>iss</sub>    | $V_{GS} = 0V$ ,                                     |       | 128  |      | pF      |  |  |  |
| Output Capacitance   | C <sub>oss</sub>    | $V_{DS} = 25V$ ,                                    |       | 18   |      |         |  |  |  |
| Reverse Transfer Capacitance                                       | C <sub>rss</sub>    | f = 1.0MHz  |       | 2.5  |      |         |  |  |  |
| Total Gate Charge  | $Q_g$               |   |       | 6    |      | nC      |  |  |  |
| Gate-Source Charge   | $Q_{gs}$            | $V_{DD} = 400V, I_{D} = 2A,$<br>$V_{GS} = 10V$      |       | 1    |      |         |  |  |  |
| Gate-Drain Charge  | $Q_{gd}$            | 93  |       | 3.5  |      |         |  |  |  |
| Turn-on Delay Time   | t <sub>d(on)</sub>  |   |       | 33.5 |      |         |  |  |  |
| Turn-on Rise Time  | t <sub>r</sub>      | $V_{DD} = 250V, I_{D} = 2A,$                        |       | 5.5  |      | ns      |  |  |  |
| Turn-off Delay Time  | t <sub>d(off)</sub> | $R_G = 25 \Omega$                                   |       | 44.5 |      |         |  |  |  |
| Turn-off Fall Time   | t <sub>f</sub>      |   |       | 17   |      |         |  |  |  |
| Drain-Source Body Diode Character                                  | istics              |   |       |      |      |         |  |  |  |
| Continuous Body Diode Current                                      | I <sub>S</sub>      |   |       |      | 2    | A       |  |  |  |
| Pulsed Diode Forward Current                                       | I <sub>SM</sub>     | T <sub>C</sub> = 25 °C                              |       |      | 8    |         |  |  |  |
| Body Diode Voltage   | V <sub>SD</sub>     | $T_J = 25^{\circ}C$ , $I_{SD} = 1A$ , $V_{GS} = 0V$ |       |      | 1.4  | V       |  |  |  |
| Reverse Recovery Time  | t <sub>rr</sub>     | $V_{GS} = 0V, I_{S} = 2A,$                          |       | 611  |      | ns      |  |  |  |
| Reverse Recovery Charge  | Q <sub>rr</sub>     | di <sub>F</sub> /dt =100A /µs                       |       | 520  |      | nC      |  |  |  |

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



#### **Typical Characteristics** $T_J = 25^{\circ}$ C, unless otherwise noted



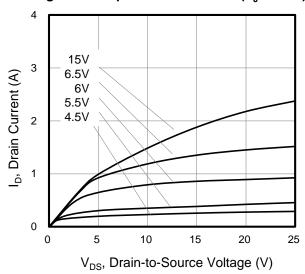


Figure 3. Drain Current vs. Temperature

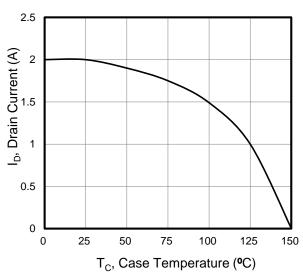


Figure 5. Transfer Characteristics

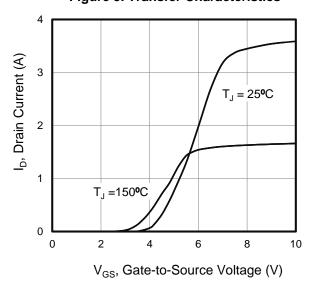
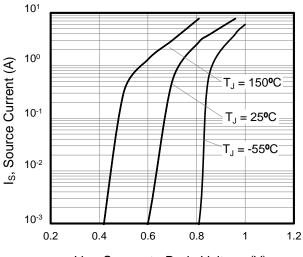


Figure 2. Body Diode Forward Voltage



V<sub>SD</sub>, Source-to-Drain Voltage (V)

Figure 4. Power Dissipation vs. Temperature TO-251,TO-252

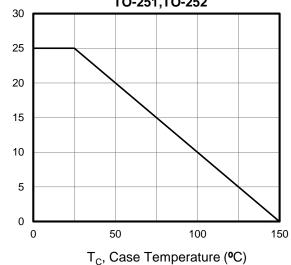
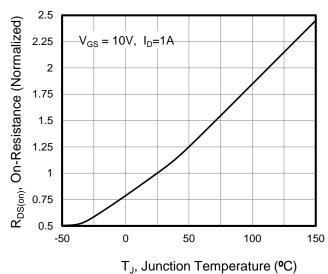


Figure 6. On-Resistance vs. Temperature



P<sub>D</sub>, Power Dissipation (w)



### **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted



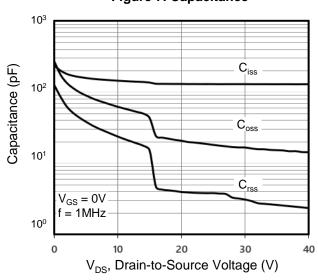


Figure 8. Gate Charge

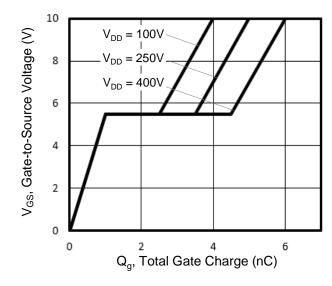


Figure 9. Transient Thermal Impedance

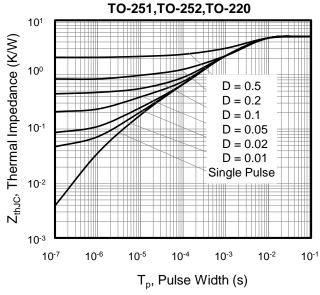


Figure 10. Transient Thermal Impedance TO-220F

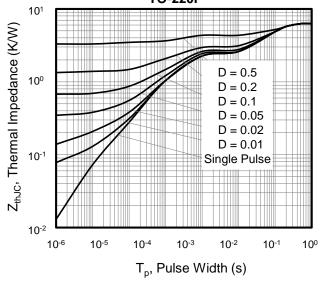




Figure A: Gate Charge Test Circuit and Waveform

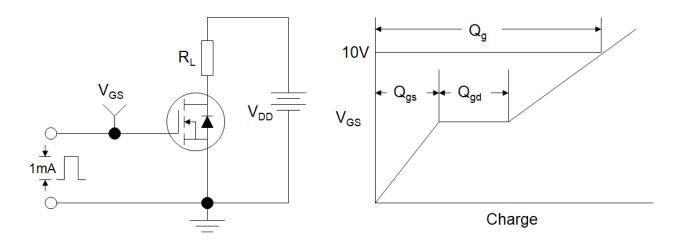


Figure B: Resistive Switching Test Circuit and Waveform

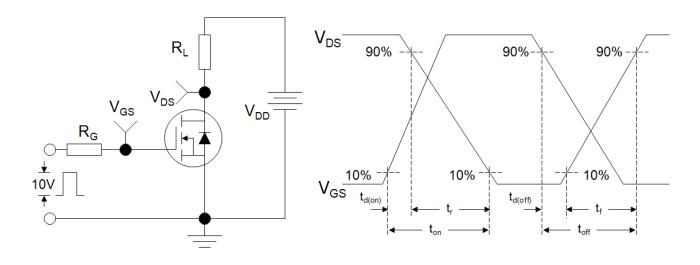
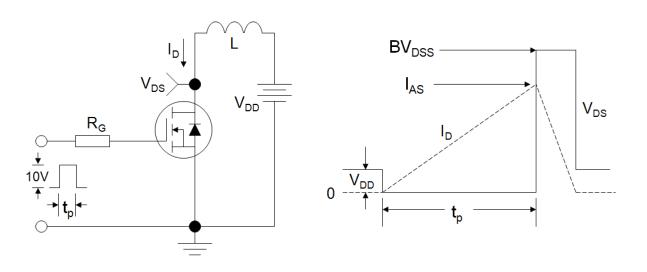
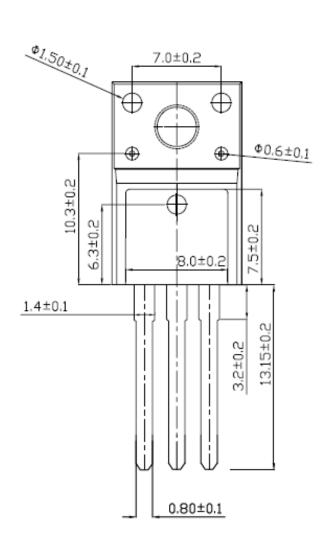


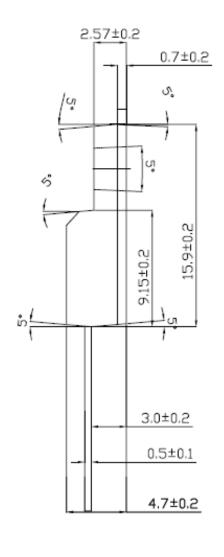
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





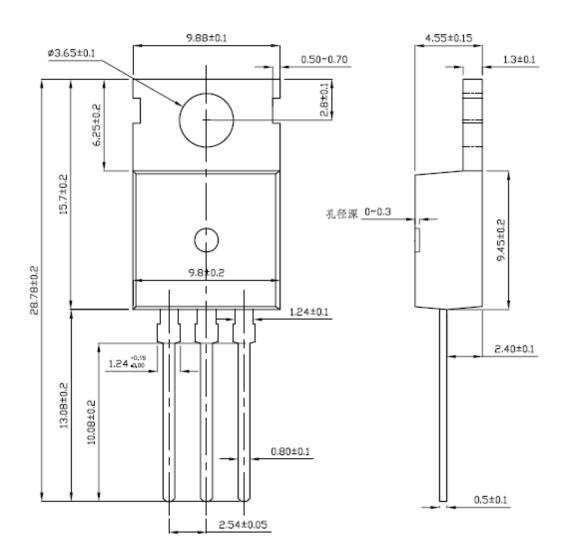
# **TO-220F**





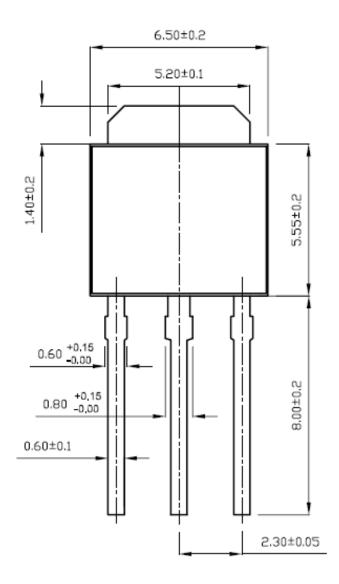


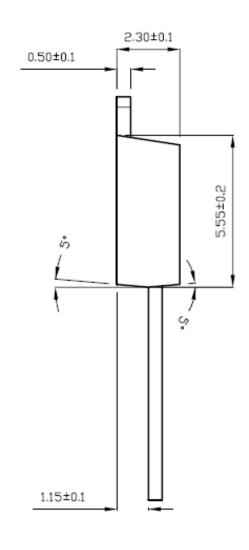
## **TO-220**





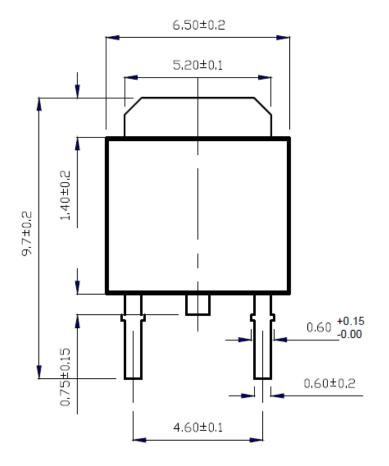
# TO-251

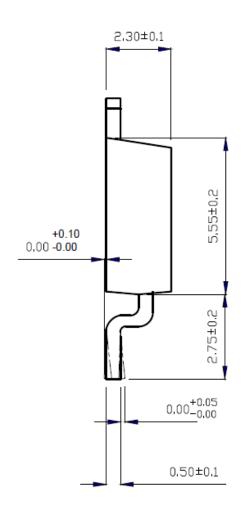






# **TO-252**





# **CS2N50DF, CS2N50DP, CS2N50DD, CS2N50DU**

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