

900V N-Channel MOSFET

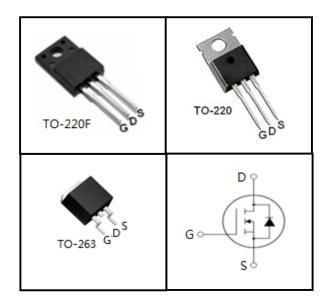
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package	Marking		
CS2N90F	TO-220F	CS2N90F		
CS2N90P	TO-220	CS2N90P		
CS2N90B	TO-263	CS2N90B		



Absolute Maximum Ratings $T_c = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol		Unit			
Farameter		TO-220F	TO-220	TO-263	Onit	
Drain-Source Voltage ($V_{GS} = 0V$)	V _{DSS}	900		V		
Continuous Drain Current	I _D		2		А	
Pulsed Drain Current (note1)	I _{DM}	8			A	
Gate-Source Voltage	V _{GSS}		±30		V	
Single Pulse Avalanche Energy (note2)	E _{AS}	45			mJ	
Avalanche Current (note1)	I _{AS}	3		А		
Repetitive Avalanche Energy (note1)	E _{AR}	27		mJ		
Power Dissipation ($T_c = 25^{\circ}C$)	P _D	25		70	W	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150		°C		

Thermal Resistance					
Parameter	Symbol	Value			Unit
		TO-220F	TO-220	TO-263	- Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	5	1.78		K/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		N/ VV



Specifications $T_J = 25^{\circ}C$, unless otherwise noted								
Parameter	Sum hal	Test Conditions	Value			11:4		
Parameter	ameter Symbol Test Conditions		Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	900			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 900V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1			
		$V_{DS} = 720V, V_{GS} = 0V, T_{J} = 125^{\circ}C$			100	μA)		
Gate-Source Leakage	I _{GSS}	V_{GS} = $\pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V _{GS} = 10V, I _D =1A		5	6	Ω		
Dynamic								
Input Capacitance	C _{iss}	$\gamma = 0 \gamma$		424		pF		
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		46				
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		8				
Total Gate Charge	Q_{g}			16		nC		
Gate-Source Charge	Q_gs	$V_{DD} = 720V, I_D = 2A, V_{GS} = 10V$		6				
Gate-Drain Charge	Q_{gd}			2				
Turn-on Delay Time	t _{d(on)}			35				
Turn-on Rise Time	t _r	V _{DD} = 450V, I _D = 2A,		12		ns		
Turn-off Delay Time	t _{d(off)}	$R_{G} = 25 \Omega$		102				
Turn-off Fall Time	t _f			41				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	۱ _s	T _C = 25 ℃			2	A		
Pulsed Diode Forward Current	I _{SM}	1 _C = 20 °C			8			
Body Diode Voltage	V_{SD}	$T_{\rm J} = 25^{\rm o}$ C, $I_{\rm SD} = 1$ A, $V_{\rm GS} = 0$ V			1.4	V		
Reverse Recovery Time	t _{rr}	V _{GS} = 0V,I _S = 2A,		397		ns		
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		0.5		μC		

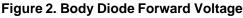
Notes

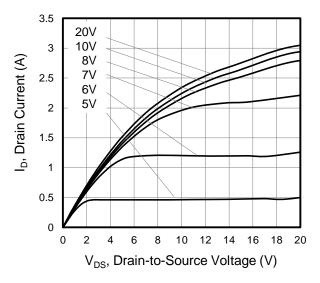
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 °C
- 3. Pulse Test: Pulse width \leq 300µs, Duty Cycle \leq 1%



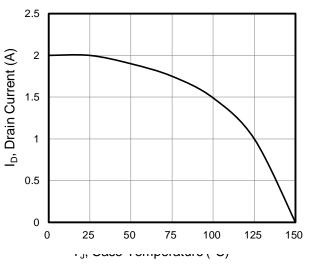
Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

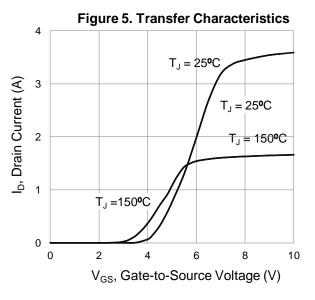
Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)











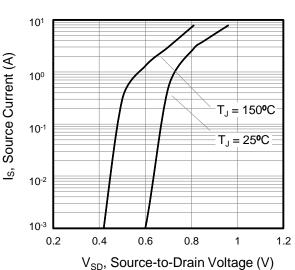


Figure 4. BV_{DSS} Variation vs. Temperature

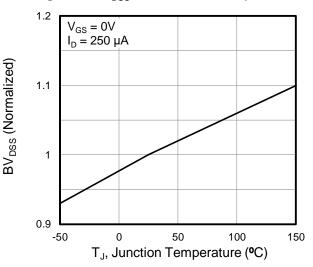
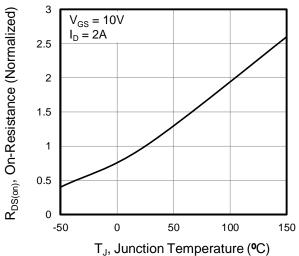


Figure 6. On-Resistance vs. Temperature





Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

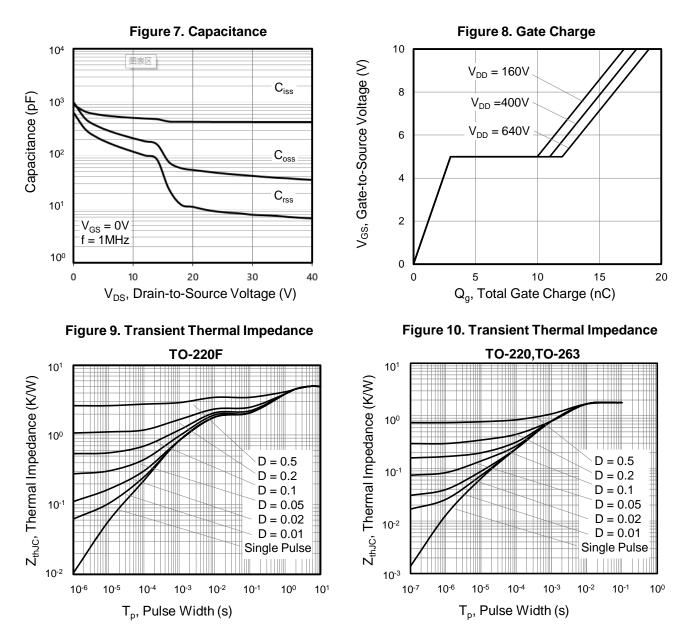




Figure A: Gate Charge Test Circuit and Waveform

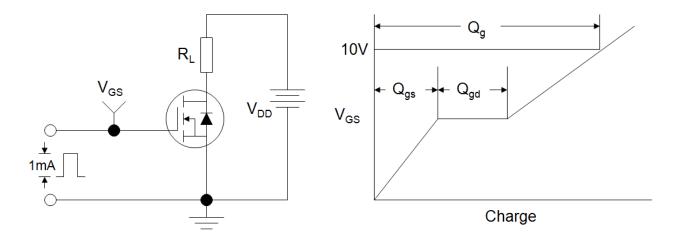


Figure B: Resistive Switching Test Circuit and Waveform

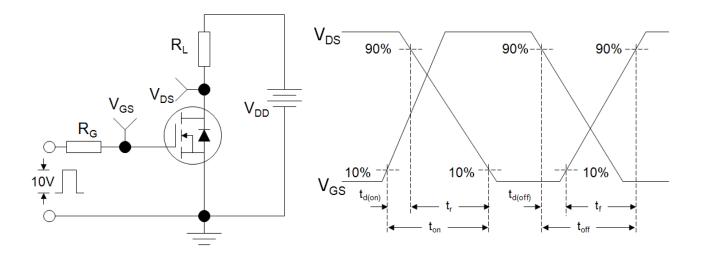
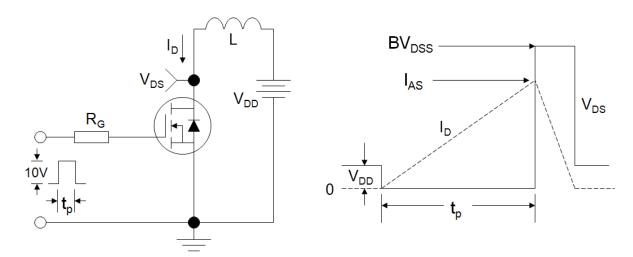
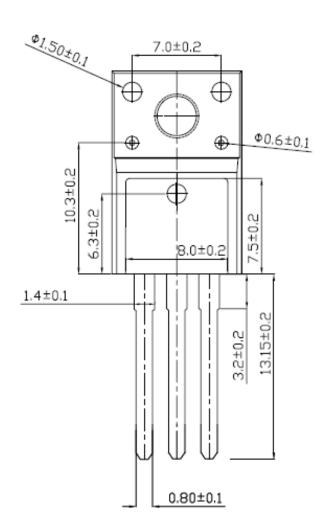


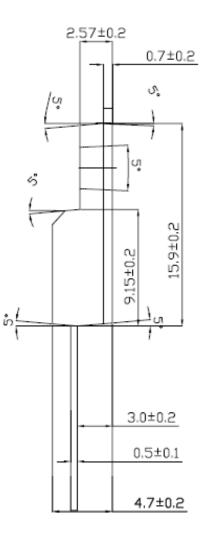
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





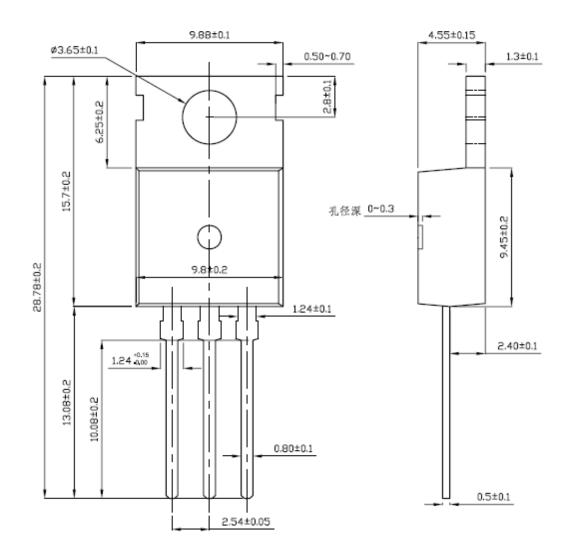
TO-220F





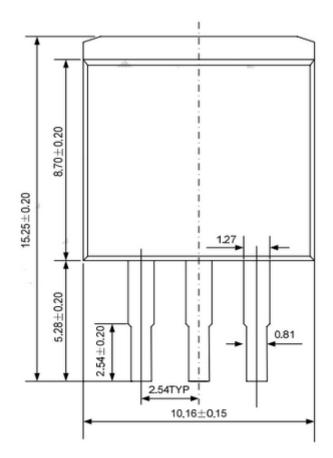


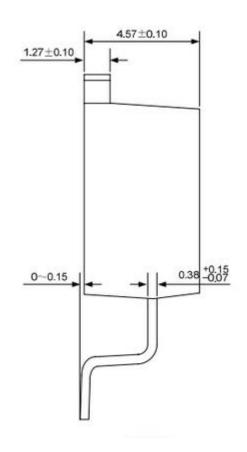
TO-220





TO-263







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