

# 1500V N-Channel MOSFET

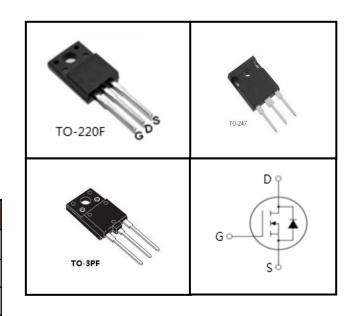
#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package	Marking		
CS3N150F	TO-220F	CS3N150F		
CS3N150W	TO-247	CS3N150W		
CS3N150VF	TO-3PF	CS3N150VF		



<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted					
Doromotor	Symbol	Value			11
Parameter		TO-220F	TO-247	TO-3PF	Unit
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>		1500	•	V
Continuous Drain Current	I <sub>D</sub>	3			Α
Pulsed Drain Current (note1)	I <sub>DM</sub>	12		Α	
Gate-Source Voltage	V <sub>GSS</sub>	±20		V	
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	88.2		mJ	
Avalanche Current (note1)	I <sub>AR</sub>	4.2		А	
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	52.9		mJ	
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	160 63		W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150			°C

Thermal Resistance						
Baramatar	Comple ed	Value			11	
Parameter	Symbol TO-220F		TO-247	TO-3PF	Unit	
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	0.78	2		IZ A A I	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	50		K/W	

<b>Specifications</b> $T_J = 25^{\circ}C$ , unless otherwise noted							
Parameter	Symbol Test	Test Conditions	Value			Unit	
raiailletei	Зуппоот	rest conditions	Min.	Тур.	Max.	Oilit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	1500			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 1500V, V_{GS} = 0V, T_{J} = 25^{\circ}C$	-		1	μA	
Gate-Source Leakage	$I_{\mathrm{GSS}}$	$V_{GS} = \pm 20V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		5.0	V	
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 1.5A$	1	6	7.2	Ω	
Dynamic							
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0V$ ,		1348			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 25V$ ,		101		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.0MHz		15			
Total Gate Charge	$Q_g$			54.5			
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 1200V, I_{D} = 3.0A,$ $V_{GS} = 10V$		6.4		nC	
Gate-Drain Charge	$Q_{gd}$	93		31.5			
Turn-on Delay Time	t <sub>d(on)</sub>			45			
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 750V, I_{D} = 3.0A,$		22.5			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		224		ns	
Turn-off Fall Time	t <sub>f</sub>			55.5			
Drain-Source Body Diode Characteristics							
Continuous Body Diode Current	I <sub>s</sub>	T 05.00			3		
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			12	Α	
Body Diode Voltage	$V_{SD}$	$T_J = 25^{\circ}\text{C}, I_{SD} = 1.5\text{A}, V_{GS} = 0\text{V}$			1.4	V	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 3.0A,$		647.5		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /μs		0.98		μC	

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^{\circ}C$ )

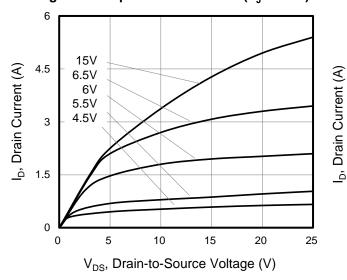


Figure 2. Forward Bias Safe Operating Area

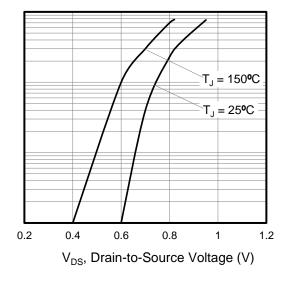


Figure 3. Drain Current vs. Temperature

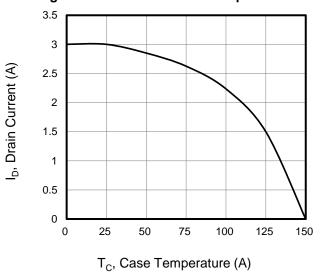


Figure 4. Power Dissipation vs. Temperature

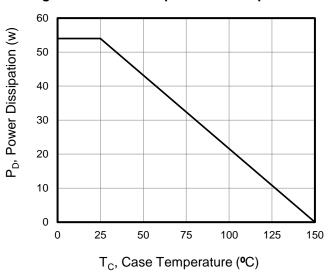


Figure 5. Transfer Characteristics

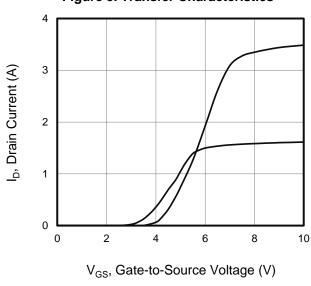
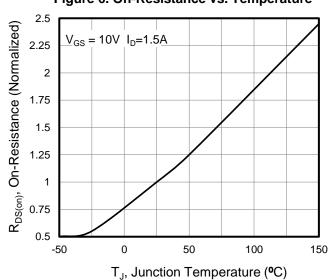


Figure 6. On-Resistance vs. Temperature



### **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

V<sub>GS</sub>, Gate-to-Source Voltage (V)

Figure 7. Capacitance

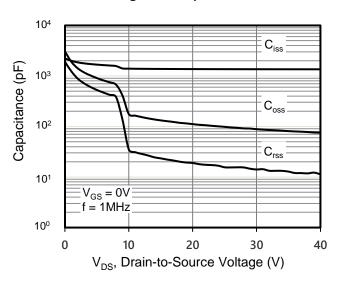


Figure 9. Transient Thermal Impedance TO-247,TO-3PF

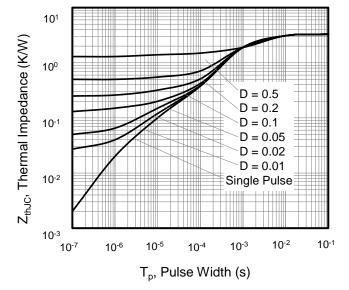


Figure 8. Gate Charge

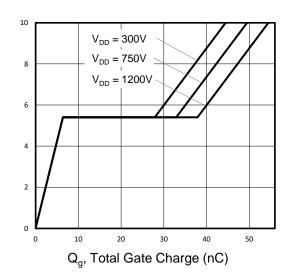


Figure 10. Transient Thermal Impedance TO-220F

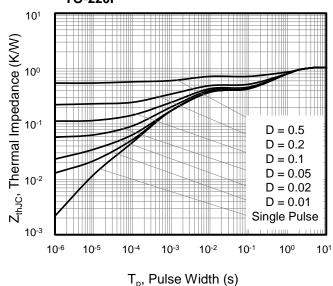


Figure A: Gate Charge Test Circuit and Waveform

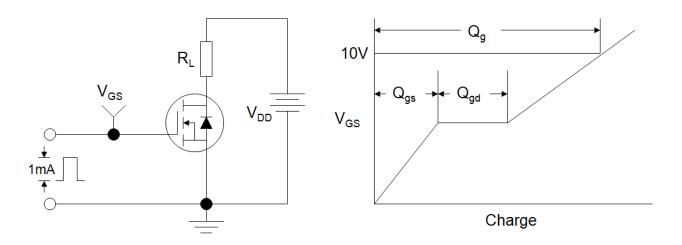


Figure B: Resistive Switching Test Circuit and Waveform

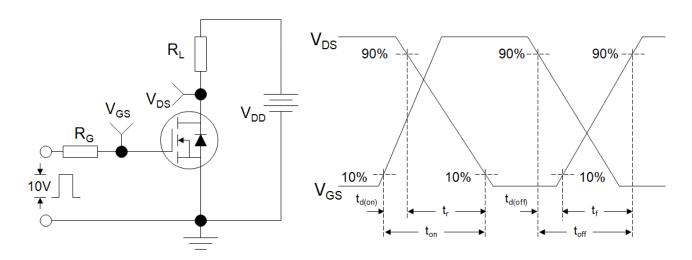
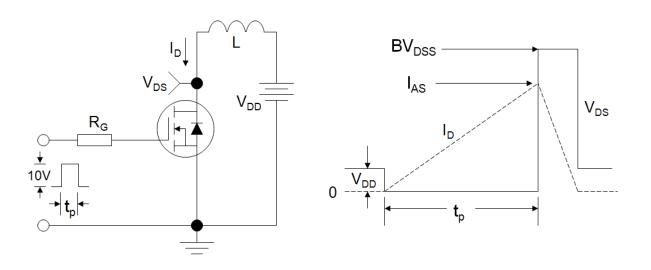
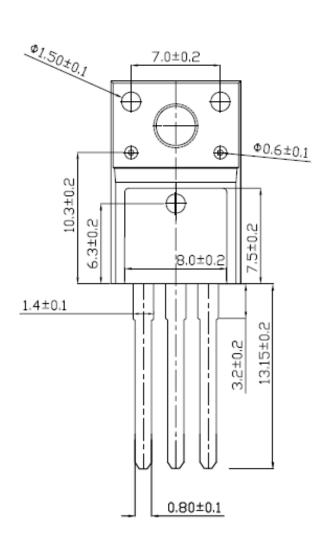


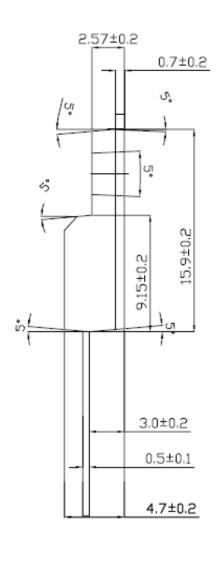
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





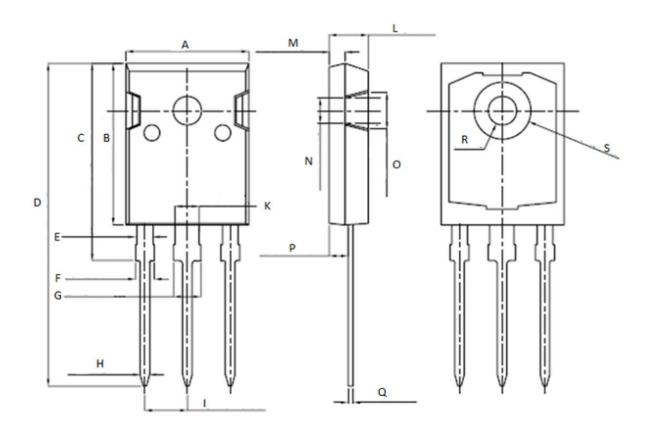
## **TO-220F**







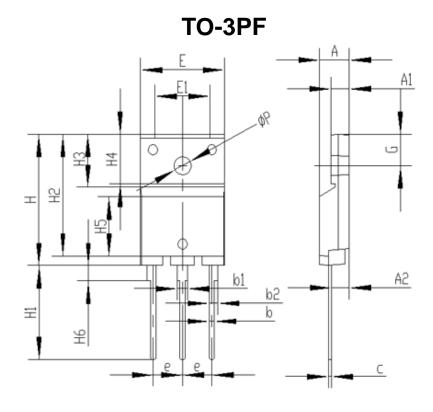
# **TO-247**



Unit: mm				
Symbol	Min.	Max.		
Α	15. 95	16. 25		
В	20. 85	21. 25		
С	20. 95	21. 35		
D	40. 5	40. 9		
E	1. 9	2. 1		
F	2. 1	2. 25		
G	3. 1	3. 25		
Н	1.1	1. 3		
I	5. 40	5. 50		

Unit: mm				
Symbol	Min.	Max.		
K	2. 90	3. 10		
L	4. 90	5. 30		
M	1. 90	2. 10		
N	4. 50	4. 70		
0	5. 40	5. 60		
Р	2. 29	2. 49		
Q	0. 51	0. 71		
R	ф 3. 5	ф 3. 7		
S	ф 7. 1	ф 7. 3		







Symbol	单位 mm				
Symbol	Min	Nom	Max		
Α	5. 30	5. 50	5. 70		
A1	3. 30	3. 50	3. 70		
A2	3. 20	3. 40	3.60		
b	0.80	1.0	1. 20		
b1	1.80	2.00	2. 20		
<b>b</b> 2	1.40	1.60	1.80		
С	0.40	0. 50	0.60		
е	5. 25	5. 45	5. 65		
E	15. 4	15. 6	15. 8		
E1	10.0	10.2	10.4		
Н	22.8	23. 0	23. 2		
H1	16. 0	16. 5	17. 0		
H2	21. 2	21. 4	21.6		
Н3	9. 10	9. 30	9. 50		
H4	8. 55	8. 75	8. 95		
H5	10. 2	10.4	10.6		
H6	2. 55	2. 70	2.85		
G	5. 3	5. 5	5. 7		
ΦР	3. 00	3. 20	3. 40		

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