900V N-Channel MOSFET

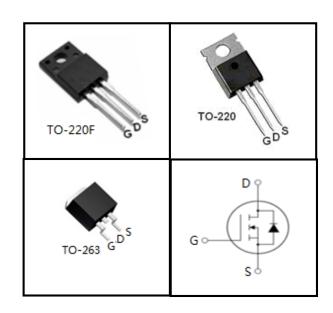
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	Package Marking			
CS3N90F	TO-220F	CS3N90F		
CS3N90P	TO-220	CS3N90P		
CS3N90B	TO-263	CS3N90B		



Absolute Maximum Ratings T _C = 25°C, unless otherwise noted						
Parameter	Symbol	Value			limit	
Farameter	Symbol	TO-220F	TO-220	TO-263	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	900		V		
Continuous Drain Current	I _D	3		Α		
Pulsed Drain Current (note1)	I _{DM}	12		А		
Gate-Source Voltage	V _{GSS}		±30		V	
Single Pulse Avalanche Energy (note2)	E _{AS}	65		mJ		
Avalanche Current (note1)	I _{AS}	3.6		Α		
Repetitive Avalanche Energy (note1)	E _{AR}	39		mJ		
Power Dissipation (T _C = 25°C)	P _D	25	7	0	W	
Operating Junction and Storage Temperature Range	T_J,T_stg	-55~+150		°C		

Thermal Resistance					
Bassastas	Symbol	Value			11
Parameter		TO-220F	TO-220	TO-263	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	5	1.78		K/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		rv vv



Denomation.		- . •	Value				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	900			>	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 900V, V _{GS} = 0V, T _J = 25°C			1	μA	
		$V_{DS} = 720V, V_{GS} = 0V, T_{J} = 125^{\circ}C$	1		1 400 1		
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 30V$			±100	nA	
Gate-Source Threshold Voltage	$V_{\rm GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V	
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_D = 1.5A$		3.6	4.8	Ω	
Dynamic							
Input Capacitance	C _{iss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		595		pF	
Output Capacitance	C _{oss}			56			
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		9			
Total Gate Charge	Q_g			21		nC	
Gate-Source Charge	Q_{gs}	$V_{DD} = 720V, I_{D} = 3A, V_{GS} = 10V$		3			
Gate-Drain Charge	Q_{gd}	- 63		10			
Turn-on Delay Time	t _{d(on)}			37			
Turn-on Rise Time	t _r	$V_{DD} = 450V, I_{D} = 3A,$		16			
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		112		ns	
Turn-off Fall Time	t _f			37			
Drain-Source Body Diode Character	istics						
Continuous Body Diode Current	I _S	T 05.00			3	^	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			12	A	
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 1.5\text{A}, V_{GS} = 0\text{V}$			1.4	V	
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 3A,$		754		ns	
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /µs		1.3		μC	

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

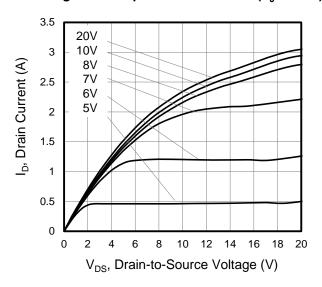


Figure 3. Drain Current vs. Temperature

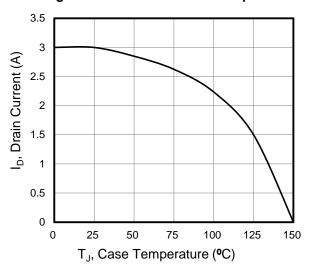


Figure 5. Transfer Characteristics

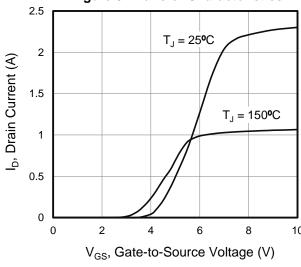


Figure 2. Body Diode Forward Voltage

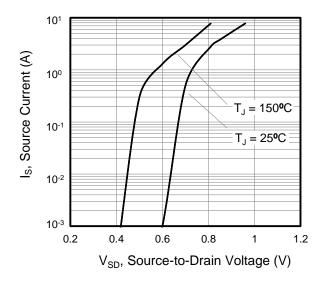


Figure 4. BV_{DSS} Variation vs. Temperature

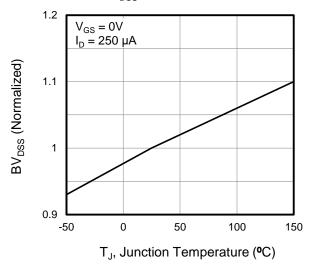
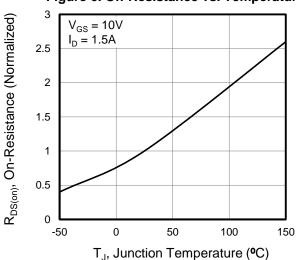


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

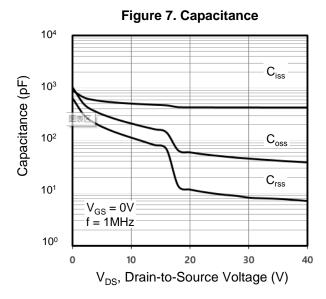


Figure 9. Transient Thermal Impedance

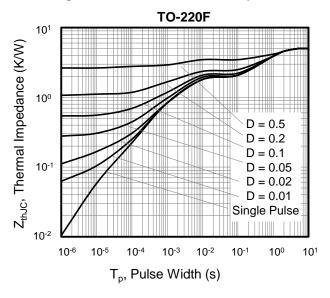


Figure 8. Gate Charge

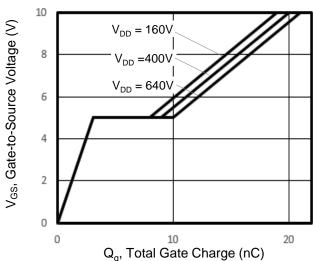


Figure 10. Transient Thermal Impedance

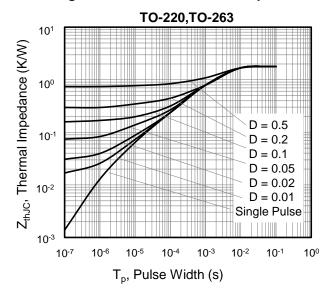


Figure A: Gate Charge Test Circuit and Waveform

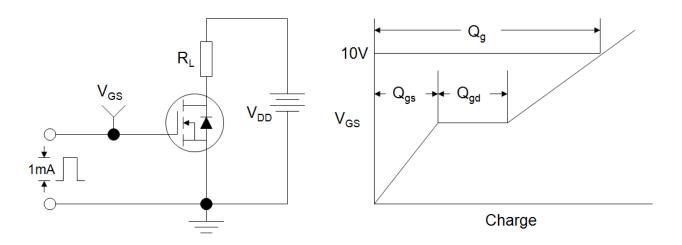


Figure B: Resistive Switching Test Circuit and Waveform

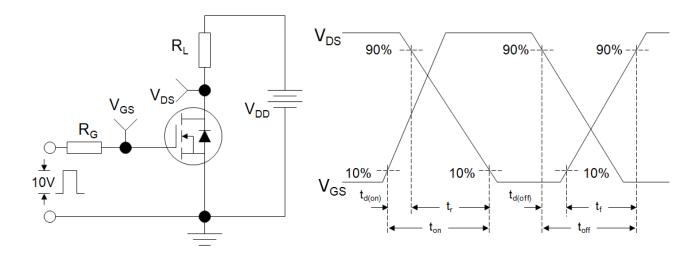
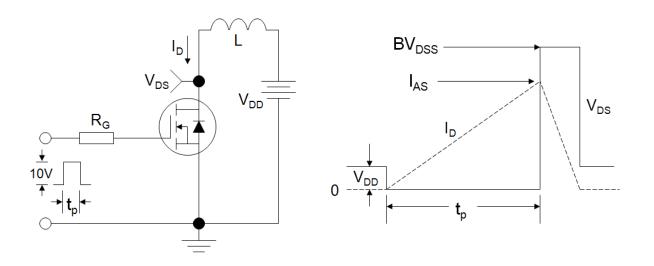
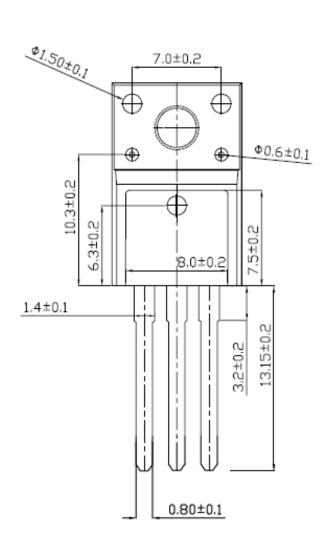


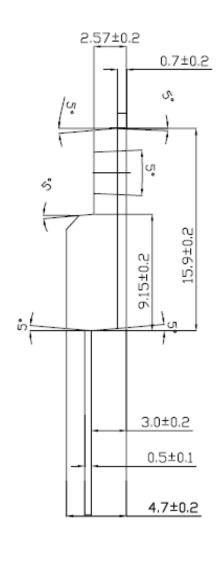
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





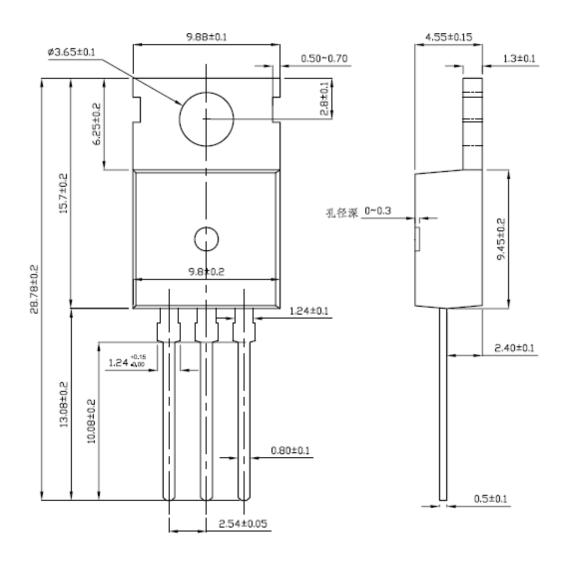
TO-220F





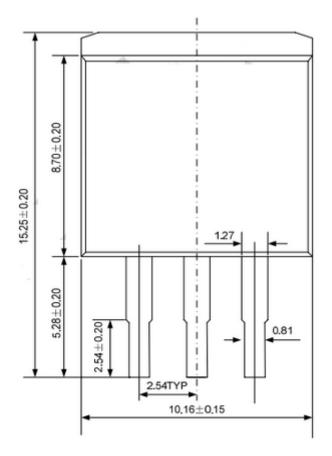


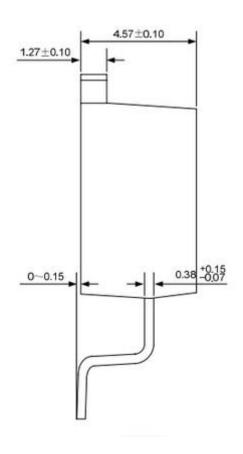
TO-220





TO-263







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