

## **1000V N-Channel MOSFET**

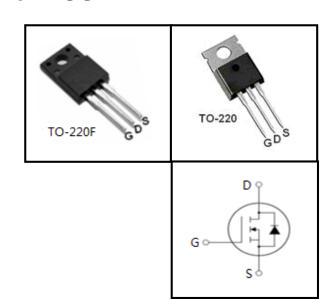
#### **FEATURES**

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information				
Device	evice Package			
CS5N100F	TO-220F	CS5N100F		
CS5N100P	TO-220	CS5N100P		



<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted					
Barranatar	Symbol	Val	11		
Parameter		TO-220F	TO-220	Unit	
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	1000		V	
Continuous Drain Current	I <sub>D</sub>	5		А	
Pulsed Drain Current (note1)	I <sub>DM</sub>	20		А	
Gate-Source Voltage	V <sub>GSS</sub>	±30		V	
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	115.2		mJ	
Avalanche Current (note1)	I <sub>AS</sub>	4.8		А	
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	69.1		mJ	
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	25	70	W	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150		°C	

Thermal Resistance				
Bassastas	Symbol	Va	l lmit	
Parameter		TO-220F	TO-220	Unit
Thermal Resistance, Junction-to-Case	R <sub>thJC</sub>	5	1.78	17/\\
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5	60	K/W

# CS5N100F, CS5N100P

<b>Specifications</b> T <sub>J</sub> = 25°C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Value			Unit		
	Gyilliboi	rest conditions	Min.	Тур.	Max.	O I III		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	1000			٧		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 1000V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_D = 2.5A$		2.1	2.5	Ω		
Dynamic								
Input Capacitance	C <sub>iss</sub>	V - 0V		1152		pF		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$ $f = 1.0MHz$		110				
Reverse Transfer Capacitance	C <sub>rss</sub>			23				
Total Gate Charge	$Q_g$	$V_{DD} = 800V, I_{D} = 5A,$ $V_{GS} = 10V$		47		nC		
Gate-Source Charge	$Q_{gs}$			4.8				
Gate-Drain Charge	$Q_{gd}$	55		24				
Turn-on Delay Time	t <sub>d(on)</sub>			43		ns		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 500V, I_{D} = 5A,$		20				
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$		230				
Turn-off Fall Time	t <sub>f</sub>			47				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	I <sub>S</sub>	T 0500			5	Α		
Pulsed Diode Forward Current	I <sub>SM</sub>	T <sub>C</sub> = 25 °C			20			
Body Diode Voltage	V <sub>SD</sub>	$T_J = 25^{\circ}\text{C}, I_{SD} = 2.5\text{A}, V_{GS} = 0\text{V}$			1.4	V		
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 5A,$		628		ns		
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>F</sub> /dt =100A /µs		1.4		μC		

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25  $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics ( $T_J = 25^{\circ}C$ )

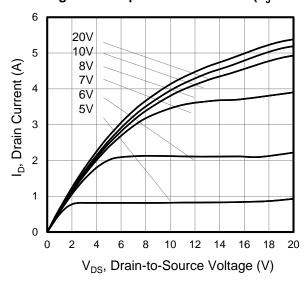


Figure 2. Body Diode Forward Voltage

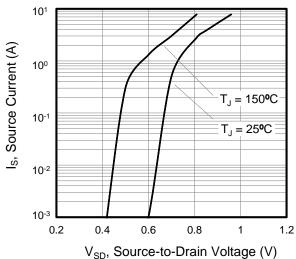


Figure 3. Drain Current vs. Temperature

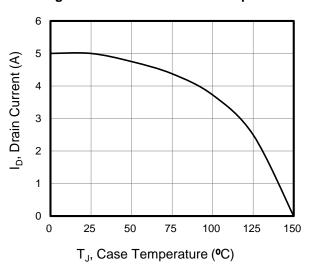


Figure 5. Transfer Characteristics

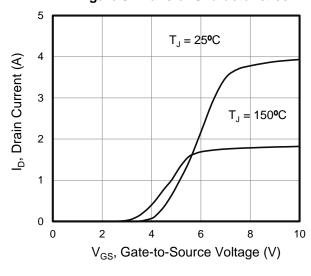


Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

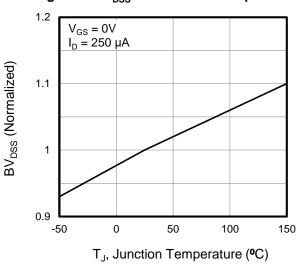
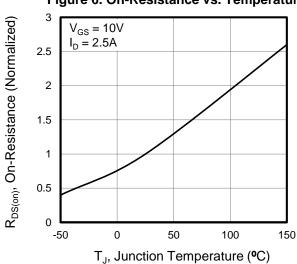


Figure 6. On-Resistance vs. Temperature





## **Typical Characteristics** $T_J = 25^{\circ}C$ , unless otherwise noted

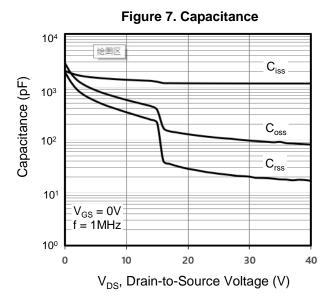


Figure 9. Transient Thermal Impedance

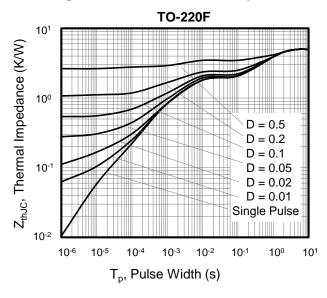


Figure 10. Transient Thermal Impedance

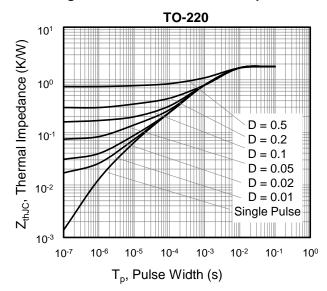




Figure A: Gate Charge Test Circuit and Waveform

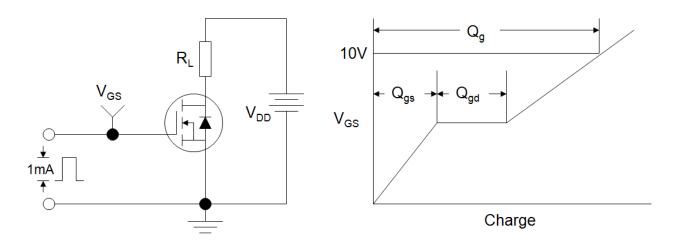


Figure B: Resistive Switching Test Circuit and Waveform

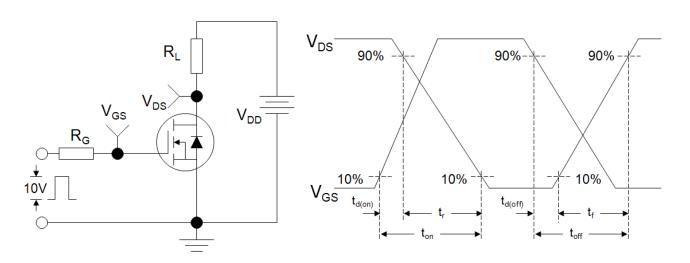
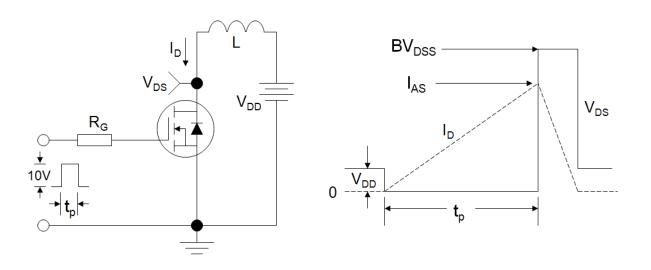
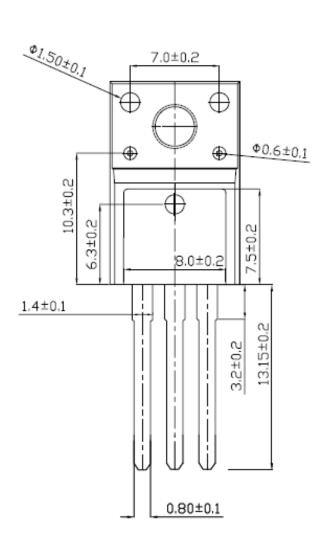


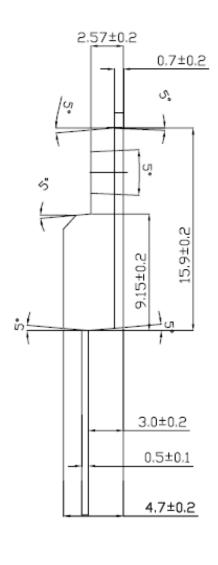
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





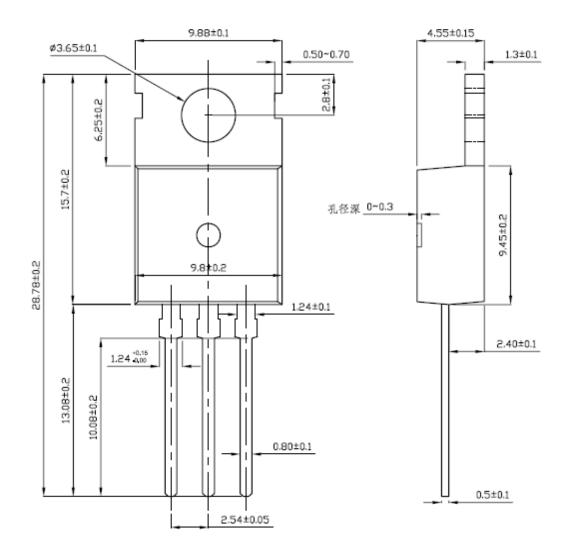
## **TO-220F**







# **TO-220**





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